

# Ajith Pasqual

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/2264527/publications.pdf>

Version: 2024-02-01

24  
papers

136  
citations

2258059

3  
h-index

2272923

4  
g-index

24  
all docs

24  
docs citations

24  
times ranked

149  
citing authors

#	ARTICLE	IF	CITATIONS
1	4K Real-Time HEVC Decoder on an FPGA. IEEE Transactions on Circuits and Systems for Video Technology, 2016, 26, 236-249.	8.3	40
2	Real time all intra HEVC HD encoder on FPGA. , 2016, , .		13
3	Online classification of imagined hand movement using a consumer grade EEG device. , 2013, , .		10
4	EdgeNet: SqueezeNet like Convolution Neural Network on Embedded FPGA. , 2018, , .		10
5	FPGA-based compact and flexible architecture for real-time embedded vision systems. , 2009, , .		9
6	High performance parallel packet Classification architecture with Popular Rule Caching. , 2012, , .		7
7	A Four-Step Method to Synthesize a DC-DC Converter for Multi-Inductor Realizable Arbitrary Voltage Conversion Ratio. IEEE Transactions on Industrial Electronics, 2022, 69, 5594-5603.	7.9	6
8	HLS approach in designing FPGA-based custom coprocessor for image preprocessing. , 2010, , .		5
9	Runtime rule-reconfigurable high throughput NIPS on FPGA. , 2017, , .		5
10	Reconfigurable co-processor architecture with limited numerical precision to accelerate deep convolutional neural networks. , 2018, , .		5
11	Nonisolated DC-DC Power Converter Synthesis Using Low-Entropy Equations. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2022, 10, 6457-6469.	5.4	5
12	Impact of ICT on learning and teaching. , 2008, , .		4
13	A Novel Low-Complexity VLSI Architecture for an EEG Feature Extraction Platform. , 2018, , .		4
14	High performance flow matching architecture for OpenFlow data plane. , 2016, , .		3
15	An Analytical Method to Derive a DC-DC Converter for an Arbitrary Voltage Conversion Ratio. , 2018, , .		3
16	SMPTE ST 2110 Compliant Scalable Architecture on FPGA for end to end Uncompressed Professional Video Transport Over IP Networks. , 2019, , .		2
17	Scalable FPGA-based multiprocessor architecture for real-time embedded vision. , 2010, , .		1
18	Line rate parallel packet classification module for NetFPGA platform. , 2013, , .		1

#	ARTICLE	IF	CITATIONS
19	FPGA based custom accelerator architecture framework for complex event processing. , 2014, , .		1
20	FPAA and FPGA based universal sensor node design. , 2017, , .		1
21	A Design Methodology to Synthesize First Degree Single-Path Hybrid DC-DC Converters. IEEE Transactions on Power Electronics, 2022, 37, 12336-12345.	7.9	1
22	HEVC inverse transform architecture utilizing coefficient sparsity. , 2015, , .		0
23	Micro Actions and Deep Static Features for Activity Recognition. , 2017, , .		0
24	VLIW Based Runtime Reconfigurable Machine Vision Coprocessor Architecture for Edge Computing. , 2019, , .		0