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List of Publications by Year in descending order

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docs citations

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citing authors

#	ARTICLE	IF	CITATIONS
1	All-digital delay line-based time difference amplifier in 65-nm CMOS technology. IET Circuits, Devices and Systems, 2019, 13, 421-427.	1.4	0
2	A novel ALL-digital low power delay lines based time difference amplifier for coarse-fine TDC. Analog Integrated Circuits and Signal Processing, 2019, 98, 137-146.	1.4	0
3	Low-power, latch-based multistage time-to-digital converter in 65-nm CMOS technology. International Journal of Circuit Theory and Applications, 2018, 46, 1264-1271.	2.0	4
4	New Design of Scan Flip-Flop to Increase Speed and Reduce Power Consumption. Journal of Circuits, Systems and Computers, 2015, 24, 1550159.	1.5	2