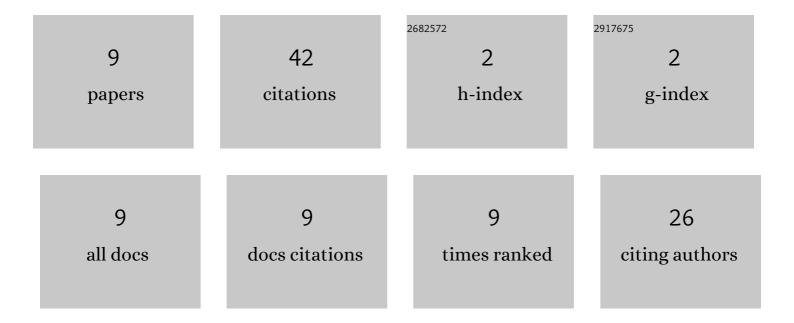
Imad Benacer

List of Publications by Year in descending order

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IMAD RENACED

| # | Article | IF | CITATIONS |
|---|--|-----|-----------|
| 1 | HPQS: A Fast, High-Capacity, Hybrid Priority Queuing System for High-Speed Networking Devices. IEEE Access, 2019, 7, 130672-130684. | 4.2 | 0 |
| 2 | A High-Speed, Scalable, and Programmable Traffic Manager Architecture for Flow-Based Networking. IEEE Access, 2019, 7, 2231-2243. | 4.2 | 2 |
| 3 | HPQ: A High Capacity Hybrid Priority Queue Architecture for High-Speed Network Switches. , 2018, , . | | 4 |
| 4 | A Fast, Single-Instruction–Multiple-Data, Scalable Priority Queue. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 1939-1952. | 3.1 | 6 |
| 5 | Design of a Low Latency 40 Gb/s Flow-Based Traffic Manager Using High-Level Synthesis. , 2018, , . | | 3 |
| 6 | A high-speed traffic manager architecture for flow-based networking. , 2017, , . | | 8 |
| 7 | A fast systolic priority queue architecture for a flow-based Traffic Manager. , 2016, , . | | 11 |
| 8 | A novel stereovision algorithm for obstacles detection based on U-V-disparity approach. , 2015, , . | | 8 |
| 9 | Hardware design and FPGA implementation for road plane extraction based on V-disparity approach. , 2015, , . | | 0 |