Imad Benacer

List of Publications by Year
in descending order

		2682572	2917675
9 papers	**42** citations	**2** h-index	**2** g-index
9 all docs	**9** docs citations	**9** times ranked	**26** citing authors

#	Article	IF	Citations
1	A fast systolic priority queue architecture for a flow-based Traffic Manager. , 2016, , .		11
2	A novel stereovision algorithm for obstacles detection based on U-V-disparity approach. , 2015, , .		8
3	A high-speed traffic manager architecture for flow-based networking. , 2017, , .		8
4	A Fast, Single-Instruction–Multiple-Data, Scalable Priority Queue. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 1939-1952.	3.1	6
5	HPQ: A High Capacity Hybrid Priority Queue Architecture for High-Speed Network Switches. , 2018, , .		4
6	Design of a Low Latency 40 Gb/s Flow-Based Traffic Manager Using High-Level Synthesis. , 2018, , .		3
7	A High-Speed, Scalable, and Programmable Traffic Manager Architecture for Flow-Based Networking. IEEE Access, 2019, 7, 2231-2243.	4.2	2
8	Hardware design and FPGA implementation for road plane extraction based on V-disparity approach. , 2015, , .		0
9	HPQS: A Fast, High-Capacity, Hybrid Priority Queuing System for High-Speed Networking Devices. IEEE Access, 2019, 7, 130672-130684.	4.2	0