Eugene B John

List of Publications by Year
in descending order

52 papers	175 citations	1937685 4 h-index
52 all docs	52 docs citations	52 times ranked

1588992
8
g-index

172
citing authors

#	Article	IF	Citations
1	Improving the Accuracy of Bluetooth Low Energy Indoor Positioning System Using Kalman Filtering. , 2016, , .		23
2	Design and Implementation of an Ultralow-Energy FFT ASIC for Processing ECG in Cardiac Pacemakers. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 983-987.	3.1	18
3	A quasi-power-gated low-leakage stable SRAM cell. , 2010, , .		9
4	A Tale of Two Processors: Revisiting the RISC-CISC Debate. Lecture Notes in Computer Science, 2009, , 57-76.	1.3	9
5	A comparative analysis of coarse-grain and fine-grain power gating for FPGA lookup tables. , 2009, , .		8
6	Analysis of Memory Sensitive SPEC CPU2006 Integer Benchmarks for Big Data Benchmarking. , 2015, , .		8
7	Resource Shared Galois Field Computation for Energy Efficient AES/CRC in IoT Applications. IEEE Transactions on Sustainable Computing, 2019, 4, 340-348.	3.1	8
8	Design and implementation of a CMOS non-restoring divider. , 2006, , .		7
9	Static noise margin and power dissipation analysis of various SRAM topologies. , 2013, , .		6
10	FlowPaP and FlowReR. Transactions on Embedded Computing Systems, 2017, 16, 1-20.	2.9	6
11	Building trust in 3PIP using asset-based security property verification. , 2016, , .		5
12	Performance of commercial multimedia workloads on the Intel Pentium 4: A case study. Computers and Electrical Engineering, 2009, 35, 18-32.	4.8	4
13	Leakage control in full adders with selectively stacked inverters. , 2010, , .		4
14	Leakage and delay analysis in FinFET array multiplier circuits. , 2014, , .		4
15	Data retention voltage analysis of various low-power SRAM topologies. , 2014, , .		4
16	Using Static Hardware Wrappers to Thwart Hardware Trojans and Code Bugs at Runtime. , 2018, , .		4
17	Wasted dynamic power and correlation to instruction set architecture for CPU throttling. Journal of Supercomputing, 2019, 75, 2436-2454.	3.6	4
18	Reducing Power and Cycle Requirement for Fast Fourier Transform of Electrocardiogram Signals Through Low Level Arithmetic Optimizations for Cardiac Implantable Devices. Journal of Low Power Electronics, 2016, 12, 21-29.	0.6	4

#	Article	IF	Citations
19	Demystifying the MLPerf Training Benchmark Suite. , 2020, , .		4
20	Leakage current control of nano-scale full adder cells using input vectors. , 2007, , .		3
21	Topology selection of FPGA look-up tables for low-leakage operation. , 2009, , .		3
22	RTL Level Instruction Profiling for CPU Throttling to Reduce Wasted Dynamic Power. , 2017, , .		3
23	Performance Analysis of Single-Precision Floating-Point MAC for Deep Learning. , 2018, , .		3
24	Impact of Operating System Behavior on Battery Life. Journal of Low Power Electronics, 2010, 6, 10-17.	0.6	3
25	Fingerprint verification in multimodal biometrics. , 2006, , .		2
26	A Study of Core Utilization and Residency in Heterogeneous Smart Phone Architectures. , 2019, , .		2
27	Low-Power Advanced Encryption Standard for Implantable Cardiac Devices. , 2019, , .		2
28	An Ultra-Low Power Charge Redistribution Successive Approximation Register A/D Converter for Biomedical Applications. Journal of Low Power Electronics, 2016, 12, 385-393.	0.6	2
29	A Thermal-Aware Scheduling Algorithm for Core Migration in Multicore Processors. Journal of Low Power Electronics, 2015, 11, 103-111.	0.6	2
30	On estimation of static power-peformance in TCAM. , 2008, , .		1
31	Implications of gated-Vss technique on leakage power in embedded caches. International Journal of Embedded Systems, 2009, 4, 17.	0.3	1
32	Adaptive instruction dispatching techniques for Simultaneous Multi-Threading (SMT) processors. Computers and Electrical Engineering, 2012, 38, 1616-1626.	4.8	1
33	Performance enhancement in shared-memory multiprocessors using dynamically classified sharing information. , 2014, , .		1
34	A quantitative performance analysis of FinFET based multiplier circuits. , 2016, , .		1
35	Enhancing Trojan Detection by Finding LTL and Taint Properties in RTL Circuit Designs: A Case Study. , 2017, , .		1
36	Instruction Profiling Based Fetch Throttling for Wasted Dynamic Power Reduction. , 2019, , .		1

#	Article	IF	Citations
37	Sequential Methods for Detecting a Change in the Distribution of an Episodic Process. , 2020, , .		1
38	Evaluating Cloud Auto-Scaler Resource Allocation Planning Under High-Performance Computing Workloads. , 2020, , .		1
39	Performance Tradeoffs in the Design of Low-Power SRAM Arrays for Implantable Devices. Journal of Low Power Electronics, 2018, 14, 18-27.	0.6	1
40	FPGA Implementation of MobileNetV2 CNN Model Using Semi-Streaming Architecture for Low Power Inference Applications. , 2020, , .		1
41	Effect of input pulse shape on FSK optical coherent communication system. , 1991, , .		0
42	Design and VLSI implementation of an access processor for a decoupled architecture. Microprocessors and Microsystems, 1992, 16, 237-247.	2.8	0
43	A new design approach for a highâ€transimpedance and lowâ€noise optoelectronic integrated lightwave receiver with 24â€GHz bandwidth. Microwave and Optical Technology Letters, 1994, 7, 259-262.	1.4	0
44	Analysing the performance of personal computers based on Intel microprocessors for sequence aligning bioinformatics applications. International Journal of Bioinformatics Research and Applications, 2007, 3, 187.	0.2	0
45	A superscalar simulation employing poisson distributed stalls. Computers and Electrical Engineering, 2008, 34, 192-201.	4.8	0
46	Energy efficient lossless image compression with prediction-based transform. , 2008, , .		0
47	Topology-related effects of Gated-V$_{dd}$ and Gated-V$_{ss}$ techniques on full-adder leakage and delay at 65nm and 45 nm. , 2008, , .		0
48	Use of increased transistor gate length for leakage reduction in caches. , 2010, , .		0
49	Reducing thermal hotspots in microprocessors with expanded component sizing. , 2012, , .		0
50	Performance-sensitivity and performance-similarity based workload reduction. , 2012, , .		0
51	A metric for measuring power efficiency and data throughput in mobile ad hoc networks. International Journal of Parallel, Emergent and Distributed Systems, 2019, 34, 556-571.	1.0	0
52	Robust Quickest Change Detection in Statistically Periodic Processes. , 2021, , .		0