

# RÃ³bert LÃ³rencz

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/212612/publications.pdf>

Version: 2024-02-01

23  
papers

164  
citations

1937685

4  
h-index

1588992

8  
g-index

25  
all docs

25  
docs citations

25  
times ranked

109  
citing authors

#	ARTICLE	IF	CITATIONS
1	True random number generator based on ring oscillator PUF circuit. Microprocessors and Microsystems, 2017, 53, 33-41.	2.8	30
2	Improved ring oscillator PUF on FPGA and its properties. Microprocessors and Microsystems, 2016, 47, 55-63.	2.8	29
3	A Design of Ring Oscillator Based PUF on FPGA. , 2015, , .		27
4	Using Power-Up SRAM State of Atmel ATmega1284P Microcontrollers as Physical Unclonable Function for Key Generation and Chip Identification. Information Security Journal, 2013, 22, 244-250.	1.9	11
5	True random number generation on an Atmel AVR microcontroller. , 2010, , .		10
6	Side-Channel Attack on the A5/1 Stream Cipher. , 2019, , .		8
7	System on chip design of a linear system solver. , 2014, , .		4
8	True Random Number Generator Based on ROPUF Circuit. , 2016, , .		4
9	Subtraction-free Almost Montgomery Inverse algorithm. Information Processing Letters, 2005, 94, 11-14.	0.6	3
10	Dedicated hardware implementation of a linear congruence solver in FPGA. , 2012, , .		3
11	Comparison of FPGA and ASIC Implementation of a Linear Congruence Solver. , 2013, , .		3
12	Proposal and Properties of Ring Oscillator-Based PUF on FPGA. Journal of Circuits, Systems and Computers, 2016, 25, 1640016.	1.5	3
13	Optimization of elliptic curve operations for ECM using double & add algorithm. , 2015, , .		2
14	Temperature Dependence of ROPUF on FPGA. , 2016, , .		2
15	Comparison of three counter value based ROPUFs on FPGA. , 2020, , .		2
16	Arithmetic Unit for Computations in GF(p) with the Left-Shifting Multiplicative Inverse Algorithm. Lecture Notes in Computer Science, 2013, , 268-279.	1.3	2
17	An ASIC linear congruence solver synthesized with three cell libraries. , 2014, , .		1
18	Linear cryptanalysis of Baby Rijndael. , 2015, , .		1

#	ARTICLE	IF	CITATIONS
19	Design of a Residue Number System Based Linear System Solver in Hardware. Journal of Signal Processing Systems, 2017, 87, 343-356.	2.1	0
20	Architecture of a Parallel MOSFET Parameter Extraction System. Lecture Notes in Computer Science, 2013, , 329-340.	1.3	0
21	Making ProTools Accessible for Visually Impaired. Lecture Notes in Computer Science, 2008, , 781-788.	1.3	0
22	Three counter value based ROPUFs on FPGA and their properties. Microprocessors and Microsystems, 2021, 88, 104375.	2.8	0
23	Symmetric and Asymmetric Schemes for Lightweight Secure Communication. Communications in Computer and Information Science, 2022, , 97-114.	0.5	0