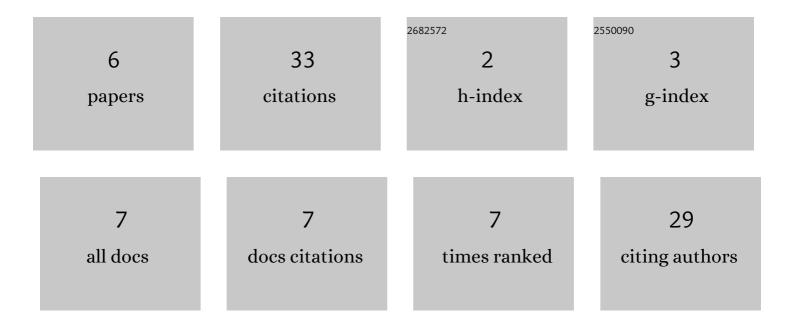
## Wei Zhang

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/2110102/publications.pdf Version: 2024-02-01



#	Article	IF	CITATIONS
1	Extending Performance-Energy Trade-offs via Dynamic Core Scaling. IEEE Transactions on Computers, 2020, , 1-1.	3.4	ο
2	A memory efficient implementation scheme of Gauss error function in a Laguerre-Volterra network for neuroprosthetic devices. Review of Scientific Instruments, 2017, 88, 044301.	1.3	1
3	Dynamic core scaling: Trading off performance and energy beyond DVFS. , 2015, , .		6
4	Adaptive front-end throttling for superscalar processors. , 2014, , .		6
5	High-Performance and Scalable System Architecture for the Real-Time Estimation of Generalized Laguerre-Volterra MIMO Model From Neural Population Spiking Activity. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2011, 1, 489-501.	3.6	17
6	FPGA Architecture of Generalized Laguerre-Volterra MIMO Model for Neural Population Activities. , 2011, , .		1