

# Tapas Roy

## List of Publications by Year in descending order

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papers

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citations

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docs citations

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times ranked

287  
citing authors

#	ARTICLE	IF	CITATIONS
1	A Step-Up Multilevel Inverter Topology Using Novel Switched Capacitor Converters With Reduced Components. IEEE Transactions on Industrial Electronics, 2021, 68, 236-247.	7.9	107
2	A switched capacitor based step-up multilevel inverter and its cascaded configuration using reduced number of components. International Transactions on Electrical Energy Systems, 2021, 31, e12721.	1.9	3
3	A Step-up Multilevel Inverter Structure using Switched Capacitor technique with Non-isolated Power Sources and Reduced Devices. , 2021, , .		0
4	A 7-Level Switched Capacitor Multilevel Inverter With Reduced Switches and Voltage Stresses. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3587-3591.	3.0	22
5	A Novel Single-Phase Switched Capacitor Multilevel Inverter with Voltage Boosting Ability for Renewable Applications. Lecture Notes in Electrical Engineering, 2021, , 107-117.	0.4	0
6	A Novel Step-up 7L Switched-Capacitor Multilevel Inverter and Its Extended Structure. , 2021, , .		3
7	A Novel 15-Level Asymmetric Modified T-Type Inverter with Reduced Device count. , 2021, , .		0
8	A novel symmetric switched capacitor multilevel inverter using non-isolated power supplies with reduced number of components. Sadhana - Academy Proceedings in Engineering Sciences, 2020, 45, 1.	1.3	8
9	A Novel Step-Up Multilevel Inverter Based On Switched-Capacitor Technique For Renewable Energy Conversion System. , 2019, , .		4
10	Fault analysis method of integrated high voltage direct current transmission lines for onshore wind farm. Journal of Modern Power Systems and Clean Energy, 2019, 7, 621-632.	5.4	15
11	Cross-Switched Multilevel Inverter Using Novel Switched Capacitor Converters. IEEE Transactions on Industrial Electronics, 2019, 66, 8521-8532.	7.9	114
12	A Novel Reduced Device Count Multilevel Inverter Structure using Non-isolated Power Supplies. , 2018, , .		0
13	A New Symmetrical Three Phase Multilevel Inverter using Switched Capacitor Basic Units for Renewable Energy Conversion Systems. , 2018, , .		3
14	A Novel Structure of Cascaded Multilevel Inverter with High Voltage Level Generation Capability using Reduced Components. , 2018, , .		0
15	A Novel Structure of Switched Capacitor Multilevel Inverter with Reduced Device Count. , 2018, , .		6
16	Closed Loop Control of a Novel Three Phase Switched Capacitor Multilevel Inverter using Model Predictive Control Technique. , 2018, , .		2
17	Harmonic Analysis in L-G Fault for LCC Based HVDC. , 2018, , .		0
18	Analysis and Implementation of a Novel Multilevel Inverter Structure Using Reduced Power Electronic Switches and DC Sources. , 2018, , .		0

#	ARTICLE	IF	CITATIONS
19	Development, Analysis and Simulation Study of a Novel Switched Capacitor Multilevel Inverter Structure for Different DC Source Configurations. , 2018, , .		0
20	PMU implementation for a Wide Area Measurement of a power system. , 2017, , .		7
21	Fault detection in direct current transmission lines using discrete fourier transform from single terminal current signals. , 2017, , .		11
22	A novel structure of cascaded multilevel inverter with reduced device count. , 2017, , .		2
23	Intelligent computing based converter fault identification in line commutated high voltage direct current transmission line. , 2017, , .		2
24	Application of nested topology of multilevel inverter in closed loop control of induction motor drives. , 2017, , .		0
25	A novel symmetrical switched capacitor based three-phase cascaded multi-level inverter. , 2016, , .		3
26	Analysis and simulation of a new topology of single phase multi-level inverter. , 2016, , .		2
27	Switched Capacitor Z-Source Inverter. , 2016, , .		2
28	Study of a new single phase multilevel inverter based on switched capacitor units. , 2016, , .		1
29	Comparison study of different pulse width modulation techniques for Extended boost Z-source inverter. , 2016, , .		2
30	Analysis and simulation study of extended boost z-source sparse matrix converter. , 2016, , .		4
31	A novel isolated boost converter fed BLDCM drive for electric vehicle. , 2016, , .		2
32	Application of an Advanced Repetitive Controller to Mitigate Harmonics in MMC With APOD Scheme. IEEE Transactions on Power Electronics, 2016, 31, 6112-6121.	7.9	35
33	Advanced Pulse Width Modulation technique for Z-Source Inverter. , 2014, , .		4
34	A new technique to implement conventional as well as advanced Pulse Width Modulation techniques for multi-level inverter. , 2014, , .		3
35	Voltage Balancing Scheme in MMC â€“ A New Approach. International Journal of Emerging Electric Power Systems, 2014, 15, 389-399.	0.8	3
36	New harmonic mitigation scheme for modular multilevel converter â€“ an experimental approach. IET Power Electronics, 2014, 7, 3090-3100.	2.1	24

#	ARTICLE	IF	CITATIONS
37	High frequency link soft switched pulse shifted modulated front end 3 level diode clamped inverter using APSO. , 2014, , .		1
38	ZVCS based high frequency link grid connected SVPWM applied three phase three level diode clamped inverter for photovoltaic applications. , 2014, , .		6
39	A Review and Advance Technology in Multi-Area Automatic Generation Control by Using Minority Charge Carrier Inspired Algorithm. International Journal of Emerging Electric Power Systems, 2013, 14, 609-627.	0.8	4
40	A Review of Optimized Heating Performance of Induction Cookers. International Journal of Power System Operation and Energy Management, 2013, , 1-3.	0.1	0