

Kiranvg Kumar

List of Publications by Year in descending order

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Version: 2024-02-01

12
papers

37
citations

2258059

3
h-index

2053705

5
g-index

13
all docs

13
docs citations

13
times ranked

21
citing authors

#	ARTICLE	IF	CITATIONS
1	FPGA implementation novel lightweight MBRISI cipher. Journal of Ambient Intelligence and Humanized Computing, 2023, 14, 11625-11637.	4.9	3
2	Design and Implementation of Novel BRISI Lightweight Cipher for Resource Constrained Devices. Microprocessors and Microsystems, 2021, 84, 104267.	2.8	7
3	Efficient Implementation of Cryptographic Arithmetic Primitives Using Reversible Logic and Vedic Mathematics. Journal of the Institution of Engineers (India): Series B, 2021, 102, 59-74.	1.9	3
4	FPGA Implementation of Simple Encryption Scheme for Resource-Constrained Devices. International Journal of Advanced Trends in Computer Science and Engineering, 2020, 9, 5631-5639.	0.2	2
5	A Novel Key Scheduling Algorithm for Lightweight Cryptographic Applications. International Journal of Advanced Trends in Computer Science and Engineering, 2020, 9, 682-684.	0.2	1
6	Design and Implementation of Efficient Cryptographic Arithmetic based on Reversible logic and Vedic Mathematics. International Journal of Advanced Trends in Computer Science and Engineering, 2020, 9, 1037-1044.	0.2	0
7	Implementation and Analysis of Cryptographic Ciphers in FPGA. Advances in Intelligent Systems and Computing, 2019, , 653-666.	0.6	1
8	Low Power High Speed Arithmetic Circuits. International Journal of Recent Technology and Engineering, 2019, 8, 807-813.	0.2	1
9	Cryptography using Modular Arithmetics. International Journal of New Technology and Research, 2019, 5, .	0.0	0
10	Implementation of lightweight cryptographic algorithms in FPGA. , 2017, , .		1
11	A Survey on Various Lightweight Cryptographic Algorithms on FPGA. IOSR Journal of Electronics and Communication Engineering, 2017, 12, 54-59.	0.1	4
12	Comparative Study of cryptographic encryption algorithms. IOSR Journal of Electronics and Communication Engineering, 2017, 12, 66-71.	0.1	2