

Yan Lu

List of Publications by Year in descending order

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2,714
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257450

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124
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124
times ranked

1547
citing authors

#	ARTICLE	IF	CITATIONS
1	A Scalable High-Current High-Accuracy Dual-Loop Four-Phase Switching LDO for Microprocessors. IEEE Journal of Solid-State Circuits, 2022, 57, 1841-1853.	5.4	8
2	RF Rectifiers With Wide Incident Angle of Incoming Waves Based on Rat-Race Couplers. IEEE Transactions on Microwave Theory and Techniques, 2022, 70, 1983-1993.	4.6	5
3	A Reconfigurable Single-Stage Asymmetrical Full-Wave Step-Down Rectifier for Bidirectional Device-to-Device Wireless Fast Charging. IEEE Journal of Solid-State Circuits, 2022, 57, 1888-1898.	5.4	2
4	A 27W D2D Wireless Power Transfer System with Compact Single-Stage Regulated Class-E Architecture and Adaptive ZVS Control. , 2022, , .		2
5	A 4A 12-to-1 Flying Capacitor Cross-Connected DC-DC Converter with Inserted >0.5 Control Achieving $>2x$ Transient Inductor Current Slew Rate and 0.73Å — Theoretical Minimum Output Undershoot of DSD. , 2022, , .		10
6	All Rivers Flow to the Sea: A High Power Density Wireless Power Receiver with Split-Dual-Path Rectification and Hybrid-Quad-Path Step-Down Conversion. , 2022, , .		0
7	A Highly Integrated Tri-Path Hybrid Buck Converter With Reduced Inductor Current and Self-Balanced Flying Capacitor Voltage. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 3841-3850.	5.4	7
8	A Capacitor-Cross-Connected Boost Converter With Duty Cycle < 0.5 Control for Extended Conversion-Ratio and Soft Start-Up. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 4272-4283.	5.4	0
9	Review of Analog-Assisted-Digital and Digital-Assisted-Analog Low Dropout Regulators. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 24-29.	3.0	15
10	A Fully Integrated FVF LDO With Enhanced Full-Spectrum Power Supply Rejection. IEEE Transactions on Power Electronics, 2021, 36, 4326-4337.	7.9	48
11	Circuit Techniques for High Efficiency Fully-Integrated Switched-Capacitor Converters. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 556-561.	3.0	8
12	Spray-coated electret materials with enhanced stability in a harsh environment for an MEMS energy harvesting device. Microsystems and Nanoengineering, 2021, 7, 15.	7.0	23
13	Bird's-eye view of analog and mixed-signal chips for the 21st century. International Journal of Circuit Theory and Applications, 2021, 49, 746-761.	2.0	7
14	Optimization of MEMS Vibration Energy Harvester With Perforated Electrode. Journal of Microelectromechanical Systems, 2021, 30, 299-308.	2.5	14
15	A 3-Phase Resonant Switched-Capacitor Converter for Data Center 48-V Rack Power Distribution. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 2714-2724.	5.4	18
16	A Highly Integrated 3-Phase 4:1 Resonant Switched-Capacitor Converter With Parasitic Loss Reduction and Fast Pre-Charge Startup. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 2608-2612.	3.0	10
17	A Hybrid Boost Converter With Cross-Connected Flying Capacitors. IEEE Journal of Solid-State Circuits, 2021, 56, 2102-2112.	5.4	18
18	A Single-Stage Dual-Output Regulating Rectifier With Hysteretic Current-Wave Modulation. IEEE Journal of Solid-State Circuits, 2021, 56, 2770-2780.	5.4	12

#	ARTICLE	IF	CITATIONS
19	A Hybrid Single-Inductor Bipolar-Output DC-DC Converter With Floating Negative Output for AMOLED Displays. IEEE Journal of Solid-State Circuits, 2021, 56, 2760-2769.	5.4	13
20	A 6.78-MHz Single-Stage Wireless Power Receiver With Ultrafast Transient Response Using Hysteretic Control and Multilevel Current-Wave Modulation. IEEE Transactions on Power Electronics, 2021, 36, 9918-9926.	7.9	17
21	A 2.4-GHz CMOS Differential Class-DE Rectifier With Coupled Inductors. IEEE Transactions on Power Electronics, 2021, 36, 9864-9875.	7.9	4
22	Adaptive Maximum Power Point Tracking With Model-Based Negative Feedback Control and Improved V _{ref} Model. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3103-3107.	3.0	3
23	A Two-Phase Three-Level Buck Converter With Cross-Connected Flying Capacitors for Inductor Current Balancing. IEEE Transactions on Power Electronics, 2021, 36, 13855-13866.	7.9	24
24	Design of Diode-Connected and Cross-Connected CMOS Rectifiers with Adaptive Tuning for RF Energy Harvesting. , 2021, , .		0
25	A Fully Integrated LDO With 50-mV Dropout for Power Efficiency Optimization. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 725-729.	3.0	25
26	Dual Active-Feedback Frequency Compensation for Output-Capacitorless LDO With Transient and Stability Enhancement in 65-nm CMOS. IEEE Transactions on Power Electronics, 2020, 35, 415-429.	7.9	56
27	Subtraction-Mode Switched-Capacitor Converters With Parasitic Loss Reduction. IEEE Transactions on Power Electronics, 2020, 35, 1200-1204.	7.9	10
28	A 2.4-GHz Mid-Field CMOS Wireless Power Receiver Achieving 46% Maximum PCE and 163-mW Output Power. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 360-364.	3.0	11
29	A Multiphase Switched-Capacitor Converter for Fully Integrated AMOLED Microdisplay System. IEEE Transactions on Power Electronics, 2020, 35, 6001-6011.	7.9	15
30	A Nano-Watt Dual-Output Subthreshold CMOS Voltage Reference. IEEE Open Journal of Circuits and Systems, 2020, 1, 100-106.	1.9	9
31	An NMOS Digital LDO With NAND-Based Analog-Assisted Loop in 28-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4041-4052.	5.4	18
32	A switched-capacitor power converter with unequal duty cycle for ripple reduction and efficiency improvement. Microelectronics Journal, 2020, 104, 104888.	2.0	4
33	A Two-Phase Three-Level Buck DC-DC Converter With X-Connected Flying Capacitors for Current Balancing. IEEE Solid-State Circuits Letters, 2020, 3, 442-445.	2.0	14
34	A Power-Efficient Hybrid Single-Inductor Bipolar-Output DC-DC Converter with Floating Negative Output for AMOLED Displays. , 2020, , .		4
35	A VHF Wide-Input Range CMOS Passive Rectifier With Active Bias Tuning. IEEE Journal of Solid-State Circuits, 2020, 55, 2629-2638.	5.4	15
36	An Analog-Proportional Digital-Integral Multiloop Digital LDO With PSR Improvement and LCO Reduction. IEEE Journal of Solid-State Circuits, 2020, , 1-14.	5.4	20

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37	Rotational electromagnetic energy harvester for human motion application at low frequency. Applied Physics Letters, 2020, 116, .	3.3	58
38	An inertial rotary energy harvester for vibrations at ultra-low frequency with high energy conversion efficiency. Applied Energy, 2020, 279, 115762.	10.1	66
39	A comparative study of digital low dropout regulators. Journal of Semiconductors, 2020, 41, 111405.	3.7	8
40	11.5 A 2-Phase Soft-Charging Hybrid Boost Converter with Doubled-Switching Pulse Width and Shared Bootstrap Capacitor Achieving 93.5% Efficiency at a Conversion Ratio of 4.5. , 2020, , .		15
41	An Integrated DC-DC Converter With Segmented Frequency Modulation and Multiphase Co-Work Control for Fast Transient Recovery. IEEE Journal of Solid-State Circuits, 2019, 54, 2637-2648.	5.4	6
42	PID Control Considerations for Analog-Digital Hybrid Low-Dropout Regulators (Invited Paper). , 2019, , .		7
43	A Reconfigurable Cross-Connected Wireless-Power Transceiver for Bidirectional Device-to-Device Wireless Charging. IEEE Journal of Solid-State Circuits, 2019, 54, 2579-2589.	5.4	32
44	A Fast-Transient-Response Fully-Integrated Digital LDO With Adaptive Current Step Size Control. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3610-3619.	5.4	20
45	A 1-nW Ultra-Low Voltage Subthreshold CMOS Voltage Reference With 0.0154%/V Line Sensitivity. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1653-1657.	3.0	30
46	A Fast-Transient-Response Fully-Integrated Digital LDO with Adaptive Current Step Size Control. , 2019, , .		6
47	A Fully Integrated FVF-Based Low-Dropout Regulator With Wide Load Capacitance and Current Ranges. IEEE Transactions on Power Electronics, 2019, 34, 11880-11888.	7.9	43
48	A 0.5-V-supply, 37.8-nW, 17.6-ppm/°C switched-capacitor bandgap reference with second-order curvature compensation. Microelectronics Journal, 2019, 87, 136-143.	2.0	3
49	Design Considerations on Integrated Rectifiers with High Efficiency and Wide Input Power Range for RF Energy Harvesting. , 2019, , .		0
50	A 200-MHz Wide Input Range CMOS Passive Rectifier with Active Bias Tuning. , 2019, , .		1
51	A Switched-Capacitor DC-DC Converter with Unequal Duty Cycle for Ripple Reduction and Efficiency Improvement. , 2019, , .		2
52	A Reconfigurable Bidirectional Wireless Power Transceiver for Battery-to-Battery Wireless Charging. IEEE Transactions on Power Electronics, 2019, 34, 7745-7753.	7.9	63
53	A digital SC converter with high efficiency and low voltage ripple. , 2018, , .		0
54	Partial analogue-assisted digital low dropout regulator with transient body-drive and 2.5Å— FOM improvement. Electronics Letters, 2018, 54, 282-283.	1.0	8

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55	Design Considerations of Distributed and Centralized Switched-Capacitor Converters for Power Supply On-Chip. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2018, 6, 515-525.	5.4	22
56	A reconfigurable cross-connected wireless-power transceiver for bidirectional device-to-device charging with 78.1% total efficiency. , 2018, , .		9
57	A 0.4V 430nA quiescent current NMOS digital LDO with NAND-based analog-assisted loop in 28nm CMOS. , 2018, , .		34
58	An Analog-Assisted Tri-Loop Digital Low-Dropout Regulator. IEEE Journal of Solid-State Circuits, 2018, 53, 20-34.	5.4	88
59	Introduction of Wireless Power Transfer. Analog Circuits and Signal Processing Series, 2018, , 1-11.	0.3	1
60	Linear Regulators for WPT. Analog Circuits and Signal Processing Series, 2018, , 97-126.	0.3	2
61	CMOS Integrated Circuit Design for Wireless Power Transfer. Analog Circuits and Signal Processing Series, 2018, , .	0.3	16
62	A Reconfigurable Switched-Capacitor DC-DC Converter and Cascode LDO for Dynamic Voltage Scaling and High PSR. , 2018, , .		6
63	An Overview of Digital Low Drop-out Regulator Design. , 2018, , .		3
64	An Integrated DC-DC Converter with Segmented Frequency Modulation and Multiphase Co-Work Control for Fast Transient Recovery. , 2018, , .		1
65	A Digital LDO With Co-SA Logics and TSPC Dynamic Latches for Fast Transient Response. IEEE Solid-State Circuits Letters, 2018, 1, 154-157.	2.0	12
66	Bi-directional Battery-to-Battery Wireless Charging Enabled by Reconfigurable Wireless Power Transceivers (Invited Paper). , 2018, , .		0
67	A Dual-Loop Digital LDO Regulator with Asynchronous-Flash Binary Coarse Tuning. , 2018, , .		15
68	A Fully Integrated Low-Dropout Regulator With Differentiator-Based Active Zero Compensation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3578-3591.	5.4	37
69	A Single-Stage Current-Mode Active Rectifier with Accurate Output-Current Regulation for IoT. , 2018, , .		8
70	Nano-Ampere Low-Dropout Regulator Designs for IoT Devices. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 4017-4026.	5.4	46
71	Missing-Code-Occurrence Probability Calibration Technique for DAC Nonlinearity With Supply and Reference Circuit Analysis in a SAR ADC. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3707-3719.	5.4	4
72	0.45â€V 5.4â€nW switchedâ€capacitor bandgap reference with intermittent operation and improved supply immunity. Electronics Letters, 2018, 54, 1154-1156.	1.0	7

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73	A 6.78 MHz active voltage doubler with near-optimal on/off delay compensation for wireless power transfer systems. , 2018, , .		4
74	Circuit Design of CMOS Rectifiers. Analog Circuits and Signal Processing Series, 2018, , 53-96.	0.3	0
75	DC-DC Converters for WPT. Analog Circuits and Signal Processing Series, 2018, , 127-141.	0.3	0
76	Wireless Power Transfer Systems. Analog Circuits and Signal Processing Series, 2018, , 13-32.	0.3	0
77	Analysis of Coupled-Coils. Analog Circuits and Signal Processing Series, 2018, , 33-51.	0.3	0
78	Design of Low Standby Power Fully Integrated Voltage Regulators. , 2018, , 33-56.		0
79	A Wide Input Range Dual-Path CMOS Rectifier for RF Energy Harvesting. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 166-170.	3.0	111
80	22.4 A reconfigurable bidirectional wireless power transceiver with maximum-current charging mode and 58.6% battery-to-battery efficiency. , 2017, , .		21
81	A sub-1V 78-nA bandgap reference with curvature compensation. Microelectronics Journal, 2017, 63, 35-40.	2.0	14
82	20.4 An output-capacitor-free analog-assisted digital low-dropout regulator with tri-loop control. , 2017, , .		55
83	20.5 A dual-symmetrical-output switched-capacitor converter with dynamic power cells and minimized cross regulation for application processors in 28nm CMOS. , 2017, , .		16
84	A digital IQ imbalance self-calibration in FDD transceiver. , 2017, , .		2
85	A four-band TD-LTE transmitter with wide dynamic range and LPF bandwidth calibration. , 2017, , .		1
86	Digital 2-/3-Phase Switched-Capacitor Converter With Ripple Reduction and Efficiency Improvement. IEEE Journal of Solid-State Circuits, 2017, 52, 1836-1848.	5.4	45
87	A Multiphase Switched-Capacitor DC-DC Converter Ring With Fast Transient Response and Small Ripple. IEEE Journal of Solid-State Circuits, 2017, 52, 579-591.	5.4	57
88	A Dual-Output Wireless Power Transfer System With Active Rectifier and Three-Level Operation. IEEE Transactions on Power Electronics, 2017, 32, 927-930.	7.9	35
89	Optic Nerve Stimulation System with Adaptive Wireless Powering and Data Telemetry. Micromachines, 2017, 8, 368.	2.9	7
90	Power Management Circuit Design for IoT Nodes. , 2017, , 287-316.		5

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91	Wireless Power Transfer System Architectures for Portable or Implantable Applications. <i>Energies</i> , 2016, 9, 1087.	3.1	29
92	Digitally assisted low dropout regulator design for low duty cycle IoT applications. , 2016, , .		5
93	A digital LDO with transient enhancement and limit cycle oscillation reduction. , 2016, , .		5
94	A 312 ps response time LDO with enhanced super source follower in 28nm CMOS. <i>Electronics Letters</i> , 2016, 52, 1368-1370.	1.0	30
95	A digitally-controlled 2/3-phase 6-ratio switched-capacitor DC-DC converter with adaptive ripple reduction and efficiency improvements. , 2016, , .		6
96	An 18-Gb/s Fully Integrated Optical Receiver With Adaptive Cascaded Equalizer. <i>IEEE Journal of Selected Topics in Quantum Electronics</i> , 2016, 22, 361-369.	2.9	24
97	Limit Cycle Oscillation Reduction for Digital Low Dropout Regulators. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2016, 63, 903-907.	3.0	49
98	A Fully Integrated Digital LDO With Coarse Fine-Tuning and Burst-Mode Operation. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2016, 63, 683-687.	3.0	116
99	Adaptive On/Off Delay-Compensated Active Rectifiers for Wireless Power Transfer Systems. <i>IEEE Journal of Solid-State Circuits</i> , 2016, 51, 712-723.	5.4	124
100	An NMOS-LDO Regulated Switched-Capacitor DC-DC Converter With Fast-Response Adaptive-Phase Digital Control. <i>IEEE Transactions on Power Electronics</i> , 2016, 31, 1294-1303.	7.9	77
101	Capacitive floating level shifter: Modeling and design. , 2015, , .		3
102	20.4 A 123-phase DC-DC converter-ring with fast-DVS for microprocessors. , 2015, , .		35
103	An all-factor modulation bandwidth extension technique for delta-sigma PLL transmitter. , 2015, , .		0
104	A Fully-Integrated Low-Dropout Regulator With Full-Spectrum Power Supply Rejection. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2015, 62, 707-716.	5.4	152
105	A 30-Gb/s 1.37-pJ/b CMOS Receiver for Optical Interconnects. <i>Journal of Lightwave Technology</i> , 2015, 33, 778-786.	4.6	19
106	A CMOS Delta-Sigma PLL Transmitter with Efficient Modulation Bandwidth Calibration. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2015, 62, 1716-1725.	5.4	10
107	A review and design of the on-chip rectifiers for RF energy harvesting. , 2015, , .		28
108	20.5 A 2/3-phase fully integrated switched-capacitor DC-DC converter in bulk CMOS for energy-efficient digital circuits with 14% efficiency improvement. , 2015, , .		63

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109	Analysis of two-phase on-chip step-down switched capacitor power converters. , 2014, , .		3
110	A low-dropout regulator with power supply rejection improvement by bandwidth-zero tracking. , 2014, , .		2
111	Efficient wireless power transmission technology based on above-CMOS integrated (ACI) high quality inductors. , 2014, , .		8
112	An adaptive wireless powering and data telemetry system for optic nerve stimulation. , 2014, , .		12
113	A 13.56 MHz CMOS Active Rectifier With Switched-Offset and Compensated Biasing for Biomedical Wireless Power Transfer Systems. IEEE Transactions on Biomedical Circuits and Systems, 2014, 8, 334-344.	4.0	209
114	A 48-mW 18-Gb/s fully integrated CMOS optical receiver with photodetector and adaptive equalizer. , 2014, , .		4
115	A 3-mW 25-Gb/s CMOS transimpedance amplifier with fully integrated low-dropout regulator for 100GbE systems. , 2014, , .		12
116	17.11 A 0.65ns-response-time 3.01ps FOM fully-integrated low-dropout regulator with full-spectrum power-supply-rejection for wideband communication systems. , 2014, , .		75
117	A 13.56MHz fully integrated 1X/2X active rectifier with compensated bias current for inductively powered devices. , 2013, , .		24
118	A \$pm\$0.5% Precision On-Chip Frequency Reference With Programmable Switch Array for Crystal-Less Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 642-646.	3.0	19
119	Input-adaptive dual-output power management unit for energy harvesting devices. , 2012, , .		4
120	Analysis and Design Strategy of On-Chip Charge Pumps for Micro-power Energy Harvesting Applications. International Federation for Information Processing, 2012, , 158-186.	0.4	12
121	Design and analysis of on-chip charge pumps for micro-power energy harvesting applications. , 2011, , .		20
122	Low-voltage constant-gm rail-to-rail CMOS operational amplifier input stage. Solid-State Electronics, 2008, 52, 957-961.	1.4	9
123	A switchedâ€œcapacitorâ€œassisted wireless power transfer system with regulating TX power and fast global loop. Electronics Letters, 0, , .	1.0	2