

Alyssa B Apsel

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

47
papers

369
citations

10
h-index

17
g-index

56
ext. papers

448
ext. citations

3.1
avg, IF

3.62
L-index

#	Paper	IF	Citations
47	Ultrawideband Frequency Synthesis Using the Compact Tunable Transmission Line (CTTL). <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2022 , 1-1	4.1	
46	Sample-Efficient Spatio-Spectral Whitespace Detection Using Least Matching Pursuit. <i>IEEE Access</i> , 2021 , 9, 138394-138402	3.5	
45	SpinOrbit torque field-effect transistor (SOTFET): Proposal for a magnetoelectric memory. <i>Applied Physics Letters</i> , 2020 , 116, 242405	3.4	4
44	An Integrated, Software-Defined FDD Transceiver: Distributed Duplexing Theory and Operation. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 271-283	3.9	1
43	Identifying Unused RF Channels Using Least Matching Pursuit 2020 ,		1
42	Modeling and Circuit Design of Associative Memories With SpinOrbit Torque FETs. <i>IEEE Journal on Exploratory Solid-State Computational Devices and Circuits</i> , 2019 , 5, 197-205	2.4	4
41	The Impact of LO Phase Noise in N-Path Filters. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2018 , 65, 1481-1494	3.9	11
40	Theory and Demonstration of Noisy Oscillator Samplers for Clock Jitter and Phase Delay Measurement. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2018 , 65, 1516-1528	3.9	2
39	Four Monolithically Integrated Switched-Capacitor DCDC Converters With Dynamic Capacitance Sharing in 65-nm CMOS. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2018 , 65, 2035-2047	3.9	6
38	A Simple Guide to Low-Power Wireless Technologies: Balancing the Tradeoffs for the Internet of Things and Medical Applications. <i>IEEE Solid-State Circuits Magazine</i> , 2018 , 10, 16-23	1.5	8
37	A Wideband Fully Integrated Software-Defined Transceiver for FDD and TDD Operation. <i>IEEE Journal of Solid-State Circuits</i> , 2017 , 52, 1274-1285	5.5	18
36	Dual-Calibration Technique for Improving Static Linearity of Thermometer DACs for I/O. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2016 , 24, 1050-1058	2.6	1
35	0.89 mW on-chip jitter-measurement circuit for high speed clock with sub-picosecond resolution 2016 ,		2
34	A quantized pulse coupled oscillator for slow clocking of peer-to-peer networks 2015 ,		2
33	Enabling Realistic Fine-Grain Voltage Scaling with Reconfigurable Power Distribution Networks 2014 ,		31
32	Low-Power, Minimally Invasive Process Compensation Technique for Sub-Micron CMOS Amplifiers. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2014 , 22, 1-12	2.6	5
31	A Reconfigurable Integrated Dispersive Delay Line (RI-DDL) in 0.13-/spl mu/m CMOS Process. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2013 , 61, 2610-2619	4.1	10

30	A Crystal-Less Self-Synchronized Bit-Level Duty-Cycled IR-UWB Transceiver System. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2013 , 60, 2488-2501	3.9	17
29	On-chip demonstration of real time spectrum analysis (RTSA) using integrated dispersive delay line (IDDL) 2013 ,		4
28	An integrated Ku-band nanosecond time-stretching system using improved dispersive delay line (DDL) 2012 ,		5
27	Theoretical Analysis and Practical Considerations for the Integrated Time-Stretching System Using Dispersive Delay Line (DDL). <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2012 , 60, 3449-3457 ^{4.1}		17
26	Improving Absolute Accuracy of Integrated Resistors With Device Diversification. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2012 , 59, 346-350	3.5	9
25	A Low-Power, Process-and- Temperature- Compensated Ring Oscillator With Addition-Based Current Source. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2011 , 58, 868-878	3.9	64
24	Process Compensation Loops for High Speed Ring Oscillators in Sub-Micron CMOS. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , 2011 , 1, 59-70	5.2	7
23	A 46- μ W Self-Calibrated Gigahertz VCO for Low-Power Radios. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2011 , 58, 847-851	3.5	13
22	A Novel Dynamically Duty-Cyclable, Low Power UWB Impulse Radio Based Event Communication 2011 ,		1
21	PCO-Based Synchronization for Cognitive Duty-Cycled Impulse Radio Sensor Networks. <i>IEEE Sensors Journal</i> , 2011 , 11, 555-564	4	20
20	A distributed amplifier based dispersive delay line 2011 ,		2
19	A self-synchronized, crystal-less, 86 μ W, dual-band impulse radio for ad-hoc wireless networks 2011 ,		4
18	Process variation compensation of a 4.6 GHz LNA in 65nm CMOS 2010 ,		4
17	PCO Based Event Propagation Scheme for Globally Synchronized Sensor Networks 2010 ,		3
16	A successive approximation based process-invariant ring oscillator 2010 ,		3
15	A Novel On-Chip Active Dispersive Delay Line (DDL) for Analog Signal Processing. <i>IEEE Microwave and Wireless Components Letters</i> , 2010 , 20, 584-586	2.6	8
14	A high-speed, on-chip implementation of Teager Kaiser operator for in-band interference rejection 2010 ,		5
13	A 19 μ W, 100kbps Impulse Radio transceiver for body-area-networks 2010 ,		1

12	Alternative m-derived termination for distributed amplifiers 2009 ,		1
11	A low variation GHz ring oscillator with addition-based current source 2009 ,		1
10	A process compensated 3-GHz ring oscillator 2009 ,		5
9	A low variation GHz ring oscillator with addition-based current source 2009 ,		7
8	Low variation current source for 90nm CMOS 2008 ,		3
7	Distributed Amplifier With Blue Noise Active Termination. <i>IEEE Microwave and Wireless Components Letters</i> , 2008 , 18, 203-205	2.6	5
6	124dB?Hz Dynamic range transimpedance amplifier for electronic-photonic channelizer 2008 ,		1
5	A 10 Gb/s optical receiver in 0.25 μm silicon-on-sapphire CMOS 2008 ,		2
4	Beating the power limit of LC oscillators. <i>Midwest Symposium on Circuits and Systems</i> , 2007 ,	1	2
3	Pulse coupled oscillator synchronization for low power UWB wireless transceivers. <i>Midwest Symposium on Circuits and Systems</i> , 2007 ,	1	10
2	Monolithic Integrated SiGe Optical Receiver and Detector 2007 ,		3
1	Process-Invariant Current Source Design: Methodology and Examples. <i>IEEE Journal of Solid-State Circuits</i> , 2007 , 42, 2293-2302	5.5	33