

Alyssa B Apsel

List of Publications by Year in descending order

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papers

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932766

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times ranked

540
citing authors

#	ARTICLE	IF	CITATIONS
1	A Low-Power, Process-and- Temperature- Compensated Ring Oscillator With Addition-Based Current Source. IEEE Transactions on Circuits and Systems I: Regular Papers, 2011, 58, 868-878.	3.5	78
2	Enabling Realistic Fine-Grain Voltage Scaling with Reconfigurable Power Distribution Networks. , 2014, , .		39
3	Process-Invariant Current Source Design: Methodology and Examples. IEEE Journal of Solid-State Circuits, 2007, 42, 2293-2302.	3.5	37
4	PCO-Based Synchronization for Cognitive Duty-Cycled Impulse Radio Sensor Networks. IEEE Sensors Journal, 2011, 11, 555-564.	2.4	30
5	Theoretical Analysis and Practical Considerations for the Integrated Time-Stretching System Using Dispersive Delay Line (DDL). IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 3449-3457.	2.9	26
6	A Wideband Fully Integrated Software-Defined Transceiver for FDD and TDD Operation. IEEE Journal of Solid-State Circuits, 2017, 52, 1274-1285.	3.5	25
7	A Crystal-Less Self-Synchronized Bit-Level Duty-Cycled IR-UWB Transceiver System. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 2488-2501.	3.5	23
8	Pulse coupled oscillator synchronization for low power UWB wireless transceivers. Midwest Symposium on Circuits and Systems, 2007, , .	1.0	18
9	A 46- μ W Self-Calibrated Gigahertz VCO for Low-Power Radios. IEEE Transactions on Circuits and Systems II: Express Briefs, 2011, 58, 847-851.	2.2	16
10	The Impact of LO Phase Noise in N-Path Filters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 1481-1494.	3.5	16
11	A Reconfigurable Integrated Dispersive Delay Line (RI-DDL) in 0.13- μ m CMOS Process. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 2610-2619.	2.9	13
12	A Novel On-Chip Active Dispersive Delay Line (DDL) for Analog Signal Processing. IEEE Microwave and Wireless Components Letters, 2010, 20, 584-586.	2.0	10
13	A Simple Guide to Low-Power Wireless Technologies: Balancing the Tradeoffs for the Internet of Things and Medical Applications. IEEE Solid-State Circuits Magazine, 2018, 10, 16-23.	0.5	10
14	Process Compensation Loops for High Speed Ring Oscillators in Sub-Micron CMOS. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2011, 1, 59-70.	2.7	9
15	Improving Absolute Accuracy of Integrated Resistors With Device Diversification. IEEE Transactions on Circuits and Systems II: Express Briefs, 2012, 59, 346-350.	2.2	9
16	Low-Power, Minimally Invasive Process Compensation Technique for Sub-Micron CMOS Amplifiers. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 1-12.	2.1	9
17	Four Monolithically Integrated Switched-Capacitor DC-DC Converters With Dynamic Capacitance Sharing in 65-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 2035-2047.	3.5	9
18	Spin-orbit torque field-effect transistor (SOTFET): Proposal for a magnetoelectric memory. Applied Physics Letters, 2020, 116, 242405.	1.5	9

#	ARTICLE	IF	CITATIONS
19	A low variation GHz ring oscillator with addition-based current source. , 2009, , .		8
20	A high-speed, on-chip implementation of Teager Kaiser operator for in-band interference rejection. , 2010, , .		8
21	A self-synchronized, crystal-less, 86µW, dual-band impulse radio for ad-hoc wireless networks. , 2011, , .		7
22	Dual-Calibration Technique for Improving Static Linearity of Thermometer DACs for I/O. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1050-1058.	2.1	7
23	Distributed Amplifier With Blue Noise Active Termination. IEEE Microwave and Wireless Components Letters, 2008, 18, 203-205.	2.0	6
24	An integrated Ku-band nanosecond time-stretching system using improved dispersive delay line (DDL). , 2012, , .		6
25	On-chip demonstration of real time spectrum analysis (RTSA) using integrated dispersive delay line (IDDL). , 2013, , .		6
26	Modeling and Circuit Design of Associative Memories With Spinâ€“Orbit Torque FETs. IEEE Journal on Exploratory Solid-State Computational Devices and Circuits, 2019, 5, 197-205.	1.1	6
27	A process compensated 3-GHz ring oscillator. , 2009, , .		5
28	Process variation compensation of a 4.6 GHz LNA in 65nm CMOS. , 2010, , .		5
29	A quantized pulse coupled oscillator for slow clocking of peer-to-peer networks. , 2015, , .		5
30	0.89 mW on-chip jitter-measurement circuit for high speed clock with sub-picosecond resolution. , 2016, , .		5
31	Analog Self-Interference Mitigation for IBFD, Joint Radar-Communications in Vehicular Applications. , 2022, , .		5
32	Monolithic Integrated SiGe Optical Receiver and Detector. , 2007, , .		4
33	Low variation current source for 90nm CMOS. , 2008, , .		4
34	PCO Based Event Propagation Scheme for Globally Synchronized Sensor Networks. , 2010, , .		3
35	A successive approximation based process-invariant ring oscillator. , 2010, , .		3
36	Beating the power limit of LC oscillators. Midwest Symposium on Circuits and Systems, 2007, , .	1.0	2

#	ARTICLE	IF	CITATIONS
37	A 10 Gb/s optical receiver in 0.25 μ m silicon-on-sapphire CMOS. , 2008, , .		2
38	A 19μW, 100kbps Impulse Radio transceiver for body-area-networks. , 2010, , .		2
39	A distributed amplifier based dispersive delay line. , 2011, , .		2
40	Theory and Demonstration of Noisy Oscillator Samplers for Clock Jitter and Phase Delay Measurement. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 1516-1528.	3.5	2
41	Ultrawideband Frequency Synthesis Using the Compact Tunable Transmission Line (CTTL). IEEE Transactions on Microwave Theory and Techniques, 2022, 70, 3374-3384.	2.9	2
42	A 6.8GHz low-power and low-phase-noise phase-locked loop design. , 2008, , .		1
43	124dBâ€¦Hz^{â€¦} Dynamic range transimpedance amplifier for electronic-photonics channelizer. , 2008, , .		1
44	Alternative m-derived termination for distributed amplifiers. , 2009, , .		1
45	A low variation GHz ring oscillator with addition-based current source. , 2009, , .		1
46	A Novel Dynamically Duty-Cyclable, Low Power UWB Impulse Radio Based Event Communication. , 2011, , .		1
47	Extending the dynamic range of implantable real-time neurochemical monitoring systems. , 2011, , .		1
48	Broadly tunable frequency division duplex transceiver: Theory and operation. , 2016, , .		1
49	Challenges and approaches to software defined duplexing radio. , 2016, , .		1
50	An Integrated, Software-Defined FDD Transceiver: Distributed Duplexing Theory and Operation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 271-283.	3.5	1
51	Identifying Unused RF Channels Using Least Matching Pursuit. , 2020, , .		1
52	Crystal-less duty-cycled-when-active IR-UWB transceivers. , 2014, , .		0
53	An FDD/FD Capable, Single Antenna RF Front End from 800MHz to 1.2GHz w/ Baseband Harmonic Predistortion. , 2018, , .		0
54	Sample-Efficient Spatio-Spectral Whitespace Detection Using Least Matching Pursuit. IEEE Access, 2021, 9, 138394-138402.	2.6	0

#	ARTICLE	IF	CITATIONS
55	Layout-Based Evaluation of Read/Write Performance of SOT-MRAM and SOTFET-RAM. , 2021, , .		0