# Darshika G Perera

List of Publications by Year
in descending order

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|---|---|---|---|
| **16**<br>papers | **171**<br>citations | 1478505<br>**6**<br>h-index | 1720034<br>**7**<br>g-index |
| **16**<br>all docs | **16**<br>docs citations | **16**<br>times ranked | **53**<br>citing authors |

| # | Article | IF | Citations |
|---|---------|-----|-----------|
| 1 | An Efficient FPGA-Based Hardware Accelerator for Convex Optimization-Based SVM Classifier for Machine Learning on Embedded Platforms. Electronics (Switzerland), 2021, 10, 1323. | 3.1 | 10 |
| 2 | Intelligent Cognitive Radio Architecture Applying Machine Learning and Reconfigurability. , 2021, , . | | 1 |
| 3 | Towards Composing Optimized Bi-Directional Multi-Ported Memories for Next-Generation FPGAs. IEEE Access, 2020, 8, 91531-91545. | 4.2 | 6 |
| 4 | Towards Dynamic and Partial Reconfigurable Hardware Architectures for Cryptographic Algorithms on Embedded Devices. IEEE Access, 2020, 8, 221720-221742. | 4.2 | 15 |
| 5 | Optimized hardware accelerators for data mining applications on embedded platforms: Case study principal component analysis. Microprocessors and Microsystems, 2019, 65, 79-96. | 2.8 | 17 |
| 6 | Efficient FPGA-Based Reconfigurable Accelerators for SIMON Cryptographic Algorithm on Embedded Platforms. , 2019, , . | | 8 |
| 7 | HDL Code Optimizations: Impact on Hardware Implementations and CAD Tools. , 2019, , . | | 6 |
| 8 | Efficient embedded architectures for fast-charge model predictive controller for battery cell management in electric vehicles. Eurasip Journal on Embedded Systems, 2018, 2018, . | 1.2 | 10 |
| 9 | Dynamic partial reconfigurable hardware architecture for principal component analysis on mobile and embedded devices. Eurasip Journal on Embedded Systems, 2017, 2017, . | 1.2 | 17 |
| 10 | FPGA-Based Reconfigurable Hardware for Compute Intensive Data Mining Applications. , 2011, , . | | 21 |
| 11 | Embedded hardware solution for principal component analysis. , 2011, , . | | 6 |
| 12 | Similarity Computation Using Reconfigurable Embedded Hardware. , 2009, , . | | 4 |
| 13 | Parallel Computation of Similarity Measures Using an FPGA-Based Processor Array. , 2008, , . | | 21 |
| 14 | Hardware acceleration for similarity computations of feature vectors. Canadian Journal of Electrical and Computer Engineering, 2008, 33, 21-30. | 2.0 | 10 |
| 15 | An Investigation of Chip-Level Hardware Support for Web Mining. , 2007, , . | | 7 |
| 16 | On-Chip Hardware Support for Similarity Measures. , 2007, , . | | 12 |