

Stefano Tonetta

List of Publications by Year in descending order

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44
papers

1,102
citations

516215

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h-index

476904

29
g-index

47
all docs

47
docs citations

47
times ranked

588
citing authors

#	ARTICLE	IF	CITATIONS
1	HUBCAP: A Novel Collaborative Approach to Model-Based Design of Cyber-Physical Systems. Lecture Notes in Networks and Systems, 2022, , 90-110.	0.5	0
2	Diagnosability of Fair Transition Systems. Artificial Intelligence, 2022, , 103725.	3.9	1
3	Asynchronous Composition of Local Interface LTL Properties. Lecture Notes in Computer Science, 2022, , 508-526.	1.0	3
4	Implicit Semi-Algebraic Abstraction for Polynomial Dynamical Systems. Lecture Notes in Computer Science, 2021, , 529-551.	1.0	3
5	Certifying proofs for SAT-based model checking. Formal Methods in System Design, 2021, 57, 178-210.	0.9	2
6	Assumption-Based Runtime Verification of Infinite-State Systems. Lecture Notes in Computer Science, 2021, , 207-227.	1.0	7
7	SMT-based satisfiability of first-order LTL with event freezing functions and metric operators. Information and Computation, 2020, 272, 104502.	0.5	8
8	A Cloud-based Collaboration Platform for Model-based Design of Cyber-Physical Systems. , 2020, , .		10
9	Safe Decomposition of Startup Requirements: Verification and Synthesis. Lecture Notes in Computer Science, 2020, , 155-172.	1.0	0
10	Extending nuXmv with Timed Transition Systems and Timed Temporal Properties. Lecture Notes in Computer Science, 2019, , 376-386.	1.0	18
11	Certifying Proofs for LTL Model Checking. , 2018, , .		13
12	Tightening the contract refinements of a system architecture. Formal Methods in System Design, 2018, 52, 88-116.	0.9	1
13	Infinite-state invariant checking with IC3 and predicate abstraction. Formal Methods in System Design, 2016, 49, 190-218.	0.9	28
14	A Temporal Logics Approach to Contract-Based Design. , 2016, , .		2
15	Infinite-State Liveness-to-Safety via Implicit Abstraction and Well-Founded Relations. Lecture Notes in Computer Science, 2016, , 271-291.	1.0	15
16	Tightening a Contract Refinement. Lecture Notes in Computer Science, 2016, , 386-402.	1.0	1
17	HyComp: An SMT-Based Model Checker for Hybrid Systems. Lecture Notes in Computer Science, 2015, , 52-67.	1.0	47
18	HRELT: A temporal logic for hybrid systems. Information and Computation, 2015, 245, 54-71.	0.5	16

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19	Safety assessment of AltaRica models via symbolic model checking. Science of Computer Programming, 2015, 98, 464-483.	1.5	24
20	Contracts-refinement proof system for component-based embedded systems. Science of Computer Programming, 2015, 97, 333-348.	1.5	54
21	Formal Design of Asynchronous Fault Detection and Identification Components using Temporal Epistemic Logic. Logical Methods in Computer Science, 2015, 11, .	0.4	10
22	Quantifier-free encoding of invariants for hybrid systems. Formal Methods in System Design, 2014, 45, 165-188.	0.9	6
23	The nuXmv Symbolic Model Checker. Lecture Notes in Computer Science, 2014, , 334-342.	1.0	268
24	Verifying LTL Properties of Hybrid Systems with K-Liveness. Lecture Notes in Computer Science, 2014, , 424-440.	1.0	18
25	IC3 Modulo Theories via Implicit Predicate Abstraction. Lecture Notes in Computer Science, 2014, , 46-61.	1.0	64
26	Requirements Refinement and Component Reuse. Advances in Systems Analysis, Software Engineering, and High Performance Computing Book Series, 2014, , 209-241.	0.5	6
27	Time-aware relational abstractions for hybrid systems. , 2013, , .		12
28	OCRA: A tool for checking the refinement of temporal contracts. , 2013, , .		82
29	Parameter synthesis with IC3. , 2013, , .		38
30	SMT-based scenario verification for hybrid systems. Formal Methods in System Design, 2013, 42, 46-66.	0.9	30
31	Validation of requirements for hybrid systems. ACM Transactions on Software Engineering and Methodology, 2012, 21, 1-34.	4.8	38
32	A Property-Based Proof System for Contract-Based Design. , 2012, , .		48
33	Symbolic systems, explicit properties: on hybrid approaches for LTL symbolic model checking. International Journal on Software Tools for Technology Transfer, 2011, 13, 319-335.	1.7	3
34	Formalizing requirements with object models and temporal constraints. Software and Systems Modeling, 2011, 10, 147-160.	2.2	17
35	Requirements Validation for Hybrid Systems. Lecture Notes in Computer Science, 2009, , 188-203.	1.0	40
36	Abstract Model Checking without Computing the Abstraction. Lecture Notes in Computer Science, 2009, , 89-105.	1.0	23

#	ARTICLE	IF	CITATIONS
37	Symbolic Compilation of PSL. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2008, 27, 1737-1750.	1.9	14
38	Object Models with Temporal Constraints. , 2008, , .		4
39	Property-Driven Partitioning for Abstraction Refinement. , 2007, , 389-404.		2
40	Boolean Abstraction for Temporal Logic Satisfiability. Lecture Notes in Computer Science, 2007, , 532-546.	1.0	28
41	Symbolic Systems, Explicit Properties: On Hybrid Approaches for LTL Symbolic Model Checking. Lecture Notes in Computer Science, 2005, , 350-363.	1.0	24
42	GSTE Is Partitioned Model Checking. Lecture Notes in Computer Science, 2004, , 229-241.	1.0	13
43	“More Deterministic” vs. “Smaller” Automata for Efficient LTL Model Checking. Lecture Notes in Computer Science, 2003, , 126-140.	1.0	49
44	Extended bounded response LTL: a new safety fragment for efficient reactive synthesis. Formal Methods in System Design, 0, , 1.	0.9	1