

# Alessandro Novello

## List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	A 1.01 NEF Low-Noise Amplifier Using Complementary Parametric Amplification. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 1065-1076.	5.4	4
2	A Second-Order Temperature-Compensated On-Chip R-RC Oscillator Achieving 7.93ppm/°C and 3.3pJ/Hz in -40°C to 125°C Temperature Range. , 2022, , .		9
3	A 1.25-GHz Fully Integrated DC-DC Converter Using Electromagnetically Coupled Class-D LC Oscillators. IEEE Journal of Solid-State Circuits, 2021, 56, 3639-3654.	5.4	6
4	17.3 A 1.25GHz Fully Integrated DC-DC Converter Using Electromagnetically Coupled Class-D LC Oscillators. , 2021, , .		4
5	A 2.3GHz Fully Integrated DC-DC Converter based on Electromagnetically Coupled Class-D LC Oscillators achieving 78.1% Efficiency in 22nm FDSOI CMOS. , 2021, , .		2
6	A 2.3-GHz Fully Integrated DC-DC Converter Based on Electromagnetically Coupled Class-D LC Oscillators Achieving 78.1% Efficiency in 22-nm FDSOI CMOS. IEEE Solid-State Circuits Letters, 2021, 4, 218-221.	2.0	0
7	A 8.7ppm/°C, 694nW, One-Point Calibrated RC Oscillator using a Nonlinearity-Aware Dual Phase-Locked Loop and DSM-Controlled Frequency-Locked Loops. , 2020, , .		8
8	An Energy-Efficient Low-Noise Complementary Parametric Amplifier Achieving 0.89 NEF. , 2020, , .		1
9	A 0.45/0.2-NEF/PEF 12-nV/√Hz Highly Configurable Discrete-Time Low-Noise Amplifier. IEEE Solid-State Circuits Letters, 2020, 3, 486-489.	2.0	8