

Ye Lu

List of Publications by Year in descending order

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Version: 2024-02-01

10
papers

138
citations

1684188

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h-index

1588992

8
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10
all docs

10
docs citations

10
times ranked

90
citing authors

#	ARTICLE	IF	CITATIONS
1	A 15-Bit Quadrature Digital Power Amplifier With Transformer-Based Complex-Domain Efficiency Enhancement. IEEE Journal of Solid-State Circuits, 2022, 57, 1610-1622.	5.4	10
2	Optimization of the Coupling Coefficient of the Inductive Link for Wireless Power Transfer to Biomedical Implants. International Journal of Antennas and Propagation, 2022, 2022, 1-12.	1.2	6
3	Analytical Model of CFET Parasitic Capacitance for Advanced Technology Nodes. IEEE Transactions on Electron Devices, 2022, 69, 936-941.	3.0	11
4	The Device and Circuit Level Benchmark of Si-Based Cold Source FETs for Future Logic Technology. IEEE Transactions on Electron Devices, 2022, 69, 3483-3489.	3.0	2
5	The Device Compact Model Based on Multi-gradient Neural Network and Its Application on MoS ₂ Field Effect Transistors. , 2022, , .		2
6	A Multiscale Simulation Framework for Steep-Slope Si Nanowire Cold Source FET. IEEE Transactions on Electron Devices, 2021, 68, 5455-5461.	3.0	2
7	EADNet: Efficient Asymmetric Dilated Network For Semantic Segmentation. , 2021, , .		7
8	Transistor Compact Model Based on Multigradient Neural Network and Its Application in SPICE Circuit Simulations for Gate-All-Around Si Cold Source FETs. IEEE Transactions on Electron Devices, 2021, 68, 4181-4188.	3.0	28
9	Wafer-scale functional circuits based on two dimensional semiconductors with fabrication optimized by machine learning. Nature Communications, 2021, 12, 5953.	12.8	42
10	Characterization and Modeling of Flicker Noise in FinFETs at Advanced Technology Node. IEEE Electron Device Letters, 2019, 40, 985-988.	3.9	28