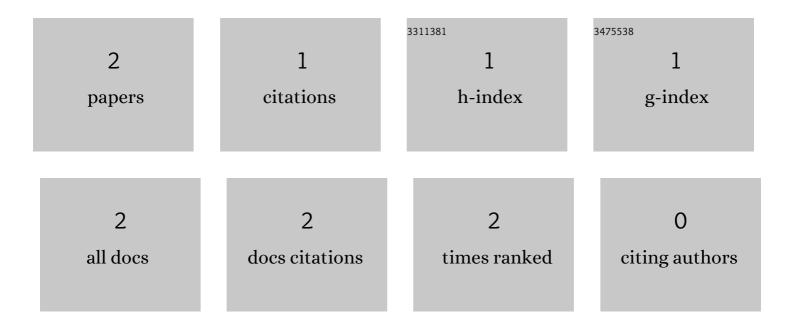
Mahditaheri

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/194034/publications.pdf Version: 2024-02-01



#	Article	IF	CITATIONS
1	Strengthened 32â€bit AES implementation: Architectural error correction configuration with a new voting scheme. IET Computers and Digital Techniques, 2021, 15, 395.	1.2	1
2	Hardware acceleration of the novel two dimensional Burrowsâ€Wheeler Aligner algorithm with maximal exact matches seed extension bernel. IET Circuits, Devices and Systems, 2021, 15, 94,103	1.4	0

Hardware acceleration of the novel two dimensional Burrowsâ€Wheeler Aligner algorithm with maximal exact matches seed extension kernel. IET Circuits, Devices and Systems, 2021, 15, 94-103. 2