

Houman Homayoun

List of Publications by Citations

Source: <https://exaly.com/author-pdf/1870884/houman-homayoun-publications-by-citations.pdf>

Version: 2024-04-27

This document has been generated based on the publications and citations recorded by exaly.com. For the latest version of this publication list, visit the link given above.

The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

57
papers

397
citations

11
h-index

16
g-index

69
ext. papers

619
ext. citations

2.1
avg, IF

4.09
L-index

#	Paper	IF	Citations
57	2SMaRT: A Two-Stage Machine Learning-Based Approach for Run-Time Specialized Hardware-Assisted Malware Detection 2019 ,		33
56	Ensemble Learning for Effective Run-Time Hardware-Based Malware Detection: A Comprehensive Analysis and Classification 2018 ,		30
55	Energy-efficient acceleration of big data analytics applications using FPGAs 2015 ,		28
54	Analyzing Hardware Based Malware Detectors 2017 ,		27
53	Adversarial Attack on Microarchitectural Events based Malware Detectors 2019 ,		19
52	Machine Learning-Based Approaches for Energy-Efficiency Prediction and Scheduling in Composite Cores Architectures 2017 ,		19
51	Accelerating Machine Learning Kernel in Hadoop Using FPGAs 2015 ,		18
50	Accelerating Big Data Analytics Using FPGAs 2015 ,		15
49	MeNa: A memory navigator for modern hardware in a scale-out environment 2017 ,		12
48	An Energy-Efficient Programmable Manycore Accelerator for Personalized Biomedical Applications. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2018 , 26, 96-109	2.6	12
47	SCARF: Detecting Side-Channel Attacks at Real-time using Low-level Hardware Features 2020 ,		11
46	On the Complexity Reduction of Dense Layers from $O(N^2)$ to $O(N \log N)$ with Cyclic Sparsely Connected Layers 2019 ,		10
45	LUT-Lock: A Novel LUT-Based Logic Obfuscation for FPGA-Bitstream and ASIC-Hardware Protection 2018 ,		10
44	Special Session: Advances and Throwbacks in Hardware-Assisted Security 2018 ,		10
43	Characterizing Hadoop applications on microservers for performance and energy efficiency optimizations 2016 ,		9
42	Lightweight Node-level Malware Detection and Network-level Malware Confinement in IoT Networks 2019 ,		8
41	Energy-aware and Machine Learning-based Resource Provisioning of In-Memory Analytics on Cloud 2018 ,		8

40	Architecture Exploration for Energy-Efficient Embedded Vision Applications: From General Purpose Processor to Domain Specific Accelerator 2016 ,		7
39	Security and Complexity Analysis of LUT-based Obfuscation: From Blueprint to Reality 2019 ,		7
38	Benchmarking the Capabilities and Limitations of SAT Solvers in Defeating Obfuscation Schemes 2018 ,		7
37	Energy-Efficient Hardware for Language Guided Reinforcement Learning 2020 ,		6
36	Comprehensive Evaluation of Machine Learning Countermeasures for Detecting Microarchitectural Side-Channel Attacks 2020 ,		6
35	Smart Grid on Chip: Work Load-Balanced On-Chip Power Delivery. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2017 , 25, 2538-2551	2.6	5
34	Power conversion efficiency-aware mapping of multithreaded applications on heterogeneous architectures: A comprehensive parameter tuning 2018 ,		5
33	Realizing complexity-effective on-chip power delivery for many-core platforms by exploiting optimized mapping 2015 ,		5
32	Automatic Detection of Respiratory Symptoms Using a Low Power Multi-Input CNN Processor. <i>IEEE Design and Test</i> , 2021 , 1-1	1.4	5
31	LESS: Big data sketching and Encryption on low power platform 2017 ,		4
30	Big vs little core for energy-efficient Hadoop computing 2017 ,		4
29	Power and performance characterization, analysis and tuning for energy-efficient edge detection on atom and ARM based platforms 2015 ,		4
28	Hybrid-shield 2020 ,		4
27	. <i>IEEE Access</i> , 2020 , 8, 138508-138528	3.5	4
26	Recent Advancements in Microarchitectural Security: Review of Machine Learning Countermeasures 2020 ,		4
25	Towards Accurate Run-Time Hardware-Assisted Stealthy Malware Detection: A Lightweight, yet Effective Time Series CNN-Based Approach. <i>Cryptography</i> , 2021 , 5, 28	1.9	3
24	ICNN. <i>Transactions on Embedded Computing Systems</i> , 2020 , 18, 1-27	1.8	3
23	Cyclic Sparsely Connected Architectures for Compact Deep Convolutional Neural Networks. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2021 , 1-14	2.6	3

22	ElasticCore: A Dynamic Heterogeneous Platform With Joint Core and Voltage/Frequency Scaling. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2018 , 26, 249-261	2.6	2
21	Compressive Sensing on Storage Data: An Effective Solution to Alleviate I/O Bottleneck in Data-Intensive Workloads 2018 ,		2
20	Scheduling multithreaded applications onto heterogeneous composite cores architecture 2017 ,		2
19	Enabling dynamic heterogeneity through core-on-core stacking 2014 ,		2
18	Cloak & Co-locate: Adversarial Railroading of Resource Sharing-based Attacks on the Cloud 2021 ,		2
17	Deep graph transformation for attributed, directed, and signed networks. <i>Knowledge and Information Systems</i> , 2021 , 63, 1305-1337	2.4	2
16	Comparative analysis of hybrid Magnetic Tunnel Junction and CMOS logic circuits 2016 ,		2
15	2019 ,		2
14	Ontology-Driven Framework for Trend Analysis of Vulnerabilities and Impacts in IoT Hardware 2021 ,		2
13	A Hardware Accelerator for Language Guided Reinforcement Learning. <i>IEEE Design and Test</i> , 2021 , 1-1	1.4	2
12	Adaptive Performance Modeling of Data-intensive Workloads for Resource Provisioning in Virtualized Environment. <i>ACM Transactions on Modeling and Performance Evaluation of Computing Systems</i> , 2021 , 5, 1-24	0.8	2
11	Resource-Efficient Wearable Computing for Real-Time Reconfigurable Machine Learning: A Cascading Binary Classification 2019 ,		1
10	Sequence-crafter 2019 ,		1
9	Data Flow Obfuscation: A New Paradigm for Obfuscating Circuits. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2021 , 29, 643-656	2.6	1
8	Comparative analysis of robustness of spin transfer torque based look up tables under process variations 2016 ,		1
7	From Cryptography to Logic Locking: A Survey on the Architecture Evolution of Secure Scan Chains. <i>IEEE Access</i> , 2021 , 9, 73133-73151	3.5	1
6	AVATAR: NN-Assisted Variation Aware Timing Analysis and Reporting for Hardware Trojan Detection. <i>IEEE Access</i> , 2021 , 9, 92881-92900	3.5	1
5	Enabling Micro AI for Securing Edge Devices at Hardware Level. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , 2021 , 11, 803-815	5.2	1

4	A Neural Network-based Cognitive Obfuscation Towards Enhanced Logic Locking. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2021 , 1-1	2.5	0
3	Security Threats in Cloud Rooted from Machine Learning-Based Resource Provisioning Systems. <i>Communications in Computer and Information Science</i> , 2022 , 22-32	0.3	
2	Cyclic Sparsely Connected Architectures: From foundations to applications. <i>IEEE Solid-State Circuits Magazine</i> , 2021 , 13, 64-76	1.5	
1	Imitating Functional Operations for Mitigating Side-Channel Leakage. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2021 , 1-1	2.5	