

# Divya Pathak

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/1862778/publications.pdf>

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10  
papers

54  
citations

2258059

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2550090

3  
g-index

10  
all docs

10  
docs citations

10  
times ranked

50  
citing authors

#	ARTICLE	IF	CITATIONS
1	ElasticCore. , 2015, , .		18
2	Smart Grid on Chip: Work Load-Balanced On-Chip Power Delivery. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 2538-2551.	3.1	10
3	Realizing complexity-effective on-chip power delivery for many-core platforms by exploiting optimized mapping. , 2015, , .		7
4	Energy efficient on-chip power delivery with run-time voltage regulator clustering. , 2016, , .		6
5	Power conversion efficiency-aware mapping of multithreaded applications on heterogeneous architectures: A comprehensive parameter tuning. , 2018, , .		5
6	On-Chip Power Supply Noise Suppression Through Hyperabrupt Junction Varactors. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2230-2240.	3.1	4
7	ElasticCore: A Dynamic Heterogeneous Platform With Joint Core and Voltage/Frequency Scaling. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 249-261.	3.1	3
8	Run-time voltage detection circuit for 3-D IC power delivery. , 2014, , .		1
9	Evolving On-Chip Power Delivery through Particle Swarm Optimization. , 2019, , .		0
10	Applying Swarm Intelligence to Distributed On-Chip Power Management. , 2019, , .		0