

List of Publications by Year in descending order

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53
papers

2,044
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39
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all docs

55
docs citations

55
times ranked

1733
citing authors

#	ARTICLE	IF	CITATIONS
1	Toward reliable biodiversity dataset references. Ecological Informatics, 2020, 59, 101132.	5.2	10
2	PRAGMA€NT: An International SDN testbed for cyberinfrastructure in the Pacific Rim. Concurrency Computation Practice and Experience, 2017, 29, e4138.	2.2	8
3	1A study on big data I/O performance with modern storage systems. , 2017, , .		1
4	Workforce-efficient consensus in crowdsourced transcription of biocollections information. Future Generation Computer Systems, 2016, 56, 526-536.	7.5	10
5	Digitization of Biodiversity Collections Reveals Biggest Data on Biodiversity. BioScience, 2015, 65, 841-842.	4.9	150
6	Reducing the Migration Times of Multiple VMs on WANs Using a Feedback Controller. , 2013, , .		7
7	On the use of virtualization technologies to support uninterrupted IT services: A case study with lessons learned from the Great East Japan Earthquake. , 2012, , .		5
8	Telecenter for secure, remote, collaborative child fatality review. Injury Prevention, 2012, 18, 399-404.	2.4	2
9	Dynamically Repairing and Replacing Neural Networks: Using Hybrid Computational and Biological Tools. IEEE Pulse, 2012, 3, 57-59.	0.3	13
10	On the Reliability of Computational Structures Using Majority Logic. IEEE Nanotechnology Magazine, 2011, 10, 1099-1112.	2.0	29
11	Towards closed-loop brain-machine experiments across wide-area networks. , 2011, , .		0
12	A multi-objective approach to virtual machine management in datacenters. , 2011, , .		61
13	Reliability evaluation of logic circuits using probabilistic gate models. Microelectronics Reliability, 2011, 51, 468-476.	1.7	103
14	User-level virtual networking mechanisms to support virtual machine migration over multiple clouds. , 2010, , .		9
15	Design of the FutureGrid experiment management framework. , 2010, , .		42
16	Cyber-Workstation for Computational Neuroscience. Frontiers in Neuroengineering, 2010, 2, 17.	4.8	6
17	Coadaptive Brain€Machine Interface via Reinforcement Learning. IEEE Transactions on Biomedical Engineering, 2009, 56, 54-64.	4.2	144
18	Time€mode circuits for analog computation. International Journal of Circuit Theory and Applications, 2009, 37, 631-659.	2.0	31

#	ARTICLE	IF	CITATIONS
19	User-Level Virtual Network Support for Sky Computing. , 2009, , .		8
20	Sky Computing. IEEE Internet Computing, 2009, 13, 43-51.	3.3	210
21	Autonomic resource management in virtualized data centers using fuzzy logic-based approaches. Cluster Computing, 2008, 11, 213-227.	5.0	97
22	Technology and Signal Processing for Brain-Machine Interfaces. IEEE Signal Processing Magazine, 2008, 25, 29-40.	5.6	37
23	CloudBLAST: Combining MapReduce and Virtualization on Distributed Resources for Bioinformatics Applications. , 2008, , .		214
24	NBTI tolerant microarchitecture design in the presence of process variation. , 2008, , .		17
25	An Analysis of Microarchitecture Vulnerability to Soft Errors on Simultaneous Multithreaded Architectures. , 2007, , .		8
26	From virtualized resources to virtual computing grids: the In-VIGO system. Future Generation Computer Systems, 2005, 21, 896-909.	7.5	148
27	Bifurcations and Fundamental Error Bounds for Fault-Tolerant Computations. IEEE Nanotechnology Magazine, 2005, 4, 395-402.	2.0	36
28	Markov Chains and Probabilistic Computation—A General Framework for Multiplexed Nanoelectronic Systems. IEEE Nanotechnology Magazine, 2005, 4, 194-205.	2.0	17
29	Morphology and Mechanical Properties of Surfactant Aggregates at Water-Silica Interfaces: Molecular Dynamics Simulations. Langmuir, 2005, 21, 5337-5342.	3.5	33
30	Seamless Access to Decentralized Storage Services in Computational Grids via a Virtual File System. Cluster Computing, 2004, 7, 113-122.	5.0	6
31	Grid-computing portals and security issues. Journal of Parallel and Distributed Computing, 2003, 63, 1006-1014.	4.1	32
32	Generation of injective and reversible modular mappings. IEEE Transactions on Parallel and Distributed Systems, 2003, 14, 1-12.	5.6	9
33	Characterization and Parallelization of Decision-Tree Induction. Journal of Parallel and Distributed Computing, 2001, 61, 322-349.	4.1	9
34	Punch: web portal for running tools. IEEE Micro, 2000, 20, 38-47.	1.8	35
35	Work-efficient routing algorithms for rearrangeable symmetrical networks. IEEE Transactions on Parallel and Distributed Systems, 1999, 10, 733-741.	5.6	17
36	An evaluation of implementations of the CMB parallel simulation algorithm on distributed memory multicomputers. Journal of Systems Architecture, 1998, 44, 519-545.	4.3	3

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37	A formalism to structure and parallelize the integration of cooperative engineering design tasks. IIE Transactions, 1998, 30, 1-15.	2.1	15
38	Communication-Minimal Partitioning and Data Alignment for Affine Nested Loops. Computer Journal, 1997, 40, 302-310.	2.4	1
39	Efficient techniques for performing an irregular computation on distributed memory machines. International Journal of Systems Science, 1997, 28, 1101-1113.	5.5	0
40	A fast VLSI-efficient self-routing permutation network. IEEE Transactions on Computers, 1995, 44, 448-453.	3.4	3
41	Frames: a simple characterization of permutations realized by frequently used networks. IEEE Transactions on Computers, 1995, 44, 695-697.	3.4	9
42	On the design of inphase and quadrature filters for delay compensation. IEEE Transactions on Signal Processing, 1994, 42, 2501-2503.	5.3	1
43	On loop transformations for generalized cycle shrinking. IEEE Transactions on Parallel and Distributed Systems, 1994, 5, 193-204.	5.6	20
44	Independent partitioning of algorithms with uniform dependencies. IEEE Transactions on Computers, 1992, 41, 190-206.	3.4	48
45	Destination tag routing techniques based on a state model for the IADM network. IEEE Transactions on Computers, 1992, 41, 274-285.	3.4	22
46	On the relationship between two systolic array design methodologies. IEEE Transactions on Computers, 1992, 41, 1589-1593.	3.4	5
47	Time optimal linear schedules for algorithms with uniform dependencies. IEEE Transactions on Computers, 1991, 40, 723-742.	3.4	112
48	A modula-2-like systems programming language and its implementation. Microprocessing and Microprogramming, 1991, 32, 625-634.	0.2	0
49	A taxonomy of reconfiguration techniques for fault-tolerant processor arrays. Computer, 1990, 23, 55-69.	1.1	109
50	The full-use-of-suitable-spares (FUSS) approach to hardware reconfiguration for fault-tolerant processor arrays. IEEE Transactions on Computers, 1990, 39, 564-571.	3.4	43
51	On the performance of combined data flow and control flow systems: Experiments using two iterative algorithms. Journal of Parallel and Distributed Computing, 1988, 5, 359-382.	4.1	0
52	A multimicroprocessor architecture for real-time computation of a class of DFT algorithms. IEEE Transactions on Acoustics, Speech, and Signal Processing, 1986, 34, 1301-1309.	2.0	10
53	Parallelism detection and transformation techniques useful for VLSI algorithms. Journal of Parallel and Distributed Computing, 1985, 2, 277-301.	4.1	72