

Ravishankar Iyer

List of Publications by Year in descending order

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7
papers

722
citations

2682572

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2917675

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g-index

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7
docs citations

7
times ranked

459
citing authors

#	ARTICLE	IF	CITATIONS
1	Advances in Microprocessor Cache Architectures Over the Last 25 Years. IEEE Micro, 2021, 41, 78-88.	1.8	5
2	A Systematic and Realistic Network-on-Chip Traffic Modeling and Generation Technique for Emerging Many-Core Systems. IEEE Transactions on Multi-Scale Computing Systems, 2018, 4, 113-126.	2.4	6
3	Neural Cache: Bit-Serial In-Cache Acceleration of Deep Neural Networks. , 2018, , .		220
4	Cache QoS: From concept to reality in the Intel® Xeon® processor E5-2600 v3 product family. , 2016, , .		92
5	NCID. , 2010, , .		17
6	QoS policies and architecture for cache/memory in CMP platforms. , 2007, , .		159
7	CQoS. , 2004, , .		223