Ravishankar Iyer

List of Publications by Year in descending order

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Version: 2024-02-01

2682572 2917675 7 722 2 2 citations g-index h-index papers 7 7 7 459 citing authors docs citations times ranked all docs

#	Article	IF	CITATIONS
1	CQoS., 2004,,.		223
2	Neural Cache: Bit-Serial In-Cache Acceleration of Deep Neural Networks. , 2018, , .		220
3	QoS policies and architecture for cache/memory in CMP platforms. , 2007, , .		159
4	Cache QoS: From concept to reality in the Intel® Xeon® processor E5-2600 v3 product family. , 2016, , .		92
5	NCID., 2010,,.		17
6	A Systematic and Realistic Network-on-Chip Traffic Modeling and Generation Technique for Emerging Many-Core Systems. IEEE Transactions on Multi-Scale Computing Systems, 2018, 4, 113-126.	2.4	6
7	Advances in Microprocessor Cache Architectures Over the Last 25 Years. IEEE Micro, 2021, 41, 78-88.	1.8	5