

Kari Stadius

List of Publications by Year in descending order

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papers

520
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933264

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48
docs citations

48
times ranked

437
citing authors

#	ARTICLE	IF	CITATIONS
1	Full-Duplex OFDM Radar With LTE and 5G NR Waveforms: Challenges, Solutions, and Measurements. IEEE Transactions on Microwave Theory and Techniques, 2019, 67, 4042-4054.	2.9	160
2	Adaptive Nonlinear RF Cancellation for Improved Isolation in Simultaneous Transmit&Receive Systems. IEEE Transactions on Microwave Theory and Techniques, 2018, 66, 2299-2312.	2.9	66
3	A Programmable 0.7&2.7 GHz Direct Δ Receiver in 40 nm CMOS. IEEE Journal of Solid-State Circuits, 2015, 50, 644-655.	3.5	35
4	Digital Interpolating Phase Modulator for Wideband Outphasing Transmitters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 705-715.	3.5	26
5	A 1.5&1.9-GHz All-Digital Tri-Phasing Transmitter With an Integrated Multilevel Class-D Power Amplifier Achieving 100-MHz RF Bandwidth. IEEE Journal of Solid-State Circuits, 2019, 54, 1517-1527.	3.5	25
6	13.5 A 0.35-to-2.6GHz multilevel outphasing transmitter with a digital interpolating phase modulator enabling up to 400MHz instantaneous bandwidth. , 2017, , .		24
7	Passive Intermodulation in Simultaneous Transmit&Receive Systems: Modeling and Digital Cancellation Methods. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 3633-3652.	2.9	21
8	Reference Receiver Enhanced Digital Linearization of Wideband Direct-Conversion Receivers. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 607-620.	2.9	20
9	A 0.7&2.7-GHz Blocker-Tolerant Compact-Size Single-Antenna Receiver for Wideband Mobile Applications. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 3339-3349.	2.9	14
10	Modeling and Joint Mitigation of TX and RX Nonlinearity-Induced Receiver Desensitization. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 2427-2442.	2.9	12
11	Performance Analysis of Frequency-Reconfigurable Antenna Cluster With Integrated Radio Transceivers. IEEE Antennas and Wireless Propagation Letters, 2018, 17, 756-759.	2.4	11
12	A 0.6&4.0 GHz RF-Resampling Beamforming Receiver With Frequency-Scaling True-Time-Delays up to Three Carrier Cycles. IEEE Solid-State Circuits Letters, 2020, 3, 234-237.	1.3	11
13	True-Time-Delay Beamforming Receiver With RF Re-Sampling. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4457-4469.	3.5	11
14	A 100&750 MS/s 11-Bit Time-to-Digital Converter With Cyclic-Coupled Ring Oscillator. IEEE Access, 2021, 9, 48147-48156.	2.6	10
15	Next-Generation RF Front-End Design Methods for Direct Δ Receivers. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2015, 5, 514-524.	2.7	7
16	A 1.5&5-GHz Integrated RF Transmitter Front End for Active Matching of an Antenna Cluster. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 4728-4739.	2.9	7
17	A 30-dBm Class-D Power Amplifier with On/Off Logic for an Integrated Tri-Phasing Transmitter in 28-nm CMOS. , 2018, , .		5
18	A 5.8-Gbps low-noise scalable low-voltage signaling serial link transmitter for MIPI M-PHY in 40-nm CMOS. Analog Integrated Circuits and Signal Processing, 2015, 82, 159-169.	0.9	4

#	ARTICLE	IF	CITATIONS
19	Tri-Phasing Modulation for Efficient and Wideband Radio Transmitters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3085-3098.	3.5	4
20	A common-gate common-source low noise amplifier based RF front end with selective input impedance matching for blocker-resilient receivers. International Journal of Circuit Theory and Applications, 2018, 46, 1427-1442.	1.3	4
21	Full-Duplex Wireless Transceiver Self-Interference Cancellation Through FD-SOI Buried-Gate Signaling. , 2018, , .		4
22	A Class-D Tri-Phasing CMOS Power Amplifier With an Extended Marchand-Balun Power Combiner. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 1022-1034.	2.9	4
23	Data Conversion With Subgate-Delay Time Resolution Using Cyclic-Coupled Ring Oscillators. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 203-214.	2.1	4
24	Design and Implementation of a Wideband Digital Interpolating Phase Modulator RF Front-End. , 2018, , .		3
25	Spectral Effects of Discrete-Time Amplitude Levels in Digital-Intensive Wideband Radio Transmitters. , 2018, , .		3
26	A 2.5 GHz Beamsteering Receiver IC With 4-Element Vivaldi Antenna Array. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 3852-3860.	2.9	3
27	A Six-Phase Two-Stage Blocker-Tolerant Harmonic-Rejection Receiver. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 1964-1976.	2.9	3
28	Electrical properties of CVD-graphene FETs. , 2011, , .		2
29	RF Input Matching Design for Closed-Loop Direct Delta-Sigma Receivers. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 1370-1379.	2.9	2
30	A Systematic Design Method for Direct Delta-Sigma Receivers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 2389-2402.	3.5	2
31	A Blocker-Tolerant Two-Stage Harmonic-Rejection RF Front-End. , 2019, , .		2
32	A Delay-Based LO Phase-Shifting Generator for a 2-5GHz Beamsteering Receiver in 28nm CMOS. , 2019, , .		2
33	A Frequency Tunable MIMO Antenna Cluster with Transmitter IC. , 2021, , .		2
34	A 5.4-GHz 2/3/4-Modulus Fractional Frequency Divider Circuit in 28-nm CMOS. , 2021, , .		2
35	A High-Speed DSP Engine for First-Order Hold Digital Phase Modulation in 28-nm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1959-1963.	2.2	1
36	A 3-43ps time-delay cell for LO phase-shifting in 1.5-6.5GHz beamsteering receiver. , 2018, , .		1

#	ARTICLE	IF	CITATIONS
37	Quantized Polar Transmitters for Power Efficient Massive MIMO Systems. IEEE Wireless Communications Letters, 2021, 10, 859-863.	3.2	1
38	Fully Digital On-Chip Wideband Background Calibration for Channel Mismatches in Time-Interleaved Time-Based ADCs. IEEE Solid-State Circuits Letters, 2022, 5, 9-12.	1.3	1
39	Energy-Efficient Cyclic-Coupled Ring Oscillator With Delay-Based Injection Locking. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 3709-3713.	2.2	1
40	Implementation of all-digital wideband RF frequency synthesizers in 65-nm CMOS technology. , 2011, , .		0
41	A programmable DSP front-end for all-digital 4G transmitters. , 2013, , .		0
42	A 20-60GHz Digitally Controlled Composite Oscillator for 5G. , 2018, , .		0
43	Quantization noise upconversion effects in mixer-first direct delta-sigma receivers. International Journal of Circuit Theory and Applications, 2019, 47, 1893-1906.	1.3	0
44	A wideband blocker-resilient direct delta sigma receiver with selective input-impedance matching. Analog Integrated Circuits and Signal Processing, 2020, 103, 195-207.	0.9	0
45	A Compact Low-Power 140-GHz Low-Noise Amplifier with 19-dB Gain and 7-dB NF. , 2021, , .		0
46	A Transmitter IC with Supply Tuning for Frequency-Reconfigurable Antenna Cluster. , 2021, , .		0
47	Characterization of an Antenna Cluster and Transmitter IC with a Modulated Signal. , 2021, , .		0
48	Leveraging frequency agility of an MIMO antenna cluster with a transmitter IC. International Journal of Microwave and Wireless Technologies, 0, , 1-10.	1.5	0