

# Jintao Yu

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/1803868/publications.pdf>

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9  
papers

141  
citations

2682572

2  
h-index

2917675

2  
g-index

9  
all docs

9  
docs citations

9  
times ranked

161  
citing authors

| # | ARTICLE                                                                                                                                             | IF  | CITATIONS |
|---|-----------------------------------------------------------------------------------------------------------------------------------------------------|-----|-----------|
| 1 | A Survey on Memory-centric Computer Architectures. ACM Journal on Emerging Technologies in Computing Systems, 2022, 18, 1-50.                       | 2.3 | 7         |
| 2 | APmap: An Open-Source Compiler for Automata Processors. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2021, , 1-1. | 2.7 | 0         |
| 3 | Skeleton-Based Synthesis Flow for Computation-in-Memory Architectures. IEEE Transactions on Emerging Topics in Computing, 2020, 8, 545-558.         | 4.6 | 6         |
| 4 | The Power of Computation-in-Memory Based on Memristive Devices. , 2020, , .                                                                         |     | 4         |
| 5 | Time-division Multiplexing Automata Processor. , 2019, , .                                                                                          |     | 8         |
| 6 | Enhanced Scouting Logic: A Robust Memristive Logic Design Scheme. , 2019, , .                                                                       |     | 8         |
| 7 | Memristive devices for computation-in-memory. , 2018, , .                                                                                           |     | 29        |
| 8 | On the robustness of memristor based logic gates. , 2017, , .                                                                                       |     | 4         |
| 9 | Scouting Logic: A Novel Memristor-Based Logic Design for Resistive Computing. , 2017, , .                                                           |     | 75        |