

Hao Yu

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

162
papers

1,753
citations

24
h-index

32
g-index

197
ext. papers

2,188
ext. citations

2.8
avg, IF

4.85
L-index

#	Paper	IF	Citations
162	A 13.5-Gb/s 140-GHz Silicon Redriver Exploiting Metadevices for Short-Range OOK Communications. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2022 , 70, 239-253	4.1	5
161	Energy-Efficient and Error-Resilient Cognitive I/O for 3-D-Integrated Manycore Microprocessors. <i>IEEE Design and Test</i> , 2020 , 1-1	1.4	
160	3D FEM Analysis of High-Frequency AlN-Based PMUT Arrays on Cavity SOI. <i>Sensors</i> , 2019 , 19,	3.8	12
159	Tensor-Solver for Deep Neural Network. <i>Computer Architecture and Design Methodologies</i> , 2019 , 63-105	0.2	
158	A 510-Gb/s 12.5-mW Source Synchronous I/O Interface With 3-D Flip Chip Package. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2019 , 66, 555-568	3.9	5
157	. <i>IEEE Nanotechnology Magazine</i> , 2018 , 17, 645-656	2.6	16
156	CMOS Terahertz Sensor 2018 , 76-105		
155	CMOS Dual-mode pH-Image Sensor 2018 , 169-185		
154	CMOS Impedance Sensor 2018 , 60-75		
153	DNA Sequencing 2018 , 213-230		
152	Cell Counting 2018 , 231-257		
151	2018 ,		5
150	CMOS 3-D-Integrated MEMS Sensor 2018 , 126-141		
149	CMOS Image Sensor 2018 , 142-168		
148	A 7.52-dB Noise Figure 128.75-132.25-GHz Super-Regenerative Receiver With 0.615-fW/ $\sqrt{\text{Hz}}$ NEP by Coupled Oscillator Networks for Portable Imaging System in 65-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2018 , 66, 4095-4107	4.1	2
147	A Multiagent Minority-Game-Based Demand-Response Management of Smart Buildings Toward Peak Load Reduction. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2017 , 36, 573-585	2.5	35
146	A Scalable Network-on-Chip Microprocessor With 2.5D Integrated Memory and Accelerator. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2017 , 64, 1432-1443	3.9	26

145	Distributed In-Memory Computing on Binary Memristor-Crossbar for Machine Learning. <i>Studies in Computational Intelligence</i> , 2017 , 275-304	0.8	2
144	Monolithic Semiconductor Lasers with Dynamically Tunable Linear-to-Circular Polarization. <i>ACS Photonics</i> , 2017 , 4, 517-524	6.3	17
143	Distributed In-Memory Computing on Binary RRAM Crossbar. <i>ACM Journal on Emerging Technologies in Computing Systems</i> , 2017 , 13, 1-18	1.7	26
142	An Energy-Efficient Digital ReRAM-Crossbar-Based CNN With Bitwise Parallelism. <i>IEEE Journal on Exploratory Solid-State Computational Devices and Circuits</i> , 2017 , 3, 37-46	2.4	46
141	An Energy-Efficient and Low-Crosstalk Sub-THz I/O by Surface Plasmonic Polariton Interconnect in CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2017 , 65, 2762-2774	4.1	24
140	Demand side management with consumer clusters in cyber-physical smart distribution system considering price-based and reward-based scheduling programs. <i>IET Cyber-Physical Systems: Theory and Applications</i> , 2017 , 2, 75-83	2.5	9
139	A Microfluidic Cytometer for Complete Blood Count With a 3.2-Megapixel, 1.1- μ m-Pitch Super-Resolution Image Sensor in 65-nm BSI CMOS. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2017 , 11, 794-803	5.1	15
138	An energy-efficient and high-throughput bitwise CNN on sneak-path-free digital ReRAM crossbar 2017 ,		12
137	LTNN: An energy-efficient machine learning accelerator on 3D CMOS-RRAM for layer-wise tensorized neural network 2017 ,		2
136	A Q-Learning Based Self-Adaptive I/O Communication for 2.5D Integrated Many-Core Microprocessor and Memory. <i>IEEE Transactions on Computers</i> , 2016 , 65, 1185-1196	2.5	9
135	A 512 \times 76 65-nm CMOS ISFET sensor for food safety screening with 123.8 mV/pH sensitivity and 0.01 pH resolution 2016 ,		2
134	On-chip sub-terahertz surface plasmon polariton transmission lines with mode converter in CMOS. <i>Scientific Reports</i> , 2016 , 6, 30063	4.9	22
133	Millimeter-Wave Sources at 60 and 140 GHz by Magnetic-Plasmon-Waveguide-Based In-Phase Coupled Oscillator Network in 65-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2016 , 64, 1560-1571	4.1	10
132	DW-AES: A Domain-Wall Nanowire-Based AES for High Throughput and Energy-Efficient Data Encryption in Non-Volatile Memory. <i>IEEE Transactions on Information Forensics and Security</i> , 2016 , 11, 2426-2440	8	31
131	A Zonotoped Macromodeling for Eye-Diagram Verification of High-Speed I/O Links With Jitter and Parameter Variations. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2016 , 35, 1040-1051	2.5	2
130	Distributed-neuron-network based machine learning on smart-gateway network towards real-time indoor data analytics 2016 ,		6
129	Machine Learning Based Single-Frame Super-Resolution Processing for Lensless Blood Cell Counting. <i>Sensors</i> , 2016 , 16,	3.8	29
128	On-line machine learning accelerator on digital RRAM-crossbar 2016 ,		4

127	Non-Volatile In-Memory Computing by Spintronics 2016 , 2, 1-161		3
126	2016 ,		2
125	An Energy-efficient Adaptive Sub-THz Wireless Interconnect with MIMO-Beamforming between Cores and DRAMs 2016 ,		2
124	A 5-GS/s, 13-mW, 2-channel time-interleaved asynchronous ADC in 65nm CMOS 2016 ,		2
123	. <i>IEEE Nanotechnology Magazine</i> , 2015 , 14, 998-1012	2.6	56
122	A 2.5-D Memory-Logic Integration With Data-Pattern-Aware Memory Controller. <i>IEEE Design and Test</i> , 2015 , 32, 1-10	1.4	8
121	An ultra-low power CMOS image sensor with on-chip energy harvesting and power management capability. <i>Sensors</i> , 2015 , 15, 5531-54	3.8	26
120	A 64B4 1200fps dual-mode CMOS ion-image sensor for accurate DNA sequencing 2015 ,		4
119	3D Many-Core Microprocessor Power Management by Space-Time Multiplexing Based Demand-Supply Matching. <i>IEEE Transactions on Computers</i> , 2015 , 64, 3022-3036	2.5	9
118	Optimizing Boolean embedding matrix for compressive sensing in RRAM crossbar 2015 ,		3
117	A Single-Frame Superresolution Algorithm for Lab-on-a-Chip Lensless Microfluidic Imaging. <i>IEEE Design and Test</i> , 2015 , 32, 32-40	1.4	13
116	A body-biasing of readout circuit for STT-RAM with improved thermal reliability 2015 ,		6
115	A 16-channel 24-V 1.8-mA power efficiency enhanced neural/muscular stimulator with exponentially decaying stimulation current 2015 ,		5
114	. <i>IEEE Transactions on Electron Devices</i> , 2015 , 62, 3012-3018	2.9	17
113	A robust recognition error recovery for micro-flow cytometer by machine-learning enhanced single-frame super-resolution processing. <i>The Integration VLSI Journal</i> , 2015 , 51, 208-218	1.4	8
112	A GPU-Accelerated Parallel Shooting Algorithm for Analysis of Radio Frequency and Microwave Integrated Circuits. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015 , 23, 480-492	2.6	6
111	A CMOS THz-sensing system towards label-free DNA sequencing 2015 ,		4
110	On-chip sub-terahertz surface plasmon polariton transmission lines in CMOS. <i>Scientific Reports</i> , 2015 , 5, 14853	4.9	44

109	A 23mW face recognition accelerator in 40nm CMOS with mostly-read 5T memory 2015,		9
108	CMOS sub-THz on-chip communication with SRR modulator and SPP interconnect 2015,		2
107	A Dual-Mode Large-Arrayed CMOS ISFET Sensor for Accurate and High-Throughput pH Sensing in Biomedical Diagnosis. <i>IEEE Transactions on Biomedical Engineering</i> , 2015 , 62, 2224-33	5	64
106	A high-sensitivity 135GHz millimeter-wave imager by compact split-ring-resonator in 65-nm CMOS. <i>Solid-State Electronics</i> , 2015 , 113, 54-60	1.7	1
105	CMOS sub-THz on-chip modulator by stacked split ring resonator with high-extinction ratio 2015,		4
104	Indoor positioning by distributed machine-learning based data analytics on smart gateway network 2015,		7
103	An energy efficient and low cross-talk CMOS sub-THz I/O with surface-wave modulator and interconnect 2015,		2
102	A 201 mV/pH, 375 fps and 512x76 CMOS ISFET sensor in 65nm CMOS technology 2015,		1
101	An energy-efficient non-volatile in-memory accelerator for sparse-representation based face recognition 2015,		4
100	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2014 , 61, 699-711	3.9	36
99	A 64x4 1200fps CMOS ion-image sensor with suppressed fixed-pattern-noise for accurate high-throughput DNA sequencing 2014,		1
98	A robustness optimization of SRAM dynamic stability by sensitivity-based reachability analysis 2014		1
97	A 54 to 62.8GHz PA with 95.2mW/mm ² output power density by 4x distributed in-phase power combining in 65nm CMOS 2014,		1
96	A 60.8x7GHz and 6.3mW injection-locked frequency divider with switching-inductor loaded transformer in 65nm CMOS 2014,		2
95	A 239x81 GHz CMOS Receiver With On-Chip Circular-Polarized Substrate Integrated Waveguide Antenna for Sub-Terahertz Imaging. <i>IEEE Transactions on Terahertz Science and Technology</i> , 2014 , 4, 686-695	3.4	39
94	A 32.5-GS/s two-channel time-interleaved CMOS sampler with switched-source follower based track-and-hold amplifier 2014,		1
93	Nonvolatile CBRAM-Crossbar-Based 3-D-Integrated Hybrid Memory for Data Retention. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2014 , 22, 957-970	2.6	14
92	Reachability-Based Robustness Verification and Optimization of SRAM Dynamic Stability Under Process Variations. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2014 , 33, 585-598	2.5	4

91	A thermal resilient integration of many-core microprocessors and main memory by 2.5D TSI I/Os 2014 ,			1
90	An overview of new design techniques for high performance CMOS millimeter-wave circuits 2014 ,			3
89	A 32.5-GS/s Sampler With Time-Interleaved Track-and-Hold Amplifier in 65-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2014 , 62, 3500-3511	4.1		12
88	A 239-281GHz Sub-THz imager with 100MHz resolution by CMOS direct-conversion receiver with on-chip circular-polarized SIW antenna 2014 ,			1
87	Reinforcement learning based self-adaptive voltage-swing adjustment of 2.5D I/Os for many-core microprocessor and memory communication 2014 ,			4
86	Portable resistive pulse-activated lens-free cell imaging system. <i>RSC Advances</i> , 2014 , 4, 56342-56345	3.7		15
85	An energy-efficient 2.5D through-silicon interposer I/O with self-adaptive adjustment of output-voltage swing 2014 ,			7
84	Variability-Aware Parametric Yield Estimation for Analog/Mixed-Signal Circuits: Concepts, Algorithms, and Challenges. <i>IEEE Design and Test</i> , 2014 , 31, 6-15	1.4		25
83	Energy efficient in-memory machine learning for data intensive image-processing by non-volatile domain-wall memory 2014 ,			14
82	High-Sensitivity CMOS Super-Regenerative Receiver with Quench-Controlled High-Q Metamaterial Resonator for Millimeter-Wave Imaging at 96 and 135 GHz. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2014 , 62, 3095-3106	4.1		12
81	A contact-imaging based microfluidic cytometer with machine-learning for single-frame super-resolution processing. <i>PLoS ONE</i> , 2014 , 9, e104539	3.7		47
80	High density CMOS electrode array for high-throughput and automated cell counting. <i>Sensors and Actuators B: Chemical</i> , 2013 , 181, 842-849	8.5		10
79	Design of Ultra-Low-Power 60-GHz Direct-Conversion Receivers in 65-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2013 , 61, 3360-3372	4.1		14
78	Stochastic Behavioral Modeling and Analysis for Analog/Mixed-Signal Circuits. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2013 , 32, 24-33	2.5		13
77	. <i>IEEE Design and Test</i> , 2013 , 30, 26-35	1.4		9
76	Reliable 3-D Clock-Tree Synthesis Considering Nonlinear Capacitive TSV Model With Electrical-Thermal-Mechanical Coupling. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2013 , 32, 1734-1747	2.5		28
75	A fractional-order RLGC model for Terahertz transmission line 2013 ,			11
74	A wideband 150GHz antenna by 3D-TSV based Composite Right/Left Handed Transmission Line for Sub-THz biomedical imaging 2013 ,			2

73	An ultralow-power memory-based big-data computing platform by nonvolatile domain-wall nanowire devices 2013 ,		10
72	A 53-to-73GHz power amplifier with 74.5mW/mm ² output power density by 2D differential power combining in 65nm CMOS 2013 ,		5
71	An efficient channel clustering and flow rate allocation algorithm for non-uniform microfluidic cooling of 3D integrated circuits. <i>The Integration VLSI Journal</i> , 2013 , 46, 57-68	1.4	10
70	SPECO: Stochastic Perturbation based Clock tree Optimization considering temperature uncertainty. <i>The Integration VLSI Journal</i> , 2013 , 46, 22-32	1.4	5
69	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2013 , 60, 37-50	3.9	10
68	A 2-D Distributed Power Combining by Metamaterial-Based Zero Phase Shifter for 60-GHz Power Amplifier in 65-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2013 , 61, 505-516	4.1	38
67	A high-frequency transimpedance amplifier for CMOS integrated 2D CMUT array towards 3D ultrasound imaging. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2013 , 2013, 101-4	0.9	2
66	A 96-GHz Oscillator by High-Q Differential Transmission Line loaded with Complementary Split-Ring Resonator in 65-nm CMOS. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2013 , 60, 127-131	3.5	13
65	Robust On-chip Signaling by Staggered and Twisted Bundle. <i>IEEE Design and Test</i> , 2013 , 1-1	1.4	
64	Design of High-Q Millimeter-Wave Oscillator by Differential Transmission Line Loaded With Metamaterial Resonator in 65-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2013 , 61, 1892-1902	4.1	26
63	A 75.7GHz to 102GHz rotary-traveling-wave VCO by tunable composite right /left hand T-line 2013 ,		5
62	Thermal-reliable 3D clock-tree synthesis considering nonlinear electrical-thermal-coupled TSV model 2013 ,		9
61	. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , 2013 , 3, 355-366	5.2	20
60	High-speed and low-power 2.5D I/O circuits for memory-logic-integration by through-silicon interposer 2013 ,		6
59	Peak power reduction and workload balancing by space-time multiplexing based demand-supply matching for 3D thousand-core microprocessor 2013 ,		5
58	Stable backward reachability correction for PLL verification with consideration of environmental noise induced jitter 2013 ,		2
57	IC-Package-System Integration Design 2013 , 341-412		3
56	80 GHz on-chip metamaterial resonator by differential transmission line loaded with split ring resonator. <i>Electronics Letters</i> , 2012 , 48, 1128-1130	1.1	14

55	Fast timing analysis of clock networks considering environmental uncertainty. <i>The Integration VLSI Journal</i> , 2012 , 45, 376-387	1.4	5
54	CMOS high density electrical impedance biosensor array for tumor cell detection. <i>Sensors and Actuators B: Chemical</i> , 2012 , 173, 903-907	8.5	40
53	A Parallel and Incremental Extraction of Variational Capacitance With Stochastic Geometric Moments. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2012 , 20, 1729-1737	2.6	4
52	Analysis and Modeling of Internal State Variables for Dynamic Effects of Nonvolatile Memory Devices. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2012 , 59, 1906-1918	3.9	44
51	SPICE simulator for hybrid CMOS memristor circuit and system 2012 ,		8
50	A Nanoelectromechanical-Switch-Based Thermal Management for 3-D Integrated Many-Core Memory-Processor System. <i>IEEE Nanotechnology Magazine</i> , 2012 , 11, 588-600	2.6	11
49	An 8mW ultra low power 60GHz direct-conversion receiver with 55dB gain and 4.9dB noise figure in 65nm CMOS 2012 ,		4
48	Design Exploration of Hybrid CMOS and Memristor Circuit by New Modified Nodal Analysis. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2012 , 20, 1012-1025	2.6	56
47	A 60GHz VCO with 25.8% tuning range by switching return-path in 65nm CMOS 2012 ,		5
46	Fast simulation of hybrid CMOS and STT-MTJ circuits with identified internal state variables 2012 ,		2
45	A 9606 1V ultra-low power CMOS image sensor for biomedical application 2012 ,		1
44	Design exploration of ultra-low power non-volatile memory based on topological insulator 2012 ,		3
43	High-speed CMOS image sensor for high-throughput lensless microfluidic imaging system 2012 ,		6
42	A Fast Non-Monte-Carlo Yield Analysis and Optimization by Stochastic Orthogonal Polynomials. <i>ACM Transactions on Design Automation of Electronic Systems</i> , 2012 , 17, 1-23	1.5	13
41	Decentralized agent based re-clustering for task mapping of tera-scale network-on-chip system 2012 ,		8
40	A new model of stacked transformers considering skin and substrate effects 2012 ,		1
39	Design exploration of 3D stacked non-volatile memory by conductive bridge based crossbar 2012 ,		7
38	An EScheduler-Based Data Dependence Analysis and Task Scheduling for Parallel Circuit Simulation. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2011 , 58, 702-706	3.5	24

37	NEMS based thermal management for 3D many-core system 2011 ,		2
36	A real-time cyber-physical energy management system for smart houses 2011 ,		6
35	A structured parallel periodic Arnoldi shooting algorithm for RF-PSS analysis based on GPU platforms 2011 ,		2
34	Stochastic analog circuit behavior modeling by point estimation method 2011 ,		16
33	Fast non-monte-carlo transient noise analysis for high-precision analog/RF circuits by stochastic orthogonal polynomials 2011 ,		4
32	Cyber-Physical Thermal Management of 3D Multi-Core Cache-Processor System with Microfluidic Cooling. <i>Journal of Low Power Electronics</i> , 2011 , 7, 110-121	1.2	25
31	3D IC Design Automation Considering Dynamic Power and Thermal Integrity 2011 , 335-350		
30	A robust periodic arnoldi shooting algorithm for efficient analysis of large-scale RF/MM ICs 2010 ,		3
29	Fast Analysis of a Large-Scale Inductive Interconnect by Block-Structure-Preserved Macromodeling. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2010 , 18, 1399-1411	2.6	8
28	Real-time thermal management of 3D multi-core system with fine-grained cooling control 2010 ,		1
27	A new modified nodal analysis for nano-scale memristor circuit simulation 2010 ,		3
26	A fast analog mismatch analysis by an incremental and stochastic trajectory piecewise linear macromodel 2010 ,		2
25	QuickYield 2010 ,		21
24	Dynamic power and thermal integrity in 3D integration 2009 ,		2
23	Allocating power ground vias in 3D ICs for simultaneous power and thermal integrity. <i>ACM Transactions on Design Automation of Electronic Systems</i> , 2009 , 14, 1-31	1.5	35
22	Fast analysis of nontree-clock network considering environmental uncertainty by parameterized and incremental macromodeling 2009 ,		2
21	Robust On-Chip Signaling by Staggered and Twisted Bundle. <i>IEEE Design and Test of Computers</i> , 2009 , 26, 92-104		2
20	. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2008 , 16, 1609-1619	2.6	31

19	Minimal skew clock embedding considering time variant temperature gradient 2007 ,		13
18	Off-chip decoupling capacitor allocation for chip package co-design. <i>Proceedings - Design Automation Conference, 2007</i> ,		5
17	Circuit-simulated obstacle-aware Steiner routing. <i>ACM Transactions on Design Automation of Electronic Systems, 2007</i> , 12, 1-18	1.5	3
16	Fast analysis of structured power grid by triangularization based structure preserving model order reduction 2006 ,		18
15	Circuit simulation based obstacle-aware Steiner routing 2006 ,		11
14	Thermal via allocation for 3D ICs considering temporally and spatially variant thermal power 2006 ,		5
13	Simultaneous power and thermal integrity driven via stapling in 3D ICs. <i>IEEE/ACM International Conference on Computer-Aided Design, Digest of Technical Papers, 2006</i> ,		13
12	Simultaneous Power and Thermal Integrity Driven Via Stapling in 3D ICs. <i>IEEE/ACM International Conference on Computer-Aided Design, Digest of Technical Papers, 2006</i> ,		6
11	A Fast Block Structure Preserving Model Order Reduction for Inverse Inductance Circuits. <i>IEEE/ACM International Conference on Computer-Aided Design, Digest of Technical Papers, 2006</i> ,		1
10	Circuit simulation based obstacle-aware Steiner routing. <i>Proceedings - Design Automation Conference, 2006</i> ,		3
9	Wideband passive multiport model order reduction and realization of RLCM circuits. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2006</i> , 25, 1496-1509	2.5	15
8	Thermal Via Allocation for 3D ICs Considering Temporally and Spatially Variant Thermal Power 2006 ,		9
7	A provably passive and cost-efficient model for inductive interconnects. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2005</i> , 24, 1283-1294	2.5	5
6	Quantitative model for the interecho time dependence of the CPMG relaxation rate in iron-rich gray matter. <i>Magnetic Resonance in Medicine, 2001</i> , 46, 159-65	4.4	21
5	A sparsified vector potential equivalent circuit model for massively coupled interconnects		1
4	Block structure preserving model order reduction		6
3	Wideband modeling of RF/analog circuits via hierarchical multi-point model order reduction		3
2	A wideband hierarchical circuit reduction for massively coupled interconnects		1

1 Staggered twisted-bundle interconnect for crosstalk and delay reduction

3