

Elham Moghaddam

List of Publications by Year in descending order

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docs citations

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79
citing authors

#	ARTICLE	IF	CITATIONS
1	Embedded Deterministic Test Points. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 2949-2961.	3.1	35
2	Logic BIST With Capture-Per-Clock Hybrid Test Points. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 1028-1041.	2.7	25
3	Embedded deterministic test points for compact cell-aware tests. , 2015, , .		21
4	Test point insertion in hybrid test compression/LBIST architectures. , 2016, , .		15
5	Hardware Protection via Logic Locking Test Points. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 3020-3030.	2.7	14
6	Time and Area Optimized Testing of Automotive ICs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 76-88.	3.1	10
7	On Test Points Enhancing Hardware Security. , 2016, , .		7