

Chika Tanaka

List of Publications by Year in descending order

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7
papers

27
citations

2682572

2
h-index

2272923

4
g-index

7
all docs

7
docs citations

7
times ranked

46
citing authors

#	ARTICLE	IF	CITATIONS
1	Implementation of TFET SPICE Model for Ultra-Low Power Circuit Analysis. IEEE Journal of the Electron Devices Society, 2016, 4, 273-277.	2.1	19
2	Spice-based performance analysis of ultra-low voltage Si nanowire CMOS circuits. , 2011, , .		3
3	Analysis of static noise margin improvement for low voltage SRAM composed of nano-scale MOSFETs with ideal subthreshold factor and small variability. Solid-State Electronics, 2015, 109, 58-62.	1.4	3
4	SPICE simulation of tunnel FET aiming at 32 kHz crystal-oscillator operation. IEICE Electronics Express, 2018, 15, 20171232-20171232.	0.8	1
5	Experimental Extraction of Impact of Depletion Capacitance on Low Frequency Noise in Sub-Micron nMOSFETs With Reverse Body Bias. IEEE Transactions on Semiconductor Manufacturing, 2020, 33, 146-149.	1.7	1
6	Analytical drain current and threshold voltage model and device design of short-channel Si nanowire transistors. Solid-State Electronics, 2013, 86, 27-31.	1.4	0
7	BSIM4 parameter extraction for tri-gate Si nanowire transistors. Microelectronics Journal, 2016, 47, 1-6.	2.0	0