

Wojciech ZajÄc

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	IPNES - Interpreted Petri Net for Embedded Systems. Procedia Computer Science, 2021, 192, 2012-2021.	2.0	7
2	Implementation of an example of Hierarchical Petri Net (HPN) in LAD language in TIA Portal. Procedia Computer Science, 2021, 192, 3657-3666.	2.0	6
3	On some aspects of Concurrent Control Processes Modelling and Implementation in LAD Diagram Language With Use of New Generation Engineering Software. Procedia Computer Science, 2020, 176, 2173-2183.	2.0	12
4	Finite State Machine Based Modelling of Discrete Control Algorithm in LAD Diagram Language With Use of New Generation Engineering Software. Procedia Computer Science, 2019, 159, 2560-2569.	2.0	22
5	Decreasing Number of LUTs for Moore FSMs. , 2019, , .		1
6	Designing EMB-based Mealy FSM with transformation of collections of output functions. AIP Conference Proceedings, 2015, , .	0.4	1
7	Code sharing in FPGA-based Moore FSMs. AIP Conference Proceedings, 2015, , .	0.4	2
8	Time Dependencies Modelling in Traffic Control Algorithms. Communications in Computer and Information Science, 2013, , 1-6.	0.5	3