# Christof Paar

## List of Publications by Year
in descending order

| 11 papers | 328 citations | 1163117 8 h-index | 1474206 9 g-index |

| 11 all docs | 11 docs citations | 11 times ranked | 241 citing authors |

| # | Article | IF | Citations |
|---|---------|-----|-----------|
| 1 | Physical Design Obfuscation of Hardware: A Comprehensive Investigation of Device and Logic-Level Techniques. IEEE Transactions on Information Forensics and Security, 2017, 12, 64-77. | 6.9 | 76 |
| 2 | FPGA Trojans Through Detecting and Weakening of Cryptographic Primitives. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2015, 34, 1236-1249. | 2.7 | 62 |
| 3 | Hardware reverse engineering: Overview and open challenges. , 2017, , . | | 49 |
| 4 | HALâ€"The Missing Piece of the Puzzle for Hardware Reverse Engineering, Trojan Detection and Insertion. IEEE Transactions on Dependable and Secure Computing, 2019, 16, 498-510. | 5.4 | 45 |
| 5 | Graph Similarity and its Applications to Hardware Security. IEEE Transactions on Computers, 2020, 69, 505-519. | 3.4 | 23 |
| 6 | Side-Channel Hardware Trojan for Provably-Secure SCA-Protected Implementations. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 1435-1448. | 3.1 | 16 |
| 7 | A survey of algorithmic methods in IC reverse engineering. Journal of Cryptographic Engineering, 2021, 11, 299-315. | 1.8 | 16 |
| 8 | The First Thorough Side-Channel Hardware Trojan. Lecture Notes in Computer Science, 2017, , 755-780. | 1.3 | 15 |
| 9 | Security Implications of Intentional Capacitive Crosstalk. IEEE Transactions on Information Forensics and Security, 2019, 14, 3246-3258. | 6.9 | 15 |
| 10 | *ARMORY*: Fully Automated and Exhaustive Fault Simulation on ARM-M Binaries. IEEE Transactions on Information Forensics and Security, 2021, 16, 1058-1073. | 6.9 | 6 |
| 11 | Towards cognitive obfuscation. , 2019, , . | | 5 |