

Abdalhossein Rezai

List of Publications by Year in descending order

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48
papers

950
citations

430874

18
h-index

477307

29
g-index

48
all docs

48
docs citations

48
times ranked

397
citing authors

#	ARTICLE	IF	CITATIONS
1	Novel multilayer SISO shift register architecture in QCA technology and its usage in communications. International Journal of Communication Systems, 2022, 35, .	2.5	5
2	Design of novel multiplexer circuits in QCA nanocomputing. Facta Universitatis - Series Electronics and Energetics, 2021, 34, 105-114.	0.9	9
3	Breast cancer segmentation based on modified Gaussian mean shift algorithm for infrared thermal images. Computer Methods in Biomechanics and Biomedical Engineering: Imaging and Visualization, 2021, 9, 574-580.	1.9	11
4	BREAST CANCER DETECTION USING RSFS-BASED FEATURE SELECTION ALGORITHMS IN THERMAL IMAGES. Biomedical Engineering - Applications, Basis and Communications, 2021, 33, 2150020.	0.6	6
5	Novel Circuits Design for SISO Shift Register in QCA Technology. Journal of Circuits, Systems and Computers, 2021, 30, 2150203.	1.5	13
6	Efficient circuit design for content-addressable memory in quantum-dot cellular automata technology. SN Applied Sciences, 2021, 3, 1.	2.9	8
7	Ultra-Low Power Pulse-Triggered CNTFET-Based Flip-Flop. IEEE Nanotechnology Magazine, 2019, 18, 756-761.	2.0	18
8	Novel design for Memristorâ€based $n \times 1$ multiplexer using new IMPLY logic approach. IET Circuits, Devices and Systems, 2019, 13, 647-655.	1.4	4
9	Design of Novel Coplanar Counter Circuit in Quantum Dot Cellular Automata Technology. International Journal of Theoretical Physics, 2019, 58, 2677-2691.	1.2	25
10	Novel Efficient Circuit Design for Multilayer QCA RCA. International Journal of Theoretical Physics, 2019, 58, 1745-1757.	1.2	47
11	Highâ€performance digital logic implementation approach using novel Memristorâ€based multiplexer. International Journal of Circuit Theory and Applications, 2019, 47, 1933-1947.	2.0	5
12	A Novel Adder Circuit Design in Quantum-Dot Cellular Automata Technology. International Journal of Theoretical Physics, 2019, 58, 184-200.	1.2	34
13	Design of efficient coplanar 1-bit comparator circuit in QCA technology. Facta Universitatis - Series Electronics and Energetics, 2019, 32, 119-128.	0.9	20
14	Novel single layer fault tolerance RCA construction for QCA technology. Facta Universitatis - Series Electronics and Energetics, 2019, 32, 601-613.	0.9	9
15	Design of Low-Complexity and High-Speed Coplanar Four-Bit Ripple Carry Adder in QCA Technology. International Journal of Theoretical Physics, 2018, 57, 1948-1960.	1.2	41
16	A novel design for ultra-low power pulse-triggered D-Flip-Flop with optimized leakage power. The Integration VLSI Journal, 2018, 60, 160-166.	2.1	17
17	An Efficient Architecture for Golay Code Encoder. , 2018, , .		3
18	Novel design for a memristor-based full adder using a new IMPLY logic approach. Journal of Computational Electronics, 2018, 17, 1303-1314.	2.5	27

#	ARTICLE	IF	CITATIONS
19	Novel circuit design of serial-parallel multiplier in quantum-dot cellular automata technology. Journal of Computational Electronics, 2018, 17, 1771-1779.	2.5	38
20	Towards Multilayer QCA SISO Shift Register Based on Efficient D-FF Circuits. International Journal of Theoretical Physics, 2018, 57, 3326-3339.	1.2	37
21	Investigation and Design of Novel Comparator in Quantum-dot Cellular Automata Technology. Journal of Nano- and Electronic Physics, 2018, 10, 05014-1-05014-4.	0.5	13
22	Design of novel efficient full adder architecture for Quantum-dot Cellular Automata technology. Facta Universitatis - Series Electronics and Energetics, 2018, 31, 279-285.	0.9	15
23	A Novel and Fast Hardware Implementation for Golay Code Encoder. Advances in Electrical and Electronic Engineering, 2018, 16, .	0.3	1
24	Compact SD: a new encoding algorithm and its application in multiplication. International Journal of Computer Mathematics, 2017, 94, 554-569.	1.8	17
25	Improved device performance in a CNTFET using La ₂ O ₃ . Journal of Computational Electronics, 2017, 16, 221-227.	2.5	12
26	Towards coplanar quantum-dot cellular automata adders based on efficient three-input XOR gate. Results in Physics, 2017, 7, 1389-1395.	4.1	82
27	Improved Device Performance in CNTFET Using Genetic Algorithm. ECS Journal of Solid State Science and Technology, 2017, 6, M9-M12.	1.8	19
28	A novel design of 5-input majority gate in quantum-dot cellular automata technology. , 2017, , .		17
29	A Design Methodology to Optimize the Device Performance in CNTFET. ECS Journal of Solid State Science and Technology, 2017, 6, M97-M102.	1.8	23
30	Key management issue in SCADA networks: A review. Engineering Science and Technology, an International Journal, 2017, 20, 354-363.	3.2	39
31	A novel approach for improving the modular exponentiation performance. , 2017, , .		0
32	High-performance full adder architecture in quantum-dot cellular automata. Journal of Engineering, 2017, 2017, 394-402.	1.1	38
33	Design of Novel Efficient Multiplexer Architecture for Quantum-dot Cellular Automata. Journal of Nano- and Electronic Physics, 2017, 9, 01012-1-01012-7.	0.5	25
34	High-bandwidth buffer amplifier for liquid crystal display applications. Facta Universitatis - Series Electronics and Energetics, 2017, 30, 549-556.	0.9	0
35	Secure Wireless Body Area Network (WBAN) Communication Method Using New Random Key Management Scheme. International Journal of Security and Its Applications, 2016, 10, 13-22.	0.8	12
36	High-performance multiplexer architecture for quantum-dot cellular automata. Journal of Computational Electronics, 2016, 15, 968-981.	2.5	69

#	ARTICLE	IF	CITATIONS
37	Advance hybrid key management architecture for SCADA network security. Security and Communication Networks, 2016, 9, 4358-4368.	1.5	14
38	High-performance scalable architecture for modular multiplication using a new digit-serial computation. Microelectronics Journal, 2016, 55, 169-178.	2.0	12
39	Design and Evaluation of Novel Effective Montgomery Modular Multiplication Architecture. International Journal of Security and Its Applications, 2016, 10, 261-270.	0.8	2
40	A modified digital to digital encoding method to improve the Wireless Body Area Network (WBAN) transmission. , 2015, , .		1
41	Algorithm design and theoretical analysis of a novel CMM modular exponentiation algorithm for large integers. RAIRO - Theoretical Informatics and Applications, 2015, 49, 255-268.	0.5	4
42	High-Throughput Modular Multiplication and Exponentiation Algorithms Using Multibit-Scanâ€™Multibit-Shift Technique. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 1710-1719.	3.1	50
43	A novel MLP network implementation in CMOL technology. Engineering Science and Technology, an International Journal, 2014, 17, 165-172.	3.2	10
44	A novel digital logic implementation approach on nanocrossbar arrays using memristor-based multiplexers. Microelectronics Journal, 2014, 45, 597-603.	2.0	41
45	A New Left-to-Right Scalar Multiplication Algorithm Using a New Recoding Technique. International Journal of Security and Its Applications, 2014, 8, 31-38.	0.8	1
46	Secure SCADA communication by using a modified key management scheme. ISA Transactions, 2013, 52, 517-524.	5.7	40
47	A New CMM-NAF Modular Exponentiation Algorithm by using a New Modular Multiplication Algorithm. Trends in Applied Sciences Research, 2012, 7, 240-247.	0.4	5
48	High-performance implementation approach of elliptic curve cryptosystem for wireless network applications. , 2011, , .		11