

# Mohamed Atri

## List of Publications by Year in descending order

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157  
papers

2,228  
citations

361045

20  
h-index

288905

40  
g-index

160  
all docs

160  
docs citations

160  
times ranked

1626  
citing authors

#	ARTICLE	IF	CITATIONS
1	Brain tumor classification based on hybrid approach. Visual Computer, 2022, 38, 107-117.	2.5	34
2	Hw/Sw Co-Design technique for 2D fast fourier transform algorithm on Zynq SoC. The Integration VLSI Journal, 2022, 82, 78-88.	1.3	4
3	Indoor objects detection system implementation using multi-graphic processing units. Cluster Computing, 2022, 25, 469-483.	3.5	3
4	Desertification Detection in Makkah Region based on Aerial Images Classification. Computer Systems Science and Engineering, 2022, 40, 607-618.	1.9	1
5	Dynamic programming with adaptive and self-adjusting penalty for real-time accurate stereo matching. Journal of Real-Time Image Processing, 2022, 19, 233-245.	2.2	9
6	Deep embedded hybrid CNN+LSTM network for lane detection on NVIDIA Jetson Xavier NX. Knowledge-Based Systems, 2022, 240, 107941.	4.0	33
7	An efficient object detection system for indoor assistance navigation using deep learning techniques. Multimedia Tools and Applications, 2022, 81, 16601-16618.	2.6	8
8	An evaluation of EfficientDet for object detection used for indoor robots assistance navigation. Journal of Real-Time Image Processing, 2022, 19, 651-661.	2.2	5
9	Glioma segmentation based on deep CNN. , 2022, , .		1
10	Deep Federated Q-Learning-Based Network Slicing for Industrial IoT. IEEE Transactions on Industrial Informatics, 2021, 17, 5572-5582.	7.2	66
11	A Convolutional Neural Network to Perform Object Detection and Identification in Visual Large-Scale Data. Big Data, 2021, 9, 41-52.	2.1	25
12	Deep CNN for Brain Tumor Classification. Neural Processing Letters, 2021, 53, 671-700.	2.0	134
13	Machine Learning Modelling-Powered IoT Systems for Smart Applications. Lecture Notes on Data Engineering and Communications Technologies, 2021, , 185-212.	0.5	1
14	Indoor sign Detection System for Indoor Assistance Navigation. , 2021, , .		1
15	In-Depth Performance Evaluation of Network Slicing Strategies in Large Scale Industry 4.0. , 2021, , .		5
16	Deep learning-based application for indoor wayfinding assistance navigation. Multimedia Tools and Applications, 2021, 80, 27115-27130.	2.6	8
17	A Transfer Learning Approach for Indoor Object Identification. SN Computer Science, 2021, 2, 1.	2.3	4
18	Influence of Joule effect on thermal response of nano FinFET transistors. Superlattices and Microstructures, 2021, 156, 106980.	1.4	3

#	ARTICLE	IF	CITATIONS
19	Accelerated Moving Humans Detection Algorithm using Combined Global Descriptors on GPU Based on CUDA. Smart Sensors, Measurement and Instrumentation, 2021, , 83-96.	0.4	0
20	Traffic Signs Detection for Real-World Application of an Advanced Driving Assisting System Using Deep Learning. Neural Processing Letters, 2020, 51, 837-851.	2.0	64
21	Fast CU partition-based machine learning approach for reducing HEVC complexity. Journal of Real-Time Image Processing, 2020, 17, 185-196.	2.2	29
22	Real-time moving human detection using HOG and Fourier descriptor based on CUDA implementation. Journal of Real-Time Image Processing, 2020, 17, 1841-1856.	2.2	8
23	Statistical 3D watermarking algorithm using non negative matrix factorization. Multimedia Tools and Applications, 2020, 79, 25889-25904.	2.6	3
24	Lightweight Hardware Architectures for the Piccolo Block Cipher in FPGA. , 2020, , .		4
25	A real-time auto calibration technique for stereo camera. International Journal of Computer Aided Engineering and Technology, 2020, 12, 74.	0.1	2
26	A survey on machine learning in Internet of Things: Algorithms, strategies, and applications. Internet of Things (Netherlands), 2020, 12, 100314.	4.9	56
27	Multidatabase ECG signal processing. , 2020, , .		2
28	Recognizing signs and doors for Indoor Wayfinding for Blind and Visually Impaired Persons. , 2020, , .		10
29	Indoor objects detection and recognition for an ICT mobility assistance of visually impaired people. Multimedia Tools and Applications, 2020, 79, 31645-31662.	2.6	44
30	Optical face detection and recognition system on low-end-low-cost Xilinx Zynq SoC. Optik, 2020, 217, 164747.	1.4	5
31	Computer vision algorithms acceleration using graphic processors NVIDIA CUDA. Cluster Computing, 2020, 23, 3335-3347.	3.5	12
32	Deep Learning Based Application for Indoor Scene Recognition. Neural Processing Letters, 2020, 51, 2827-2837.	2.0	41
33	Electrocardiogram heartbeat classification based on a deep convolutional neural network and focal loss. Computers in Biology and Medicine, 2020, 123, 103866.	3.9	80
34	Fast intra-coding unit partition decision in H.266/FVC based on deep learning. Journal of Real-Time Image Processing, 2020, 17, 1971-1981.	2.2	12
35	An Evaluation of RetinaNet on Indoor Object Detection for Blind and Visually Impaired Persons Assistance Navigation. Neural Processing Letters, 2020, 51, 2265-2279.	2.0	71
36	Face Recognition Systems: A Survey. Sensors, 2020, 20, 342.	2.1	279

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37	A Flexible High-Level Fusion for an Accurate Human Action Recognition System. Journal of Circuits, Systems and Computers, 2020, 29, 2050190.	1.0	4
38	Strided Convolution Instead of Max Pooling for Memory Efficiency of Convolutional Neural Networks. Smart Innovation, Systems and Technologies, 2020, , 234-243.	0.5	22
39	Indoor Image Recognition and Classification via Deep Convolutional Neural Network. Smart Innovation, Systems and Technologies, 2020, , 364-371.	0.5	14
40	A Deep CNN-LSTM Framework for Fast Video Coding. Lecture Notes in Computer Science, 2020, , 205-212.	1.0	4
41	A Real-Time Auto Calibration Technique for Stereo Camera. International Journal of Computer Aided Engineering and Technology, 2020, 12, 1.	0.1	0
42	Comparative Study of Face Recognition Approaches. , 2020, , .		0
43	A new deep CNN for brain tumor classification. , 2020, , .		4
44	Fast FPGA Prototyping based Real-Time Image and Video Processing with High-Level Synthesis. International Journal of Advanced Computer Science and Applications, 2020, 11, .	0.5	4
45	Distributed Q-learning based-Decentralized Resource Allocation for Future Wireless Networks. , 2020, , .		1
46	SVM-based Inter Prediction Mode Decision for HEVC. , 2020, , .		1
47	CUDA memory optimisation strategies for motion estimation. IET Computers and Digital Techniques, 2019, 13, 20-27.	0.9	8
48	A Survey of Localization Systems in Internet of Things. Mobile Networks and Applications, 2019, 24, 761-785.	2.2	122
49	An efficient end-to-end deep learning architecture for activity classification. Analog Integrated Circuits and Signal Processing, 2019, 99, 23-32.	0.9	6
50	Indoor Object Classification for Autonomous Navigation Assistance Based on Deep CNN Model. , 2019, , .		11
51	Real-time stereo matching on CUDA using Fourier descriptors and dynamic programming. Computational Visual Media, 2019, 5, 59-71.	10.8	9
52	Implementation of convolutional-LSTM network based on CPU, GPU and pynq-zl board. , 2019, , .		3
53	A hybrid feature extraction approach for brain MRI classification based on Bag-of-words. Biomedical Signal Processing and Control, 2019, 48, 144-152.	3.5	54
54	A new fuzzy logic based node localization mechanism for Wireless Sensor Networks. Future Generation Computer Systems, 2019, 93, 799-813.	4.9	52

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55	An efficient pass-parallel architecture for embedded block coder in JPEG 2000. Journal of Real-Time Image Processing, 2019, 16, 1595-1606.	2.2	14
56	To Perform Road Signs Recognition for Autonomous Vehicles Using Cascaded Deep Learning Pipeline. Artificial Intelligence Advances, 2019, 1, 1-10.	0.2	14
57	A Novel Dataset For Intelligent Indoor Object Detection Systems. Artificial Intelligence Advances, 2019, 1, .	0.2	7
58	Analysis and Classification Techniques of ECG Signals. , 2019, , .		0
59	Parallel Full Search Algorithm for Motion Estimation on Graphic Processing Unit. Recent Advances in Electrical and Electronic Engineering, 2019, 12, 317-323.	0.2	2
60	Localization and Energy-Efficient Data Routing for Unmanned Aerial Vehicles: Fuzzy-Logic-Based Approach. IEEE Communications Magazine, 2018, 56, 129-133.	4.9	58
61	Optimisation of HEVC motion estimation exploiting SAD and SSD GPU-based implementation. IET Image Processing, 2018, 12, 243-253.	1.4	21
62	Real-time motion estimation diamond search algorithm for the new high efficiency video coding on FPGA. Analog Integrated Circuits and Signal Processing, 2018, 94, 259-276.	0.9	14
63	Zynq FPGA Based Memory Efficient and Real-Time Harris Corner Detection Algorithm Implementation. , 2018, , .		3
64	Real Time Stereo Matching Using Two Step Zero-Mean SAD and Dynamic Programing. , 2018, , .		2
65	Fast Moving Human Detection Using Fourier and HOG Descriptors Based CUDA. , 2018, , .		3
66	Efficient implementation of integrall image algorithm on NVIDIA CUDA. , 2018, , .		3
67	A Dynamic Adaptation Mechanism for Traffic Conditions in Wireless Sensor Network. Wireless Personal Communications, 2018, 101, 1967-1982.	1.8	5
68	A novel face detection approach using local binary pattern histogram and support vector machine. , 2018, , .		14
69	Brain MRI classification using discrete wavelet transform and bag-of-words. , 2018, , .		5
70	3D mesh watermarking using salient points. Multimedia Tools and Applications, 2018, 77, 32287-32309.	2.6	19
71	A comparative study of CFs, LBP, HOG, SIFT, SURF, and BRIEF techniques for face recognition. , 2018, , .		6
72	Investigation of Dual-Phase-Lag Model with Robin Boundary Condition in Metal-Oxide-Semiconductor-Field-Effect Transistor. Journal of Computational and Theoretical Nanoscience, 2018, 15, 3114-3117.	0.4	1

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73	Efficient 2D Convolution Filters Implementations on Graphics Processing Unit Using NVIDIA CUDA. International Journal of Image Graphics and Signal Processing, 2018, 10, 1-8.	0.8	3
74	A Novel Real-Time Lighting-Invariant Lane Departure Warning System. Advances in Information Security, Privacy, and Ethics Book Series, 2018, , 231-252.	0.4	0
75	Zynq FPGA based and Optimized Design of Points of Interest Detection and Tracking in Moving Images for Mobility System. International Journal of Advanced Computer Science and Applications, 2018, 9, .	0.5	1
76	Long-term Robust Tracking whith on Failure Recovery. Telkomnika (Telecommunication Computing) Tj ETQq0 0 0 rgBT /Overlck 10 Tf 5	0.6	0
77	High-reliability Vehicle Detection and Lane Collision Warning System. International Journal of Wireless and Microwave Technologies, 2018, 8, 1-14.	0.8	2
78	Comparison of FPGA and GPU Implementations of LPC Algorithm for Voice Processing. Recent Advances in Electrical and Electronic Engineering, 2018, 11, 188-193.	0.2	0
79	Implementation of Robust Tracking Algorithm on Nano-Computer. Indonesian Journal of Electrical Engineering and Computer Science, 2018, 11, 446.	0.7	0
80	A novel mechanism for indoor localization in wireless sensor networks. , 2017, , .		0
81	A Queue Enhanced Backoff Algorithm for wireless sensor networks. , 2017, , .		3
82	A novel illumination-invariant lane detection system. , 2017, , .		22
83	Robust technique for 3D shape reconstruction. Journal of Computational Science, 2017, 21, 333-339.	1.5	5
84	Priority-based queuing and transmission rate management using a fuzzy logic controller in WSNs. ICT Express, 2017, 3, 101-105.	3.3	17
85	Automatic landmark detection and 3D Face data extraction. Journal of Computational Science, 2017, 21, 340-348.	1.5	9
86	Optimization and performance evaluation of graphic processing units for voice processing. Journal of Algorithms and Computational Technology, 2017, 11, 388-394.	0.4	0
87	A Novel Robust Statistical Watermarking of 3D Meshes. Communications in Computer and Information Science, 2017, , 27-38.	0.4	2
88	Image feature extraction algorithm based on CUDA architecture: case study GFD and GCFD. IET Computers and Digital Techniques, 2017, 11, 125-132.	0.9	13
89	Robust auto calibration technique for stereo camera. , 2017, , .		2
90	Channel length effect on heat transfer process in nano MOSFETs. , 2017, , .		1

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91	Execution-time optimization based on thread and block repartitions on a graphic processing unit. , 2017, , .		1
92	Energy-Saving Performance of an Improved DV-Hop Localization Algorithm for Wireless Sensor Networks. , 2017, , .		12
93	New CSMA/CA prioritisation based on fuzzy control mechanism. International Journal of Intelligent Engineering Informatics, 2017, 5, 253.	0.1	4
94	Naive Bayesian Fusion for Action Recognition from Kinect. , 2017, , .		1
95	New CSMA/CA prioritisation based on fuzzy control mechanism. International Journal of Intelligent Engineering Informatics, 2017, 5, 253.	0.1	4
96	FPGA Prototyping and Design Evaluation of a NoC-Based MPSoC. International Journal of Advanced Computer Science and Applications, 2017, 8, .	0.5	1
97	An efficient hardware implementation of parallel EBCOT algorithm for JPEG 2000. Journal of Real-Time Image Processing, 2016, 11, 63-74.	2.2	6
98	Human action recognition using RGB data. , 2016, , .		12
99	Sobel edge detection system design and integration on an FPGA based HD video streaming architecture. , 2016, , .		14
100	A FPGA-based implementation of JPEG encoder. , 2016, , .		3
101	Efficient implementation of a real-time lane departure warning system. , 2016, , .		13
102	Efficient implementation of sobel filter based on GPUs cards. , 2016, , .		2
103	Fast motion estimation for HEVC video coding. , 2016, , .		11
104	Statistical robust watermarking for 3D mesh models based on salient points. , 2016, , .		2
105	Efficient and high-performance pedestrian detector implementation for intelligent vehicles. IET Intelligent Transport Systems, 2016, 10, 438-444.	1.7	6
106	Accelerating Fourier Descriptor for Image Recognition Using GPU. Applied Mathematics and Information Sciences, 2016, 10, 297-306.	0.7	4
107	Analysis of the IEEE 802.15.4 MAC Parameters to Achieve Lower Packet Loss Rates. Procedia Computer Science, 2015, 73, 443-451.	1.2	4
108	Optimized parallel implementation of face detection based on GPU component. Microprocessors and Microsystems, 2015, 39, 393-404.	1.8	18

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109	3D face landmark auto detection. , 2015, , .		4
110	Spatio-Temporal Wavelet Based Video Compression: a Simulink Implementation for Acceleration. International Review on Computers and Software, 2015, 10, 513.	0.1	2
111	Contribution to the realization of a video surveillance system by optical flow and stereovision. , 2014, , .		0
112	Cost/performance evaluation for a 3D symmetric NoC router. , 2014, , .		1
113	MatLab acceleration for DWT &#x201C;Daubechies 9/7&#x201D; for JPEG2000 standard on GPU. , 2014, , .		0
114	Fusion of three techniques for person detection in real time. , 2014, , .		0
115	Hardware implementation of GCFD for color images recognition. , 2014, , .		1
116	A high performance MQ decoder architecture in JPEG2000. , 2014, , .		2
117	Analysis and implementation of parallel causal bit plane coding in JPEG2000 standard. , 2014, , .		0
118	Fast Generalized Fourier Descriptor for object recognition of image using CUDA. , 2014, , .		6
119	Robust spatio-temporal descriptors for real-time SVM-based fall detection. , 2014, , .		4
120	Efficient implementation of Sobel edge detection algorithm on CPU, GPU and FPGA. International Journal of Advanced Media and Communication, 2014, 5, 105.	0.2	10
121	An efficient, high speed architecture for JPEG2000 MQ-coder. , 2014, , .		1
122	Area efficient, high speed VLSI design for BPC coder in JPEG 2000. , 2014, , .		0
123	Pedestrian detection using covariance features. , 2014, , .		1
124	An efficient low-cost FPGA implementation of a configurable motion estimation for H.264 video coding. Journal of Real-Time Image Processing, 2014, 9, 19-30.	2.2	14
125	High-level design for image processing on FPGA using Xilinx AccelDSP. , 2014, , .		2
126	An efficient hardware implementation of MQ decoder of the JPEG2000. Microprocessors and Microsystems, 2014, 38, 659-668.	1.8	4



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127	Efficient smart-camera accelerator: A configurable motion estimator dedicated to video codec. Journal of Systems Architecture, 2013, 59, 870-877.	2.5	2
128	Integral image computation on GPU. , 2013, , .		2
129	Preliminary study of block matching algorithms for wavelet-based t+2D video coding. , 2013, , .		5
130	High-level implementation of Video compression chain coding based on MCTF lifting scheme. , 2013, , .		4
131	Optimized spatio-temporal descriptors for real-time fall detection: comparison of support vector machine and Adaboost-based classification. Journal of Electronic Imaging, 2013, 22, 041106.	0.5	105
132	Evaluation of the performance of block matching algorithms in wavelet-based t+2D video coding. , 2012, , .		0
133	Efficient Hardware/Software Implementation of LPC Algorithm in Speech Coding Applications. Journal of Signal and Information Processing, 2012, 03, 122-129.	0.8	5
134	Definition and Performance Evaluation of a Robust SVM Based Fall Detection Solution. , 2012, , .		85
135	Real time FPGA acceleration for discrete wavelet transform of the 5/3 filter for JPEG 2000. , 2012, , .		6
136	Real time hardware co-simulation of Edge Detection for video processing system. , 2012, , .		16
137	Hardware Implementation of a Configurable Motion Estimator for Adjusting the Video Coding Performances. Lecture Notes in Computer Science, 2012, , 96-107.	1.0	1
138	An efficient hardware implementation of Diamond Search motion estimation using CAL dataflow language. , 2011, , .		2
139	Human detection based on integral Histograms of Oriented Gradients and SVM. , 2011, , .		21
140	Software, hardware and co-simulation for detecting and tracking moving objects. , 2011, , .		0
141	Using Xilinx System Generator for Real Time Hardware Co-simulation of Video Processing System. Lecture Notes in Electrical Engineering, 2010, , 227-236.	0.3	13
142	Generalized Fourier Descriptors with Applications to Objects Recognition inÂSVM Context. Journal of Mathematical Imaging and Vision, 2008, 30, 43-71.	0.8	54
143	On Hardware Implementation of DCT/IDCT for Image Processing. , 2008, , .		0
144	Design of a NoC-router guaranteeing QoS based on Virtual Channels reservation. , 2008, , .		6

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145	Implementation of JPEG 2000 MQ-Coder. , 2008, , .		5
146	Lossless Image Compression Using Burrows Wheeler Transform (Methods and Techniques). , 2008, , .		5
147	A SystemC QoS router design with virtual channels reservation in a wormhole-switched NoC. , 2008, , .		1
148	A Improved Fast Motion Block Matching for Wavelet Video Coding. , 2007, , .		1
149	Rapid Prototyping IP for Autocorrelation Computation. , 2007, , .		0
150	Design of a 2D Mesh-Torus Router for Network on Chip. , 2007, , .		8
151	An FPGA-based accelerator for Fourier Descriptors computing for color object recognition using SVM. Journal of Real-Time Image Processing, 2007, 2, 249-258.	2.2	20
152	G729 Voice Decoder Design. Journal of Signal Processing Systems, 2006, 42, 173-184.	1.0	4
153	Design of New Optimized Architecture Processor for DWT. Real Time Imaging, 2000, 6, 297-312.	1.6	7
154	Design of an adaptive flow control algorithm for ATM networks. , 0, , .		0
155	Fast Motion Estimationâ€™s Configuration Using Diamond Pattern and ECU, CFM, and ESD Modes for Reducing HEVC Computational Complexity. , 0, , .		1
156	Network Slicing for Industrial IoT and Industrial Wireless Sensor Network: Deep Federated Learning Approach and Its Implementation Challenges. , 0, , .		4
157	A novel automatic approach for glioma segmentation. Neural Computing and Applications, 0, , .	3.2	2