

Fadi R Shahroury

List of Publications by Year in descending order

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Version: 2024-02-01

14
papers

49
citations

2258059

3
h-index

1872680

6
g-index

14
all docs

14
docs citations

14
times ranked

35
citing authors

#	ARTICLE	IF	CITATIONS
1	The Design of Low LO-Power 60-GHz CMOS Quadrature-Balanced Self-Switching Current-Mode Mixer. IEEE Microwave and Wireless Components Letters, 2008, 18, 692-694.	3.2	14
2	The design of integrated 3-GHz to 11-GHz CMOS transmitter for full-band ultra-wideband (UWB) applications. , 2008, , .		8
3	A Multi-Output Multi-String High-Efficiency WLED Driver Using 40 nm CMOS Technology. Journal of Low Power Electronics and Applications, 2021, 11, 47.	2.0	5
4	Current-mode design techniques in low-voltage 24-GHz RF CMOS receiver front-end. Analog Integrated Circuits and Signal Processing, 2009, 58, 183-195.	1.4	4
5	A new modulation scheme for low power consumption and small size passive RFID tags. , 2013, , .		3
6	The Design and Optimization of Low-Voltage Pseudo Differential Pair Operational Transconductance Amplifier in 130 nm CMOS Technology. , 2016, , .		3
7	Design of a passive CMOS implantable continuous monitoring biosensors transponder front-end. Microelectronics Journal, 2019, 90, 141-153.	2.0	3
8	Low power passive RFID transponder frontend design for implantable biosensor applications. , 2011, , .		2
9	A low-power and high-data rate passive RFID transceiver using 28-nm CMOS technology. Microelectronics Journal, 2015, 46, 1426-1433.	2.0	2
10	Design of a low-power CMOS transceiver for semi-passive wireless sensor network application. The Integration VLSI Journal, 2020, 71, 95-104.	2.1	2
11	Design Aspects of a Single-Output Multi-String WLED Driver Using 40 nm CMOS Technology. Journal of Low Power Electronics and Applications, 2022, 12, 5.	2.0	2
12	A Low -- Power and High -- Efficiency CMOS Transmitter for Wireless Sensor Network Application. , 2014, , .		1
13	The Design Methodology of Fully Digital Pulse Width Modulation. Journal of Low Power Electronics and Applications, 2021, 11, 41.	2.0	0
14	Design of a High Efficiency WLED Driver in 40 nm CMOS Technology. , 2020, , .		0