

# Gopalakrishnan Sundararajan

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/1609825/publications.pdf>

Version: 2024-02-01

5

papers

91

citations

3311381

1

h-index

3475538

1

g-index

5

all docs

5

docs citations

5

times ranked

76

citing authors

#	ARTICLE	IF	CITATIONS
1	Noisy Gradient Descent Bit-Flip Decoding for LDPC Codes. IEEE Transactions on Communications, 2014, 62, 3385-3400.	7.8	68
2	FreePDK v2.0: Transitioning VLSI education towards nanometer variation-aware designs. , 2009, , .	16	
3	A Winner-Take-All circuit with improved accuracy and tolerance to mismatch and process variations. , 2013, , .	5	
4	CNTFET-RFB: An Error Correction Implementation for Multi-valued CNTFET Logic. , 2016, , .	2	
5	Fault Tolerance in Carbon Nanotube Transistors Based Multi Valued Logic. , 0, , .	0	