

ZhaoXu Xu

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

93
papers

1,052
citations

17
h-index

30
g-index

122
ext. papers

1,465
ext. citations

2.7
avg, IF

4.48
L-index

#	Paper	IF	Citations
93	A 3-GHz Inverse-Coupled Current-Reuse VCO Implemented by 1:1 Transformer. <i>IEEE Microwave and Wireless Components Letters</i> , 2022 , 1-3	2.6	
92	A Ku-Band Eight-Element Phased-Array Transmitter With Built-in Self-Test Capability in 180-nm CMOS Technology. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2022 , 1-12	2.6	
91	Temperature-Dependent Threshold Voltage Extraction of FinFETs Using Noise Measurements. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2022 , 1-1	4.1	
90	A 33 $\bar{4}$ 1-GHz SiGe-BiCMOS Digital Step Attenuator With Minimized Unit Impedance Variation. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2021 , 29, 568-579	2.6	1
89	A SiGe Power Amplifier With Double Gain Peaks Based on the Control of Stationary Points of Impedance Transformation. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2021 , 69, 2279-2290	4.1	1
88	A 60-GHz Variable Gain Phase Shifter With 14.8-dB Gain Tuning Range and 6-Bit Phase Resolution Across 25 \bar{C} 10 \bar{C} . <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2021 , 69, 2371-2385	4.1	8
87	An Improved Large-Signal Equivalent Circuit Model for Partially Depleted Silicon-on-Insulator MOSFET. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2021 , 69, 2972-2980	4.1	1
86	A 21-to-41-GHz High-Gain Low Noise Amplifier With Triple-Coupled Technique for Multiband Wireless Applications. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 1857-1861	3.5	6
85	A Wideband CMOS Frequency Quadrupler With Transformer-Based Tail Feedback Loop. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 1153-1157	3.5	2
84	Analysis and Design of High-Harmonic-Rejection Multi-Ratio mm-Wave Frequency Multipliers. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 1-1	5.5	2
83	An Improved Surface-Potential-Based Model for MOSFETs Considering the Carrier Gaussian Distribution. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2020 , 68, 4082-4090	4.1	1
82	Analysis and design of transformer-based CMOS ultra-wideband millimeter-wave circuits for wireless applications: a review. <i>Frontiers of Information Technology and Electronic Engineering</i> , 2020 , 21, 97-115	2.2	3
81	A K-Band Frequency Tripler Using Transformer-Based Self-Mixing Topology With Peaking Inductor. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2020 , 68, 1688-1696	4.1	5
80	A 37 $\bar{4}$ 0-GHz Low-Phase-Imbalance CMOS Attenuator With Tail-Capacitor Compensation Technique. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 3400-3409	3.9	6
79	A linearized power amplifier with nonlinear feedback architecture. <i>Microwave and Optical Technology Letters</i> , 2020 , 62, 1552-1556	1.2	
78	A Ka-Band CMOS Variable Gain Amplifier with High Gain Resolution and Low Phase Variation 2020 ,		2
77	A 21.7-to-41.7-GHz Injection-Locked LO Generation With a Narrowband Low-Frequency Input for Multiband 5G Communications. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2020 , 68, 170-183	4.1	7

76	A Harmonic-Tuned VCO With an Intrinsic-High-Q F23 Inductor in 65-nm CMOS. <i>IEEE Microwave and Wireless Components Letters</i> , 2020 , 30, 981-984	2.6	0
75	A 68.5~90 GHz High-Gain Power Amplifier With Capacitive Stability Enhancement Technique in 0.13 μm SiGe BiCMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2020 , 1-1	4.1	2
74	A 27.5-43.5 GHz 65-nm CMOS up-conversion mixer with 0.42 dBm OP1dB for 5G applications. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2550	1	
73	A 10-mW 3.9-dB NF transformer-based V-band low-noise amplifier in 65-nm CMOS. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2576	1	2
72	An improved open-short equivalent circuit model for CMOS transistors de-embedding. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2589	1	2
71	Millimeter wave balun design and optimization based on compensation matching capacitors and active S parameter. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2644	1	1
70	An improved wideband equivalent circuit model for integrated spiral inductors in CMOS technology. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2640	1	2
69	A millimeter-wave scalable small signal model of RF CMOS transistor against number of fingers. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2608	1	1
68	A 2.9 GHz CMOS Phase-Locked Loop with Improved Ring Oscillator 2019 ,		1
67	An Injection-Current-Boosting Locking-Range Enhancement Technique for Ultra-Wideband mm-Wave Injection-Locked Frequency Triplers. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2019 , 67, 3174-3186	4.1	21
66	A 6285-GHz High Linearity Upconversion Mixer With 18-GHz IF Bandwidth. <i>IEEE Microwave and Wireless Components Letters</i> , 2019 , 29, 219-221	2.6	4
65	A Scalable Model of On-Chip Inductor Including Tunable Dummy Metal Density Factor. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , 2019 , 9, 296-305	1.7	1
64	Fully Coupled Electrothermal Simulation of Large RRAM Arrays in the Thermal-House. <i>IEEE Access</i> , 2019 , 7, 3897-3908	3.5	5
63	Multimode orbital angular momentum antenna based on four-arm planar spiral. <i>Electronics Letters</i> , 2019 , 55, 875-876	1.1	4
62	An Improved Small Signal Equivalent Circuit Modeling Based On 65nm CMOS Technology 2019 ,		1
61	Differential low-loss T/R switch for phase array application in 0.18- μm CMOS technology. <i>IET Microwaves, Antennas and Propagation</i> , 2019 , 13, 813-818	1.6	1
60	An Improved Surface Potential-Based High-Order Channel Length Modulation Model 2019 ,		3
59	A 15-27 GHz Low Conversion Loss and High Isolation Resistive Ring Mixer for Direct Conversion Receiver 2019 ,		2

58	A High Linearity Low Noise Amplifier for 5G Front-End Modules 2019 ,		1
57	Multi-bias Small Signal Circuit Model for FinFET Transistors 2019 ,		3
56	A 19.5% Efficiency 5173-GHz High-Output Power Frequency Doubler in 65-nm CMOS. <i>IEEE Microwave and Wireless Components Letters</i> , 2019 , 29, 818-821	2.6	8
55	An Improved Ultrawideband Open-Short De-Embedding Method Applied up to 220 GHz. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , 2018 , 8, 269-276	1.7	11
54	Suspended stripline bandpass filter using HTLSIR with controllable Tz. <i>Electronics Letters</i> , 2018 , 54, 29-31	1.1	
53	An Improved RF MOSFET Model Accounting Substrate Coupling Among Terminals. <i>IEEE Microwave and Wireless Components Letters</i> , 2018 , 28, 138-140	2.6	9
52	Analysis and Design of Ultra-Wideband mm-Wave Injection-Locked Frequency Dividers Using Transformer-Based High-Order Resonators. <i>IEEE Journal of Solid-State Circuits</i> , 2018 , 53, 2177-2189	5.5	37
51	A 62-90 GHz High Linearity and Low Noise CMOS Mixer Using Transformer-Coupling Cascode Topology. <i>IEEE Access</i> , 2018 , 6, 19338-19344	3.5	17
50	An Improved Small-Signal Equivalent Circuit Model Considering Channel Current Magnetic Effect. <i>IEEE Microwave and Wireless Components Letters</i> , 2018 , 28, 804-806	2.6	4
49	An asynchronous dual switch envelope tracking supply modulator with 86% efficiency. <i>IEICE Electronics Express</i> , 2018 , 15, 20180206-20180206	0.5	1
48	A 256-QAM 39 GHz Dual-Channel Transceiver Chipset with LTCC Package for 5G Communication in 65 nm CMOS 2018 ,		16
47	66 GHz bias-dependent equivalent circuit model for CMOS transistor based on 90 nanometers CMOS technology. <i>Microwave and Optical Technology Letters</i> , 2018 , 60, 1808-1812	1.2	1
46	A package-level wideband driver amplifier with 134% fractional bandwidth. <i>IEICE Electronics Express</i> , 2018 , 15, 20180179-20180179	0.5	1
45	An Empirical Nonlinear Capacitance Model for SOI Transistor 2018 ,		1
44	RF CMOS Transistor Equivalent Circuit Model up to 66 GHz 2018 ,		3
43	A 700 MHz-920 MHz CMOS Power Amplifier for LTE Applications 2018 ,		1
42	A 5-Gb/s 66 dB CMOS Variable-Gain Amplifier With Reconfigurable DC-Offset Cancellation for Multi-Standard Applications. <i>IEEE Access</i> , 2018 , 6, 54139-54146	3.5	4
41	Compact 2-D Scanning Multibeam Array Utilizing the SIW Three-Way Couplers at 28 GHz. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2018 , 17, 1915-1919	3.8	14

40	A Ku-band Phased Array in Package Integrating Four 180 nm CMOS Transceivers with On-chip Antennas 2018 ,		4
39	A 39 GHz MIMO Transceiver Based on Dynamic Multi-Beam Architecture for 5G Communication with 150 Meter Coverage 2018 ,		10
38	Blind Nonlinear Self-Interference Cancellation for Wireless Full-Duplex Transceivers. <i>IEEE Access</i> , 2018 , 6, 37725-37737	3.5	13
37	Analysis and Design of Inductorless Wideband Low-Noise Amplifier With Noise Cancellation Technique. <i>IEEE Access</i> , 2017 , 5, 9389-9397	3.5	24
36	Compact 4-port MIMO antenna system for 5G mobile terminal 2017 ,		14
35	A 220-GHz Compact Equivalent Circuit Model of CMOS Transistors. <i>IEEE Microwave and Wireless Components Letters</i> , 2017 , 27, 651-653	2.6	9
34	An On-Chip Frequency-Reconfigurable Antenna For Q-Band Broadband Applications. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2017 , 16, 2232-2235	3.8	14
33	A Hybrid Integrated High-Gain Antenna With an On-Chip Radiator Backed by Off-Chip Ground for System-on-Chip Applications. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , 2017 , 7, 114-122	1.7	10
32	An Architecture for Capturing the Nonlinear Distortion of Analog Self-Interference Cancellers in Full-Duplex Radios. <i>IEEE Microwave and Wireless Components Letters</i> , 2017 , 27, 845-847	2.6	3
31	45-GHz and 60-GHz 90 nm CMOS power amplifiers with a fully symmetrical 8-way transformer power combiner. <i>Science China Information Sciences</i> , 2017 , 60, 1	3.4	1
30	A Compact Ka-Band Active Integrated Antenna With a GaAs Amplifier in a Ceramic Package. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2017 , 16, 2416-2419	3.8	11
29	Stereoscopic Image Quality Assessment Based on Depth and Texture Information. <i>IEEE Systems Journal</i> , 2017 , 11, 2829-2838	4.3	7
28	A 24 GHz CMOS mixer using symmetrical design methodology with I/Q imbalance calibration 2017 ,		2
27	A 27.5-33.5 GHz high linearity up-conversion CMOS mixer for 5G communication 2017 ,		4
26	CMOS 90 nm multi-bias transistor model Up to 66 GHz 2017 ,		4
25	A Ku band 4-Element phased array transceiver in 180 nm CMOS 2017 ,		13
24	A CMOS K-Band 6-bit Attenuator With Low Phase Imbalance for Phased Array Applications. <i>IEEE Access</i> , 2017 , 5, 19657-19661	3.5	17
23	A highly-applicable supply modulator with a highly-linear envelope detector for WCDMA envelope-tracking applications 2016 ,		1

22	Eight-Port Orthogonally Dual-Polarized Antenna Array for 5G Smartphone Applications. <i>IEEE Transactions on Antennas and Propagation</i> , 2016 , 64, 3820-3830	4.9	201
21	A suspended stripline bandpass filter using hybrid transmission line stepped impedance resonator. <i>Microwave and Optical Technology Letters</i> , 2016 , 58, 892-895	1.2	0
20	SIW Multibeam Array for 5G Mobile Devices. <i>IEEE Access</i> , 2016 , 4, 2788-2796	3.5	90
19	Characterization of CVD graphene permittivity and conductivity in micro-/millimeter wave frequency range. <i>AIP Advances</i> , 2016 , 6, 095014	1.5	8
18	A V-band inverse class F power amplifier with 16.3% PAE in 65nm CMOS 2016 ,		2
17	A 60-GHz 19.8-mW Current-Reuse Active Phase Shifter With Tunable Current-Splitting Technique in 90-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2016 , 64, 1572-1584	4.1	34
16	Multiple antennas for future 4G/5G smartphone applications 2016 ,		3
15	Complete model for CMOS transistors up to 66GHz 2016 ,		1
14	A Broadband and Equivalent-Circuit Model for Millimeter-Wave On-Chip M:N Six-Port Transformers and Baluns. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2015 , 63, 3109-3121	4.1	22
13	A 60-GHz vector summing phase shifter with digital tunable current-splitting and current-reuse techniques in 90 nm CMOS 2015 ,		1
12	Analysis and Equivalent-Circuit Model for CMOS On-Chip Multiple Coupled Inductors in the Millimeter-Wave Region. <i>IEEE Transactions on Electron Devices</i> , 2015 , 62, 3957-3964	2.9	29
11	A Q-band CMOS LNA exploiting transformer feedback and noise-cancelling. <i>Science China Information Sciences</i> , 2015 , 58, 1-10	3.4	0
10	Decoupled Planar WWAN Antennas With T-Shaped Protruded Ground for Smartphone Applications. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2014 , 13, 483-486	3.8	38
9	Small-Size Multiresonant Octaband Antenna for LTE/WWAN Smartphone Applications. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2014 , 13, 619-622	3.8	28
8	Low-Profile Narrow-Frame Antenna for Seven-Band WWAN/LTE Smartphone Applications. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2014 , 13, 463-466	3.8	32
7	Compact Eight-Band Frequency Reconfigurable Antenna for LTE/WWAN Tablet Computer Applications. <i>IEEE Transactions on Antennas and Propagation</i> , 2014 , 62, 471-475	4.9	64
6	Decoupled Closely Spaced Heptaband Antenna Array for WWAN/LTE Smartphone Applications. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2014 , 13, 31-34	3.8	32
5	3D Image Quality Assessment Based on Texture Information 2014 ,		1

4	Decoupled Hepta-Band Antenna Array for WWAN/LTE Smartphone Applications. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2014 , 13, 999-1002	3.8	49
3	A 780-MHz low power transceiver for wireless nodes applications in Internet of Things 2013 ,		2
2	A magnetically resonant coupling system for wireless power transmission 2012 ,		1
1	Millimeter-Wave Passives in 45-nm Digital CMOS. <i>IEEE Electron Device Letters</i> , 2010 , 31, 1080-1082	4.4	21