

# Bassel Soudan

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/1498909/publications.pdf>

Version: 2024-02-01

42  
papers

1,323  
citations

1039880

9  
h-index

887953

17  
g-index

44  
all docs

44  
docs citations

44  
times ranked

1288  
citing authors

#	ARTICLE	IF	CITATIONS
1	Recent progress in the use of renewable energy sources to power water desalination plants. Desalination, 2018, 435, 97-113.	4.0	433
2	Outlook of carbon capture technology and challenges. Science of the Total Environment, 2019, 657, 56-72.	3.9	281
3	Geothermal based hybrid energy systems, toward eco-friendly energy approaches. Renewable Energy, 2020, 147, 2003-2012.	4.3	142
4	Overview of ocean power technology. Energy, 2019, 175, 165-181.	4.5	118
5	Prospects and challenges of concentrated solar photovoltaics and enhanced geothermal energy technologies. Science of the Total Environment, 2019, 659, 851-861.	3.9	101
6	Early detection and localization of stator inter-turn faults based on discrete wavelet energy ratio and neural networks in induction motor. Energy, 2020, 212, 118684.	4.5	44
7	Community-scale baseload generation from marine energy. Energy, 2019, 189, 116134.	4.5	37
8	Autonomous smart switching control for off-grid hybrid PV/battery/diesel power system. Energy, 2020, 211, 118567.	4.5	24
9	Low-cost dye-sensitized solar cells with ball-milled tellurium-doped graphene as counter electrodes and a natural sensitizer dye. International Journal of Energy Research, 2019, 43, 5824-5833.	2.2	23
10	Bio-Inspired Electronic-Mutation with genetic properties for Secured Identification. , 2007, , .		14
11	VLSI Design Exchange with Intellectual Property Protection in FPGA Environment Using both Secret and Public-Key Cryptography. , 0, , .		13
12	An Evolutionary Dynamic Population Size PSO Implementation. , 2008, , .		10
13	Bulk turbostratic graphene deposition on aluminum substrates via high-pressure graphite blasting. Applied Nanoscience (Switzerland), 2018, 8, 1943-1950.	1.6	10
14	On Some Directions in Security-Oriented Research. , 2007, , .		8
15	Deploying FPGA self-configurable cell structure for micro crypto-functions. , 2009, , .		7
16	Computer-Aided Training for Quranic Recitation. Procedia, Social and Behavioral Sciences, 2015, 192, 778-787.	0.5	7
17	Attempting cardiac arrest prediction using artificial intelligence on vital signs from Electronic Health Records. Smart Health, 2022, 25, 100294.	2.0	7
18	Reducing mutual inductance of wide signal busses through swizzling. , 0, , .		6

#	ARTICLE	IF	CITATIONS
19	MIES: a microarchitecture design tool. , 1989, , .		5
20	Controlling Inductive Coupling in Wide Global Signal Busses Through Swizzling. Analog Integrated Circuits and Signal Processing, 2005, 43, 191-203.	0.9	5
21	Effects of environmental and turbine parameters on energy gains from wind farm system: Artificial neural network simulations. Wind Engineering, 2020, 44, 181-195.	1.1	5
22	Design and implementation of an FTIR camera-based multi-touch display. , 2009, , .		4
23	Controlling on-chip inductive coupling of signal busses through swizzling. , 0, , .		3
24	Fuzzy modular multiplication architecture and low complexity IPR-protection for FPGA technology. , 2006, , .		3
25	Semi-random net reordering for reducing timing variations and improving signal integrity. Microelectronics Journal, 2011, 42, 483-500.	1.1	2
26	The effects of swizzling on inductive and capacitive coupling for wide signal busses. , 2003, , .		1
27	Reducing inductive coupling skew in wide global signal busses. , 2006, , .		1
28	Secure Communication in an Embedded Environment. , 0, , .		1
29	Reconfigurable Cell Architecture for Systolic and Pipelined Computing Datapaths. , 2008, , .		1
30	Reducing signal timing variations in inter-core busses. The Integration VLSI Journal, 2010, 43, 237-249.	1.3	1
31	Filtering technique for high speed database sequence comparison. , 2015, , .		1
32	High Speed Database Sequence Comparison. Procedia Computer Science, 2015, 62, 73-80.	1.2	1
33	A protection mechanism for intellectual property rights (IPR) in FPGA design environment. , 0, , .		0
34	Electronic mutation technology and secured identification. , 2007, , .		0
35	Globally verifiable clone-resistant device identity with mutual authentication. , 2008, , .		0
36	Preventing IP over-deployment in a multiple IP SOC design. , 2008, , .		0

#	ARTICLE	IF	CITATIONS
37	Preventing IP Over-Deployment Through a Public/Secret-key Encryption Mechanism. , 2008, , .		0
38	Reducing inductive coupling variance in wide global signal busses. International Journal of Electronics, 2009, 96, 925-933.	0.9	0
39	Improving timing characteristics through Semi-Random Net Reordering. , 2010, , .		0
40	The effect of SRNR on timing characteristics of signal busses. , 2011, , .		0
41	Computation Time Reduction to Speed-up the Database Searching Process. , 2015, , .		0
42	Modular Hybrid Energy System for Refugee Camps. , 2021, , .		0