

# Kyojin Choo

## List of Publications by Year in descending order

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times ranked

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citing authors

#	ARTICLE	IF	CITATIONS
1	A 510-pW 32-kHz Crystal Oscillator With High Energy-to-Noise-Ratio Pulse Injection. IEEE Journal of Solid-State Circuits, 2022, 57, 434-451.	5.4	4
2	A Delta Sigma-Modulated Sample and Average Common-Mode Feedback Technique for Capacitively Coupled Amplifiers in a 192-nW Acoustic Analog Front-End. IEEE Journal of Solid-State Circuits, 2022, 57, 1138-1152.	5.4	2
3	An Analog-Assisted Digital LDO With Single Subthreshold Output pMOS Achieving 1.44-fs FOM. IEEE Solid-State Circuits Letters, 2021, 4, 154-157.	2.0	4
4	An Ultra-Low-Power Image Signal Processor for Hierarchical Image Recognition With Deep Neural Networks. IEEE Journal of Solid-State Circuits, 2021, 56, 1071-1081.	5.4	7
5	A 192 nW 0.02 Hz High Pass Corner Acoustic Analog Front-End with Automatic Saturation Detection and Recovery. , 2021, , .		2
6	Reference Oversampling PLL Achieving $\hat{\sim}256$ -dB FoM and $\hat{\sim}78$ -dBc Reference Spur. IEEE Journal of Solid-State Circuits, 2021, 56, 2993-3007.	5.4	12
7	Sample and Average Common-Mode Feedback in a 101 nW Acoustic Amplifier. , 2020, , .		3
8	A 67-fs <sub>rms</sub> Jitter, $\hat{\sim}130$ dBc/Hz In-Band Phase Noise, $\hat{\sim}256$ -dB FoM Reference Oversampling Digital PLL With Proportional Path Timing Control. IEEE Solid-State Circuits Letters, 2020, 3, 430-433.	2.0	7
9	An Efficient Piezoelectric Energy Harvesting Interface Circuit Using a Sense-and-Set Rectifier. IEEE Journal of Solid-State Circuits, 2019, 54, 3348-3361.	5.4	27
10	Energy-Efficient Motion-Triggered IoT CMOS Image Sensor With Capacitor Array-Assisted Charge-Injection SAR ADC. IEEE Journal of Solid-State Circuits, 2019, 54, 2921-2931.	5.4	25
11	5.2 Energy-Efficient Low-Noise CMOS Image Sensor with Capacitor Array-Assisted Charge-Injection SAR ADC for Motion-Triggered Low-Power IoT Applications. , 2019, , .		27
12	A Noise Reconfigurable All-Digital Phase-Locked Loop Using a Switched Capacitor-Based Frequency-Locked Loop and a Noise Detector. IEEE Journal of Solid-State Circuits, 2018, 53, 50-65.	5.4	15
13	A Maximum-Likelihood Sequence Detection Powered ADC-Based Serial Link. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 2269-2278.	5.4	9
14	A Noise-Efficient Neural Recording Amplifier Using Discrete-Time Parametric Amplification. IEEE Solid-State Circuits Letters, 2018, 1, 203-206.	2.0	6
15	8.4 A 2.5ps 0.8-to-3.2GHz bang-bang phase- and frequency-detector-based all-digital PLL with noise self-adjustment. , 2017, , .		14
16	22.6 A fully integrated counter-flow energy reservoir for 70%-efficient peak-power delivery in ultra-low-power systems. , 2017, , .		10
17	A start-up boosting circuit with 133Å— speed gain for 2-transistor voltage reference. , 2017, , .		0
18	A Fully Integrated Counter Flow Energy Reservoir for Peak Power Delivery in Small Form-Factor Sensor Systems. IEEE Journal of Solid-State Circuits, 2017, 52, 3155-3167.	5.4	0

#	ARTICLE	IF	CITATIONS
19	A 4.7 $\mu$ W switched-bias MEMS microphone preamplifier for ultra-low-power voice interfaces. , 2017, , .		10
20	27.3 Area-efficient 1GS/s 6b SAR ADC with charge-injection-cell-based DAC. , 2016, , .		51