

# Kamal El-Sankary

## List of Publications by Year in descending order

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Version: 2024-02-01

54  
papers

336  
citations

1163117

8  
h-index

940533

16  
g-index

55  
all docs

55  
docs citations

55  
times ranked

301  
citing authors

| #  | ARTICLE  | IF  | CITATIONS |
|----|--|-----|-----------|
| 1  | High-Speed AES Encryptor With Efficient Merging Techniques. IEEE Embedded Systems Letters, 2010, 2, 67-71.   | 1.9 | 54        |
| 2  | Impact of Approximate Multipliers on VGG Deep Learning Network. IEEE Access, 2018, 6, 60438-60444.   | 4.2 | 45        |
| 3  | Energy-Aware Encryption for Securing Video Transmission in Internet of Multimedia Things. IEEE Transactions on Circuits and Systems for Video Technology, 2019, 29, 610-624.                     | 8.3 | 25        |
| 4  | CNN Inference Using a Preprocessing Precision Controller and Approximate Multipliers With Various Precisions. IEEE Access, 2021, 9, 7220-7232.   | 4.2 | 20        |
| 5  | Distortion Analysis Using Volterra Series and Linearization Technique of Nano-Scale Bulk-Driven CMOS RF Amplifier. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 19-28. | 5.4 | 14        |
| 6  | Preweighted Linearized VCO Analog-to-Digital Converter. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 1983-1987.   | 3.1 | 14        |
| 7  | A Comparative Study on Machine Learning Algorithms for the Control of a Wall Following Robot. , 2019, , .  |     | 13        |
| 8  | Practical Considerations for Accuracy Evaluation in Sensor-Based Machine Learning and Deep Learning. Sensors, 2019, 19, 3491.  | 3.8 | 11        |
| 9  | Time-Varying Respiratory Mechanics as a Novel Mechanism Behind Frequency Dependence of Impedance: A Modeling Approach. IEEE Transactions on Biomedical Engineering, 2019, 66, 2433-2446.         | 4.2 | 9         |
| 10 | Bandgap reference with curvature corrected compensation using subthreshold MOSFETs. , 2009, , .  |     | 8         |
| 11 | Enhanced RF to DC CMOS rectifier with capacitor-bootstrapped transistor. , 2010, , .   |     | 8         |
| 12 | Design of low-voltage wide tuning range CMOS multipass voltage-controlled ring oscillator. , 2010, , .   |     | 6         |
| 13 | Trade-Offs Between Efficiency and Output Voltage of a Single Boost DC-DC Converter for Photo-Voltaic Low Power Harvesting Applications. , 2019, , .  |     | 6         |
| 14 | VCO-Based ADC With Built-In Supply Noise Immunity Using Injection-Locked Ring Oscillators. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1089-1093.                    | 3.0 | 6         |
| 15 | Efficient Implementation of 400 Gbps Optical Communication FEC. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 496-509.  | 5.4 | 6         |
| 16 | Respiratory Bidirectional Ultrasonic TOF Flow Sensor Resilience to Ambient Temperature Fluctuations. IEEE Sensors Journal, 2021, 21, 18920-18931.  | 4.7 | 6         |
| 17 | A high-order curvature compensation technique for bandgap voltage reference using subthreshold MOSFETs. International Journal of Electronics, 2010, 97, 783-796.                                 | 1.4 | 5         |
| 18 | Stochastic ADC with random U-quadratic distributed reference voltages to uniformly distribute comparators trip point. Analog Integrated Circuits and Signal Processing, 2013, 74, 461-465.       | 1.4 | 5         |

| #  | ARTICLE   | IF  | CITATIONS |
|----|---|-----|-----------|
| 19 | Supply-Insensitive Digitally Controlled Delay Lines for 3-D IC Clock Synchronization Architectures. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 1480-1484.        | 3.1 | 5         |
| 20 | Deep Learning Training with Simulated Approximate Multipliers. , 2019, , .  |     | 5         |
| 21 | Area- and Power-Efficient Staircase Encoder Implementation for High-Throughput Fiber-Optical Communications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 843-847. | 3.1 | 5         |
| 22 | An Adaptive ELD Compensation Technique Using a Predictive Comparator. IEEE Transactions on Circuits and Systems II: Express Briefs, 2009, 56, 619-623.  | 3.0 | 4         |
| 23 | Crosstalk Rejection in 3-D-Stacked Interchip Communication With Blind Source Separation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 726-730.                         | 3.0 | 4         |
| 24 | A Low-Power, High-Sensitivity, OOK-Super-Regenerative Receiver for WBANs. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 793-797.  | 3.0 | 4         |
| 25 | Low Power Photo-Voltaic Harvesting Matrix Based Boost DC-DC Converter with Recycled and Synchro-Recycled Scheme. Journal of Low Power Electronics and Applications, 2020, 10, 39.                 | 2.0 | 4         |
| 26 | Dual segmentation approximate multiplier. Electronics Letters, 2021, 57, 718-720.   | 1.0 | 4         |
| 27 | An Efficient PV Battery Charger/Harvester for Low Power Applications, Suitable for Heavily Overcast Operations. IEEE Open Journal of Power Electronics, 2022, 3, 290-302.                         | 5.7 | 4         |
| 28 | A high-output impedance, wide swing bulk-driven current source with dynamic biasing. , 2012, , .  |     | 3         |
| 29 | A PVT-Robust Super-Regenerative Receiver with Background Frequency Calibration and Concurrent Quenching Waveform. Electronics (Switzerland), 2019, 8, 1119.                                       | 3.1 | 3         |
| 30 | A High-Performance OTA with Hybrid of Inverter-Based OTA and Nauta OTA for High Speed Applications. , 2021, , .   |     | 3         |
| 31 | High-voltage DMOS integrated circuits using floating-gate protection technique. Analog Integrated Circuits and Signal Processing, 2010, 62, 223-235.  | 1.4 | 2         |
| 32 | A methodology to design bulk-driven mixer with harmonic mixing rejection. Analog Integrated Circuits and Signal Processing, 2013, 77, 503-511.  | 1.4 | 2         |
| 33 | CMOS Voltage Reference using a Self-Cascode Composite Transistor and a Schottky Diode. Electronics (Switzerland), 2019, 8, 1271.  | 3.1 | 2         |
| 34 | Tracking Respiratory Mechanics With Oscillometry: Introduction of Time-Varying Error. IEEE Sensors Journal, 2019, 19, 311-321.  | 4.7 | 2         |
| 35 | A Type-II Analog PLL with Time-Domain Processing. , 2021, , .   |     | 2         |
| 36 | High Frequency-Low Amplitude Oscillometry: Continuous Unobtrusive Monitoring of Respiratory Function on PAP Machines. IEEE Transactions on Biomedical Engineering, 2022, 69, 2202-2211.           | 4.2 | 2         |

| #  | ARTICLE   | IF  | CITATIONS |
|----|---|-----|-----------|
| 37 | Design of Time-Mode PI Controller for Switched-Capacitor DC/DC Converter Using Differential Evolution Algorithm—A Design Methodology. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 5621-5634. | 2.7 | 2         |
| 38 | Lung Mechanics Tracking With Forced Oscillation Technique (FOT) Based on CMOS Synchronous Demodulation Principle. IEEE Transactions on Biomedical Circuits and Systems, 2023, 17, 1282-1292.  | 4.0 | 2         |
| 39 | Memory Optimized Hardware Implementation of Open FEC Encoder. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2022, 30, 1548-1552.  | 3.1 | 2         |
| 40 | A Distortion-Compensated Charge Transfer Amplifier for a 1.66-MHz Cyclic Pipeline ADC. IEEE Transactions on Circuits and Systems II: Express Briefs, 2010, 57, 507-511.   | 3.0 | 1         |
| 41 | A UHF current-mode continuous-time band-pass delta sigma modulator using fully balanced active inductor. Analog Integrated Circuits and Signal Processing, 2011, 67, 261-272.   | 1.4 | 1         |
| 42 | Wideband complementary metal-oxide-semiconductor double bulk harmonic rejection mixer. IET Circuits, Devices and Systems, 2015, 9, 237-243.   | 1.4 | 1         |
| 43 | Offset-injection digital background calibration for VCO-based ADC. Analog Integrated Circuits and Signal Processing, 2017, 92, 501-506.   | 1.4 | 1         |
| 44 | A linearized wideband low noise amplifier in 65nm CMOS for multi-standard RF communication applications. , 2017, , .  |     | 1         |
| 45 | A Blind Background Calibration Technique for Super-Regenerative Receivers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 344-348.   | 3.0 | 1         |
| 46 | A 0.2 pJ/step open loop VCO-based ADC with inverse $2R$ preweighted linearization. Analog Integrated Circuits and Signal Processing, 2020, 104, 103-108.  | 1.4 | 1         |
| 47 | A PVT Compensated Resistance to Frequency Converter for Sensor Array Read-Out. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 2418-2422.   | 3.0 | 1         |
| 48 | A 14.5-Bit ENOB, 10MS/s SAR-ADC With 2 <sup>nd</sup> Order Hybrid Passive-Active Resonator Noise Shaping. IEEE Access, 2022, 10, 54589-54598.   | 4.2 | 1         |
| 49 | A charge sampling baseband filter using a new high linearity $G_m$ for multimode receiver. , 2009, , .  |     | 0         |
| 50 | Design of Frequency Modulated Fuzzy Logic Controller for Switched Capacitor Converter. , 2018, , .  |     | 0         |
| 51 | A blind digital background calibration for all-digital VCO-based ADC. Analog Integrated Circuits and Signal Processing, 2018, 97, 387-394.  | 1.4 | 0         |
| 52 | Beyond Rail-to-Rail Compliant Current Sources for Mismatch-Insensitive Voltage-to-Time Conversion. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2165-2169.   | 3.1 | 0         |
| 53 | Phase-domain ADC with $\hat{f}$ -modulation frequency tracking loop. Analog Integrated Circuits and Signal Processing, 2020, 104, 183-189.  | 1.4 | 0         |
| 54 | A High Bandwidth-Power Efficiency, Low THD <sub>2,3</sub> Driver Amplifier with Dual-Loop Active Frequency Compensation for High-Speed Applications. Electronics (Switzerland), 2021, 10, 2311.   | 3.1 | 0         |