

Yu-Yun Dai

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Verification and Synthesis of Clock-Gated Circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 366-379.	2.7	0
2	Sequential equivalence checking of clock-gated circuits. , 2015, , .		1