Yu-Yun Dai

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/1465008/publications.pdf

Version: 2024-02-01

2 papers l citations

2 all docs

2 docs citations

times ranked

2

2 citing authors

#	Article	IF	CITATIONS
1	Sequential equivalence checking of clock-gated circuits. , 2015, , .		1
2	Verification and Synthesis of Clock-Gated Circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 366-379.	2.7	0