Olga Krestinskaya

List of Publications by Year
in descending order

49 papers	717 citations	1040056 9 h-index	888059 17 g-index
53 all docs	53 docs citations	53 times ranked	601 citing authors

#	Article	IF	Citations
1	Neuromemristive Circuits for Edge Computing: A Review. IEEE Transactions on Neural Networks and Learning Systems, 2020, 31, 4-23.	11.3	184
2	Learning in Memristive Neural Network Architectures Using Analog Backpropagation Circuits. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 719-732.	5.4	105
3	A memristor-based long short term memory circuit. Analog Integrated Circuits and Signal Processing, 2018, 95, 467-472.	1.4	59
4	Hierarchical Temporal Memory Features with Memristor Logic Circuits for Pattern Recognition. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 1143-1156.	2.7	51
5	Analog Backpropagation Learning Circuits for Memristive Crossbar Neural Networks. , 2018, , .		43
6	Bit-Plane Extracted Moving-Object Detection Using Memristive Crossbar-CAM Arrays for Edge Computing Image Devices. IEEE Access, 2018, 6, 18954-18966.	4.2	32
7	Hierarchical Temporal Memory Using Memristor Networks: A Survey. IEEE Transactions on Emerging Topics in Computational Intelligence, 2018, 2, 380-395.	4.9	28
8	Memristive Non-Idealities: Is there any Practical Implications for Designing Neural Network Chips?. , 2019, , .		21
9	Facial emotion recognition using min-max similarity classifier. , 2017, , .		20
10	On-chip face recognition system design with memristive Hierarchical Temporal Memory. Journal of Intelligent and Fuzzy Systems, 2018, 34, 1393-1402.	1.4	20
11	Feature extraction without learning in an analog spatial pooler memristive-CMOS circuit design of hierarchical temporal memory. Analog Integrated Circuits and Signal Processing, 2018, 95, 457-465.	1.4	20
12	Introduction to Memristive HTM Circuits. , 2018, , .		16
13	Memristor load current mirror circuit. , 2015, , .		13
14	Design of CMOS-memristor Circuits for LSTM architecture. , 2018, , .		13
15	Recursive Threshold Logicâ€"A Bioinspired Reconfigurable Dynamic Logic System With Crossbar Arrays. IEEE Transactions on Biomedical Circuits and Systems, 2020, 14, 1311-1322.	4.0	10
16	Automating Analogue AI Chip Design with Genetic Search. Advanced Intelligent Systems, 2020, 2, 2000075.	6.1	10
17	Variation-aware Binarized Memristive Networks. , 2019, , .		9
18	Who is the Winner? Memristive-CMOS Hybrid Modules: CNN-LSTM Versus HTM. IEEE Transactions on Biomedical Circuits and Systems, 2020, 14, 164-172.	4.0	7

#	Article	IF	Citations
19	Design and implication of a rule based weight sparsity module in HTM spatial pooler. , 2017, , .		6
20	Neuromorphic Adaptive Edge-Preserving Denoising Filter. , 2017, , .		6
21	Wafer Quality Inspection using Memristive LSTM, ANN, DNN and HTM. , 2018, , .		6
22	Memristors: Properties, Models, Materials. Modeling and Optimization in Science and Technologies, 2020, , 13-40.	0.7	6
23	Bioinspired memory model for HTM face recognition. , 2016, , .		3
24	Unified Model for Contrast Enhancement and Denoising. , 2017, , .		3
25	Binarized Neural Network with Stochastic Memristors. , 2019, , .		3
26	HTM Theory. Modeling and Optimization in Science and Technologies, 2020, , 169-180.	0.7	3
27	Programmable Memristive Threshold Logic Gate Array. , 2018, , .		2
28	Notice of Retraction: Variability Analysis of Memristor-based Sigmoid Function. , 2018, , .		2
29	Notice of Retraction: Analysis of Multilayer Perceptron with Rectifier Linear Unit Activation Function. , 2018, , .		2
30	AnalogHTM: Memristive Spatial Pooler Learning with Backpropagation. , 2019, , .		2
31	AMSNet: Analog Memristive System Architecture for Mean-Pooling with Dropout Convolutional Neural Network. , 2019, , .		2
32	Inference Dropouts in Binary Weighted Analog Memristive Crossbar. IEEE Nanotechnology Magazine, 2022, 21, 271-277.	2.0	2
33	Image Based HTM Word Recognizer for Language Processing. , 2018, , .		1
34	Real-Time Analog Pixel-to-Pixel Dynamic Frame Differencing with Memristive Sensing Circuits. , 2018, , .		1
35	Notice of Retraction: New Voltage Mode Sense Amplifier Design with CMOS-Memristive Components. , 2018, , .		1
36	Notice of Retraction: Design And Analysis Of CMOS-Memristive Cascode Current Mirror. , 2018, , .		1

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37	Notice of Retraction: Perceptron Linear Activation Function Design with CMOS-Memristive Circuits. , 2018, , .		1
38	Learning Algorithms and Implementation. Modeling and Optimization in Science and Technologies, 2020, , 91-102.	0.7	1
39	Memristive Deep Convolutional Neural Networks. Modeling and Optimization in Science and Technologies, 2020, , 131-137.	0.7	1
40	Notice of Retraction: Instrumentation Amplifier Design: Comparison of CMOS-Memristive and CMOS Implementations. , 2018, , .		0
41	Notice of Retraction: CMOS-Memristive Sigmoid Activation Function. , 2018, , .		0
42	Notice of Retraction: Analysis of CMOS-Memristive Analog Multiplier Design. , 2018, , .		0
43	Notice of Retraction: Variability Analysis of Cascode Current Mirror Designs with CMOS-memristive Components. , 2018, , .		0
44	CMOS-Memristive Self-Cascode Current Mirror. , 2018, , .		0
45	Effects of Memristors on Fully Differential Transimpedance Amplifier Performance. , 2018, , .		0
46	Notice of Retraction: CMOS-Memristive Analog Multiplier Design. , 2018, , .		0
47	Notice of Retraction: Current Sense Amplifier Design with CMOS-Memristive Circuits. , 2018, , .		0
48	Learning memristive spiking neurons and beyond. , 2021, , 517-528.		0
49	Analog circuit integration of backpropagation learning in memristive HTM architecture. , 2021, , 427-438.		0