

Taesu Kim

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/1431093/publications.pdf>

Version: 2024-02-01

5
papers

162
citations

1684188

5
h-index

2053705

5
g-index

5
all docs

5
docs citations

5
times ranked

347
citing authors

#	ARTICLE	IF	CITATIONS
1	Improved Synapse Device With MLC and Conductance Linearity Using Quantized Conduction for Neuromorphic Systems. IEEE Electron Device Letters, 2018, 39, 312-315.	3.9	60
2	Input Voltage Mapping Optimized for Resistive Memory-Based Deep Neural Network Hardware. IEEE Electron Device Letters, 2017, 38, 1228-1231.	3.9	37
3	Effect of conductance linearity and multi-level cell characteristics of TaO _x -based synapse device on pattern recognition accuracy of neuromorphic system. Nanotechnology, 2018, 29, 115203.	2.6	37
4	Efficient Synapse Memory Structure for Reconfigurable Digital Neuromorphic Hardware. Frontiers in Neuroscience, 2018, 12, 829.	2.8	17
5	BitBlade: Energy-Efficient Variable Bit-Precision Hardware Accelerator for Quantized Neural Networks. IEEE Journal of Solid-State Circuits, 2022, 57, 1924-1935.	5.4	11