

Gracia Nirmala Rani D

List of Publications by Year in descending order

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23
papers

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#	ARTICLE	IF	CITATIONS
1	A C4.5 decision tree classifier based floorplanning algorithm for System-on-Chip design. <i>Microelectronics Journal</i> , 2022, 121, 105361.	2.0	9
2	Design and analysis of 28Å<scp>GHz CMOS</scp> low power <scp>LNA</scp> with 6.4Å<scp>dB</scp> gain variability for <scp>5G</scp> applications. <i>Transactions on Emerging Telecommunications Technologies</i> , 2022, 33, .	3.9	0
3	Optical Grating Techniques for MEMS-Based Spectrometerâ€”A Review. <i>IEEE Sensors Journal</i> , 2021, 21, 5645-5655.	4.7	7
4	Design and analysis of a sleep and wake-up CMOS low noise amplifier for 5G applications. <i>Telecommunication Systems</i> , 2021, 76, 461-470.	2.5	6
5	Design of 5-bit Flash ADC Using 180Ånm Technology for Medical Applications. <i>Wireless Personal Communications</i> , 2021, 120, 1229-1249.	2.7	1
6	Design of Low Noise, Low Power, Bulk-Driven CMOS Based Operational Transconductance Amplifier for Biosensor Applications. <i>Journal of Electrical Engineering and Technology</i> , 2021, 16, 2793.	2.0	1
7	Thermal Aware Floorplanner for Multi-Layer ICs with Fixed-Outline Constraints. , 2021, , .		0
8	Design and analysis of 0.9 and 2.3â€GHz concurrent dualâ€band CMOS LNA for mobile communication. <i>International Journal of Circuit Theory and Applications</i> , 2020, 48, 1-14.	2.0	12
9	A 1-V, 5 Î¼W, Atto Current Bulk-Driven CMOS Based Operational Transconductance Amplifier for Biosensor Applications. <i>ECS Journal of Solid State Science and Technology</i> , 2020, 9, 115003.	1.8	3
10	Reviewâ€”Power Approaches for Biosensors based Bio-Medical Devices. <i>ECS Journal of Solid State Science and Technology</i> , 2020, 9, 121005.	1.8	4
11	Machine Learning Optimization Techniques for 3D IC Physical Design. <i>Advances in Computational Intelligence and Robotics Book Series</i> , 2020, , 47-61.	0.4	1
12	Design and optimisation of feedforward noise cancelling complementary metal oxide semiconductor LNA for 2.4â€GHz WLAN applications. <i>IET Circuits, Devices and Systems</i> , 2019, 13, 908-919.	1.4	10
13	Survey on parameter optimization of mobile communication band low noise amplifier design. <i>International Journal of RF and Microwave Computer-Aided Engineering</i> , 2019, 29, e21720.	1.2	6
14	Design of CMOS Based Biosensor for Implantable Medical Devices. <i>Communications in Computer and Information Science</i> , 2019, , 695-704.	0.5	3
15	Design and Analysis of CMOS Low Power OTA for Biomedical Applications. , 2019, , .		3
16	Design of CMOS based LNA for 5G Wireless Applications. , 2018, , .		7
17	Low power VLSI architecture design of BMC, BPSC and PC schemes. <i>Analog Integrated Circuits and Signal Processing</i> , 2017, 93, 169-178.	1.4	2
18	Design and implementation of Configurable Logic Block of an FPGA using quantum dot cellular automata. , 2016, , .		2

#	ARTICLE	IF	CITATIONS
19	Design of static random access memory using QCA technology. , 2016, , .		3
20	Thermal Aware Floor planning Technique for Nano Circuits. Research Journal of Applied Sciences, Engineering and Technology, 2014, 8, 1279-1284.	0.1	0
21	Low hardware overhead implementation of 3-weight pattern generation technique for VLSI testing. , 2014, , .		1
22	A novel 3D algorithm for VLSI floorplanning. , 2013, , .		0
23	A survey on B*-Tree-based evolutionary algorithms for VLSI floorplanning optimisation. International Journal of Computer Applications in Technology, 2013, 48, 281.	0.5	3