Gracia Nirmala Rani D

List of Publications by Year
in descending order

23 papers	87 citations	1684188 5 h-index	1720034 7 g-index
24 all docs	24 docs citations	24 times ranked	67 citing authors

#	Article	IF	Citations
1	Design and analysis of 0.9 and 2.3‑GHz concurrent dual‑band CMOS LNA for mobile communication. International Journal of Circuit Theory and Applications, 2020, 48, 1-14.	2.0	12
2	Design and optimisation of feedforward noise cancelling complementary metal oxide semiconductor LNA for 2.4‑…GHz WLAN applications. IET Circuits, Devices and Systems, 2019, 13, 908-919.	1.4	10
3	A C4.5 decision tree classifier based floorplanning algorithm for System-on-Chip design. Microelectronics Journal, 2022, 121, 105361.	2.0	9
4	Design of CMOS based LNA for 5G Wireless Applications. , 2018, , .		7
5	Optical Grating Techniques for MEMS-Based Spectrometer—A Review. IEEE Sensors Journal, 2021, 21, 5645-5655.	4.7	7
6	Survey on parameter optimization of mobile communication band low noise amplifier design. International Journal of RF and Microwave Computer-Aided Engineering, 2019, 29, e21720.	1.2	6
7	Design and analysis of a sleep and wake-up CMOS low noise amplifier for 5G applications. Telecommunication Systems, 2021, 76, 461-470.	2.5	6
8	Review—Power Approaches for Biosensors based Bio-Medical Devices. ECS Journal of Solid State Science and Technology, 2020, 9, 121005.	1.8	4
9	A survey on B*-Tree-based evolutionary algorithms for VLSI floorplanning optimisation. International Journal of Computer Applications in Technology, 2013, 48, 281.	0.5	3
10	Design of static random access memory using QCA technology. , 2016, , .		3
11	Design of CMOS Based Biosensor for Implantable Medical Devices. Communications in Computer and Information Science, 2019, , 695-704.	0.5	3
12	Design and Analysis of CMOS Low Power OTA for Biomedical Applications. , 2019, , .		3
13	A 1-V, 5 µW, Atto Current Bulk-Driven CMOS Based Operational Transconductance Amplifier for Biosensor Applications. ECS Journal of Solid State Science and Technology, 2020, 9, 115003.	1.8	3
14	Design and implementation of Configurable Logic Block of an FPGA using quantum dot cellular automata. , 2016, , .		2
15	Low power VLSI architecture design of BMC, BPSC and PC schemes. Analog Integrated Circuits and Signal Processing, 2017, 93, 169-178.	1.4	2
16	Low hardware overhead implementation of 3-weight pattern generation technique for VLSI testing. , 2014, , .		1
17	Design of 5-bit Flash ADC Using 180Ânm Technology for Medical Applications. Wireless Personal Communications, 2021, 120, 1229-1249.	2.7	1
18	Design of Low Noise, Low Power, Bulk-Driven CMOS Based Operational Transconductance Amplifier for Biosensor Applications. Journal of Electrical Engineering and Technology, 2021, 16, 2793.	2.0	1

#	Article	IF	Citations
19	Machine Learning Optimization Techniques for 3D IC Physical Design. Advances in Computational Intelligence and Robotics Book Series, 2020, , 47-61.	0.4	1
20	A novel 3D algorithm for VLSI floorplanning. , 2013, , .		0
21	Thermal Aware Floor planning Technique for Nano Circuits. Research Journal of Applied Sciences, Engineering and Technology, 2014, 8, 1279-1284.	0.1	0
22	Design and analysis of 28 GHz CMOS low power LNA with 6.4 dB gain variability for 5G applications. Transactions on Emerging Telecommunications Technologies, 2022, 33, .	3.9	0
23	Thermal Aware Floorplanner for Multi-Layer ICs with Fixed-Outline Constraints. , 2021, , .		0