Lilian Bossuet

List of Publications by Year
in descending order

34	695	687363 13	552781 26
papers	citations	h-index	g-index

34	34	34	612
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	A PUF Based on a Transient Effect Ring Oscillator and Insensitive to Locking Phenomenon. IEEE Transactions on Emerging Topics in Computing, 2014, 2, 30-36.	4.6	108
2	Implementation and Characterization of a Physical Unclonable Function for IoT: A Case Study With the TERO-PUF. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 97-109.	2.7	91
3	Reconfigurable Hardware for High-Security/ High-Performance Embedded Systems: The SAFES Perspective. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2008, 16, 144-155.	3.1	65
4	Survey of hardware protection of design data for integrated circuits and intellectual properties. IET Computers and Digital Techniques, 2014, 8, 274-287.	1.2	59
5	Automatic low-cost IP watermarking technique based on output mark insertions. Design Automation for Embedded Systems, 2012, 16, 71-92.	1.0	50
6	Design, Evaluation, and Optimization of Physical Unclonable Functions Based on Transient Effect Ring Oscillators. IEEE Transactions on Information Forensics and Security, 2016, 11, 1291-1305.	6.9	48
7	Key Reconciliation Protocols for Error Correction of Silicon PUF Responses. IEEE Transactions on Information Forensics and Security, 2017, 12, 1988-2002.	6.9	31
8	The Security of ARM TrustZone in a FPGA-Based SoC. IEEE Transactions on Computers, 2019, 68, 1238-1248.	3.4	30
9	ELmD: A Pipelineable Authenticated Encryption and Its Hardware Implementation. IEEE Transactions on Computers, 2016, 65, 3318-3331.	3.4	23
10	Timing attack on NoC-based systems: Prime+Probe attack and NoC-based protection. Microprocessors and Microsystems, 2017, 52, 556-565.	2.8	22
11	Fault model of electromagnetic attacks targeting ring oscillator-based true random number generators. Journal of Cryptographic Engineering, 2016, 6, 61-74.	1.8	19
12	Experimental Study of Locking Phenomena on Oscillating Rings Implemented in Logic Devices. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 2560-2571.	5.4	19
13	Message-Recovery Laser Fault Injection Attack on the Classic McEliece Cryptosystem. Lecture Notes in Computer Science, 2021, , 438-467.	1.3	15
14	Electromagnetic security tests for SoC. , 2016, , .		14
15	Fast Digital Post-Processing Technique for Integral Nonlinearity Correction of Analog-to-Digital Converters: Validation on a 12-Bit Folding-and-Interpolating Analog-to-Digital Converter. IEEE Transactions on Instrumentation and Measurement, 2011, 60, 768-775.	4.7	13
16	A Reconfigurable Multi-core Cryptoprocessor for Multi-channel Communication Systems. , 2011, , .		11
17	Areaâ€oriented comparison of lightweight block ciphers implemented in hardware for the activation mechanism in the antiâ€counterfeiting schemes. International Journal of Circuit Theory and Applications, 2017, 45, 274-291.	2.0	9
18	JTAG Fault Injection Attack. IEEE Embedded Systems Letters, 2018, 10, 65-68.	1.9	9

#	Article	IF	Citations
19	Multi-Spot Laser Fault Injection Setup: New Possibilities forÂFault Injection Attacks. Lecture Notes in Computer Science, 2022, , 151-166.	1.3	9
20	High-level fault injection to assess FMEA on critical systems. Microelectronics Reliability, 2021, 122, 114135.	1.7	8
21	From secured logic to IP protection. Microprocessors and Microsystems, 2016, 47, 44-54.	2.8	7
22	Contactless transmission of intellectual property data to protect FPGA designs. , 2015, , .		6
23	Sustainable electronics: On the trail of reconfigurable computing. Sustainable Computing: Informatics and Systems, 2014, 4, 196-202.	2.2	5
24	Disposable configuration of remotely reconfigurable systems. Microprocessors and Microsystems, 2015, 39, 382-392.	2.8	5
25	Comments on â€œA PUF-FSM Binding Scheme for FPGA IP Protection and Pay-per-Device Licensingâ€• IEEE Transactions on Information Forensics and Security, 2016, 11, 2624-2625.	6.9	5
26	Secure Internal Communication of a Trustzone-Enabled Heterogeneous Soc Lightweight Encryption. , 2019, , .		3
27	Virtual Platform to Analyze the Security of a System on Chip at Microarchitectural Level. , 2021, , .		3
28	An Ultra-Lightweight Transmitter for Contactless Rapid Identification of Embedded IP in FPGA. IEEE Embedded Systems Letters, 2015, 7, 97-100.	1.9	2
29	Pipelined Hardware Implementation of COPA, ELmD, and COLM. IEEE Transactions on Computers, 2020, 69, 1533-1543.	3.4	2
30	Performing Cache Timing Attacks from the Reconfigurable Part of a Heterogeneous SoCâ€”An Experimental Study. Applied Sciences (Switzerland), 2021, 11, 6662.	2.5	2
31	Identification of IP control units by state encoding and side channel verification. Microprocessors and Microsystems, 2016, 47, 11-22.	2.8	1
32	Security Assessment of Heterogeneous SoC-FPGA: On the Practicality of Cache Timing Attacks. , 2021, , .		1
33	Physical Security of Ring-based PUF. , 2020, , .		0
34	The use of ellipse-based estimator as a sub-key distinguisher for Side-Channel Analysis. Computers and Electrical Engineering, 2021, 94, 107311.	4.8	0