

# T Nirmalraj

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/140334/publications.pdf>

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7  
papers

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citations

2682572

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2550090

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docs citations

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times ranked

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citing authors

#	ARTICLE	IF	CITATIONS
1	An Efficient Design for Area-Efficient Truncated Adaptive Booth Multiplier for Signal Processing Applications. Journal of Circuits, Systems and Computers, 2021, 30, 2150037.	1.5	2
2	Automatic diagnosis of single fault in interconnect testing of SRAM-based FPGA. IET Computers and Digital Techniques, 2021, 15, 362-371.	1.2	3
3	Development of single channel EEG Acquisition system for BCI applications. Research Journal of Pharmacy and Technology, 2021, , 4705-4709.	0.8	1
4	An enhanced Gate Diffusion Input technique for low power applications. Microelectronics Journal, 2019, 93, 104621.	2.0	3
5	Low latency power aware selfchecking based CSA for sequential multiplier. , 2017, , .		2
6	Design of low power, high speed PLL frequency synthesizer using dynamic CMOS VLSI technology. , 2017, , .		0
7	LOW POWER DUAL STACK IN MTCMOS CIRCUITS USING DYNAMIC LOGIC AND SLEEP TRANSISTOR TECHNIQUES. Far East Journal of Electronics and Communications, 0, , 163-170.	0.2	0