

Ghanshyamsinh Gohil

List of Publications by Year in descending order

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42
papers

1,006
citations

840776

11
h-index

888059

17
g-index

42
all docs

42
docs citations

42
times ranked

728
citing authors

#	ARTICLE	IF	CITATIONS
1	An Approach Towards Extreme Fast Charging Station Power Delivery for Electric Vehicles with Partial Power Processing. IEEE Transactions on Industrial Electronics, 2020, 67, 8076-8087.	7.9	117
2	Line Filter Design of Parallel Interleaved VSCs for High-Power Wind Energy Conversion Systems. IEEE Transactions on Power Electronics, 2015, 30, 6775-6790.	7.9	108
3	Modified Discontinuous PWM for Size Reduction of the Circulating Current Filter in Parallel Interleaved Converters. IEEE Transactions on Power Electronics, 2015, 30, 3457-3470.	7.9	98
4	Design Considerations and Development of an Innovative Gate Driver for Medium-Voltage Power Devices With High $\frac{dv}{dt}$. IEEE Transactions on Power Electronics, 2019, 34, 5256-5267.	7.9	81
5	An Integrated Inductor for Parallel Interleaved VSCs and PWM Schemes for Flux Minimization. IEEE Transactions on Industrial Electronics, 2015, 62, 7534-7546.	7.9	61
6	Review of modular power converters solutions for smart transformer in distribution system. , 2013, , .		55
7	Analysis and modelling of circulating current in two parallel-connected inverters. IET Power Electronics, 2015, 8, 1273-1283.	2.1	55
8	An Integrated Inductor for Parallel Interleaved Three-Phase Voltage Source Converters. IEEE Transactions on Power Electronics, 2016, 31, 3400-3414.	7.9	55
9	Extreme fast charging station architecture for electric vehicles with partial power processing. , 2018, , .		55
10	Magnetic Integration for Parallel Interleaved VSCs Connected in a Whiffletree Configuration. IEEE Transactions on Power Electronics, 2016, 31, 7797-7808.	7.9	25
11	Flux-Balancing Scheme for PD-Modulated Parallel-Interleaved Inverters. IEEE Transactions on Power Electronics, 2017, 32, 3442-3457.	7.9	25
12	Optimized Integrated Harmonic Filter Inductor for Dual-Converter-Fed Open-End Transformer Topology. IEEE Transactions on Power Electronics, 2017, 32, 1818-1831.	7.9	24
13	Isolated Gate Driver for Medium-Voltage SiC Power Devices Using High-Frequency Wireless Power Transfer for a Small Coupling Capacitance. IEEE Transactions on Industrial Electronics, 2021, 68, 10992-11001.	7.9	21
14	Design of the trap filter for the high power converters with parallel interleaved VSCs. , 2014, , .		19
15	Integrated inductor for interleaved operation of two parallel three-phase voltage source converters. , 2015, , .		19
16	Parallel interleaved VSCs: Influence of the PWM scheme on the design of the coupled inductor. , 2014, , .		17
17	Optimal interleaving angle determination in multi paralleled converters considering the DC current ripple and grid Current THD. , 2015, , .		15
18	Design of Isolated Gate Driver Power Supply in Medium Voltage Converters using High Frequency and Compact Wireless Power Transfer. , 2019, , .		15

#	ARTICLE	IF	CITATIONS
19	Analytical method to calculate the DC link current stress in voltage source converters. , 2014, , .		14
20	Reduction of DC-link capacitor in case of cascade multilevel converters by means of reactive power control. , 2014, , .		14
21	Study of Voltage Balancing Techniques for Series-Connected Insulated Gate Power Devices. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2022, 10, 2380-2394.	5.4	11
22	EMI Performance of Active Neutral Point Clamped Phase Leg for Dual Active Bridge DC-DC Converter. IEEE Transactions on Industry Applications, 2021, 57, 6093-6104.	4.9	10
23	Coil-to-Coil Efficiency Optimization of Double-Sided LCC Topology for Electric Vehicle Inductive Chargers. IEEE Transactions on Industrial Electronics, 2022, 69, 11242-11252.	7.9	10
24	Dual Converter Fed Open-End Transformer Topology with Parallel Converters and Integrated Magnetics. IEEE Transactions on Industrial Electronics, 2016, , 1-1.	7.9	9
25	Circulating current controller for parallel interleaved converters using PR controllers. , 2015, , .		8
26	A Gate Driver Design for Medium Voltage Silicon Carbide Power Devices with High dv/dt . , 2018, , .		8
27	Dual-Output Isolated Gate Driver Power Supply for Medium Voltage Converters using High Frequency Wireless Power Transfer. , 2020, , .		8
28	Evaluation of circulating current suppression methods for parallel interleaved inverters. , 2016, , .		6
29	Line reactor for parallel-interleaved high power inverters. , 2017, , .		6
30	Closed Loop dv/dt Control for Equal Voltage Sharing Between Series Connected SiC MOSFETs. , 2019, , .		6
31	Common-mode Current Analysis and Cancellation Technique for Dual Active Bridge Converter based DC System. , 2019, , .		5
32	Common-Mode Current Analysis and Cancellation Technique for Dual Active Bridge Converter Based DC System. IEEE Transactions on Industry Applications, 2022, 58, 4955-4966.	4.9	5
33	Comparative evaluation of modulation schemes for grid-connected parallel interleaved inverters. , 2016, , .		4
34	Benchmarking and Qualification of Gate Drivers for Medium Voltage (MV) Operation using 10 kV Silicon Carbide (SiC) MOSFETs. , 2019, , .		4
35	Performance Evaluation of 10kV SiC-based Extreme Fast Charger for Electric Vehicles with Direct MV AC Grid Interconnection. , 2020, , .		3
36	Control of SiC based front-end rectifier under unbalanced supply voltage. , 2015, , .		2

#	ARTICLE	IF	CITATIONS
37	Comparison between grid side and inverter side current control for parallel interleaved grid connected converters. , 2015, , .		2
38	SiC-Enabled Medium Voltage Isolated DC-DC Converter based Power Optimizer for Large Photovoltaic Parks. , 2020, , .		2
39	Magnetic integration of the harmonic filter inductor for dual-converter fed open-end transformer topology. , 2016, , .		1
40	Mission Profile based Reliability Analysis of a Medium Voltage Power Conversion Architecture for PMSG based Wind Energy Conversion System. , 2018, , .		1
41	Fault Location and Classification for MVDC Networks. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2022, 10, 589-603.	5.4	1
42	Harmonic filter with low coupling capacitance for Medium Voltage, high dv/dt PWM converters. , 2020, , .		1