

# Andrei Pashkovich

## List of Publications by Year in descending order

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2682572  
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#	ARTICLE	IF	CITATIONS
1	Voltage-in-Current Latency Insertion Method for Diodes and MOSFETs. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2020, 10, 1708-1720.	2.5	4
2	Compact DC and Quasi-Static Capacitances Modeling of a-Si:H TFTs, Including Parasitic Capacitances. IEEE Transactions on Electron Devices, 2021, 68, 3384-3389.	3.0	4
3	Analytical I-V and C-V models for symmetric double-gate AOSTFTs. Semiconductor Science and Technology, 0, , .	2.0	0
4	Thin-Film Transistor Simulations With the Voltage-In-Current Latency Insertion Method. IEEE Access, 2021, 9, 159334-159348.	4.2	0