

Young-Kyun Cho

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Slam Control Method for Improving Light Load Efficiency of Boost Converter. IEEE Transactions on Power Electronics, 2022, 37, 42-48.	7.9	0
2	A Low-Power Class-C Voltage-Controlled Oscillator With Robust Start-Up and Compact High-Q Capacitor Array. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 819-823.	3.0	2
3	Regression Model-Based AMS Circuit Optimization Technique Utilizing Parameterized Operating Condition. Electronics (Switzerland), 2022, 11, 408.	3.1	4
4	Sub-Terahertz High-Gain Antenna for Long-Distance Transmission. The Journal of Korean Institute of Electromagnetic Engineering and Science, 2021, 32, 10-18.	0.3	0
5	Single-Stage Wireless Battery Charging Circuit with Coupling Coefficient Prediction. Electronics (Switzerland), 2021, 10, 2615.	3.1	0
6	Noninverting Buck-Boost DC-DC Converter Using a Duobinary-Encoded Single-Bit Delta-Sigma Modulator. IEEE Transactions on Power Electronics, 2020, 35, 484-495.	7.9	16
7	Reconfigurable Beamforming Silicon Plasma Antenna with Vertical PIN Diode Array. Advanced Electronic Materials, 2020, 6, 2000257.	5.1	1
8	Delta-Sigma Modulator-Based Step-Up DC-DC Converter with Dynamic Output Voltage Scaling. Electronics (Switzerland), 2020, 9, 498.	3.1	2
9	A Low Switching Noise and High-Efficiency Buck Converter Using a Continuous-Time Reconfigurable Delta-Sigma Modulator. IEEE Transactions on Power Electronics, 2018, 33, 10501-10511.	7.9	18
10	A Low-Power Continuous-Time Delta-Sigma Modulator Using a Resonant Single Op-Amp Third-Order Loop Filter. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 854-858.	3.0	6
11	Wideband and multiband long-term evolution transmitter using envelope delta-sigma modulation technique. Analog Integrated Circuits and Signal Processing, 2017, 91, 155-162.	1.4	2
12	5.2 mW 61 dB SNDR 15 MHz Bandwidth CT $\Sigma\Delta$ Modulator Using Single Operational Amplifier and Single Feedback DAC. ETRI Journal, 2016, 38, 217.	2.0	2
13	Single op-amp second-order loop filter for continuous-time delta-sigma modulators. Electronics Letters, 2015, 51, 619-621.	1.0	8
14	A 9-bit 80 MS/s Successive Approximation Register Analog-to-Digital Converter With a Capacitor Reduction Technique. IEEE Transactions on Circuits and Systems II: Express Briefs, 2010, 57, 502-506.	3.0	42