

Felipe Machado SÃ¡nchez

List of Publications by Year in descending order

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Version: 2024-02-01

18
papers

108
citations

1937685

4
h-index

1588992

8
g-index

18
all docs

18
docs citations

18
times ranked

124
citing authors

#	ARTICLE	IF	CITATIONS
1	An algorithm selection methodology for automated focusing in optical microscopy. <i>Microscopy Research and Technique</i> , 2022, 85, 1742-1756.	2.2	3
2	Hardware Architectures for Real-Time Medical Imaging. <i>Electronics (Switzerland)</i> , 2021, 10, 3118.	3.1	10
3	Parametric CAD modeling for open source scientific hardware: Comparing OpenSCAD and FreeCAD Python scripts. <i>PLoS ONE</i> , 2019, 14, e0225795.	2.5	22
4	Title is missing!. , 2019, 14, e0225795.		0
5	Title is missing!. , 2019, 14, e0225795.		0
6	Title is missing!. , 2019, 14, e0225795.		0
7	Title is missing!. , 2019, 14, e0225795.		0
8	Objective Assessment of Olfactory Function Using Functional Magnetic Resonance Imaging (fMRI). <i>IEEE Transactions on Instrumentation and Measurement</i> , 2010, 59, 2602-2608.	4.7	16
9	A project-oriented integral curriculum on Electronics for Telecommunication Engineers. , 2010, , .		5
10	Project based learning experience in VHDL digital electronic circuit design. , 2009, , .		14
11	Disjoint Region Partitioning for Probabilistic Switching Activity Estimation at Register Transfer Level. <i>Lecture Notes in Computer Science</i> , 2009, , 399-408.	1.3	3
12	A Binary Decision Diagram Structure for Probabilistic Switching Activity Estimation. <i>Journal of Low Power Electronics</i> , 2008, 4, 247-262.	0.6	1
13	A Reconfigurable, Wearable, Wireless ECG System. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society</i> , 2007, 2007, 1659-62.	0.5	30
14	A Method for Switching Activity Analysis of VHDL-RTL Combinatorial Circuits. <i>Lecture Notes in Computer Science</i> , 2006, , 645-657.	1.3	1
15	Switching activity propagation of VHDL-RTL combinational designs through an automated tool. , 2005, , .		0
16	Ardid: A Tool and a Model for the Quality Analysis of VHDL Based Designs. , 2001, , 65-80.		2
17	A set of hardware components for a reconfigurable control and communications board. , 0, , .		0
18	Exploiting VHDL-RTL features to reduce the complexity of power estimation in combinational circuits. , 0, , .		1