

Eric Chang

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/1279498/publications.pdf>

Version: 2024-02-01

5
papers

102
citations

2258059

3
h-index

2550090

3
g-index

5
all docs

5
docs citations

5
times ranked

56
citing authors

| # | ARTICLE | IF | CITATIONS |
|---|---|-----|-----------|
| 1 | An Eight-Core 1.44-GHz RISC-V Vector Processor in 16-nm FinFET. IEEE Journal of Solid-State Circuits, 2022, 57, 140-152. | 5.4 | 5 |
| 2 | LAYGO: A Template-and-Grid-Based Layout Generation Engine for Advanced CMOS Technologies. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 1012-1022. | 5.4 | 19 |
| 3 | Design and Automatic Generation of High-Speed Circuits for Wireline Communications. , 2019, , . | | 0 |
| 4 | An Automated SerDes Frontend Generator Verified With a 16-nm Instance Achieving 15 Gb/s at 1.96 pJ/bit. IEEE Solid-State Circuits Letters, 2018, 1, 245-248. | 2.0 | 5 |
| 5 | BAG2: A process-portable framework for generator-based AMS circuit design. , 2018, , . | | 73 |